

TECHNICAL MANUAL

TIMEX SINCLAIR
2068
PERSONAL
COLOR COMPUTER



Published by The Time Designs Magazine Co.



TIMEX SINCLAIR 2068
PERSONAL COLOR COMPUTER

TECHNICAL REFERENCE MANUAL

Prepared by

V. C. Corcoran
and
M. H. Branigin

TIMEX COMPUTER CORPORATION
Waterbury CT 06720

© May 1984

Second Edition Printing
Published Exclusively by:
TIME DESIGNS MAGAZINE CO.
COLTON, OREGON 97017

© JANUARY 1986

PREFACE

This manual is dedicated to the many individuals associated with the Timex Computer Corporation in the development and production of the TS2068. Our special thanks to Nan Parsons who prepared the TS2068 Schematic and other drawings used in this manual.

While every effort has been made to make this document complete and accurate, use of the technical information contained herein is at user's sole risk. The Timex Corp. or its affiliates, and Time Designs Magazine Company assume no responsibility or liability for the safety or performance of any product manufactured relying on the technical data contained herein, or any liability, loss, damage, or expense sustained by reason of any claim that such products infringe any patent or other industrial property right.

The Second Edition of this Technical Manual has been re-edited by Tim Woods. Special thanks to Bob Orrfelt and Dave Clifford for technical assistance.

If you would like to receive information on a magazine and other publications for the Timex Sinclair 2068, direct your inquiry to: Time Designs Magazine Company, 29722 Hult Rd., Colton, OR 97017.

Timex Sinclair 2068 Technical Manual (2nd Edition), © Copyright 1986 by the Time Designs Magazine Company. Reproduction of this document in whole or in part by any means without expressed written permission from Time Designs, is prohibited by law.

This manual was printed by Toad's Litho Printing and Composition, Oregon City, OR 97045.

TABLE OF CONTENTS

1.0	INTRODUCTION	1
1.1	TS 2068 Overview	1
	1.1.1	Hardware Overview
	1.1.2	System Software Overview
	1.1.3	Cartridge Software Overview
2.0	HARDWARE GUIDE	7
2.1	Major Hardware Functions	7
	2.1.1	AC Adapter
	2.1.2	Voltage Regulation
	2.1.3	Z80A CPU
	2.1.4	ROM
	2.1.5	32K RAM
	2.1.6	Programmable Sound Generator
	2.1.7	Joystick Port
	2.1.8	Control Logic
	2.1.9	Keyboard
	2.1.10	16K Video Display RAM
	2.1.11	Video Generation
	2.1.12	Cassette I/O
	2.1.13	Port Map
2.2	Schematic	(see inside back cover and Appendix D)
2.3	Unit Absolute Ratings	53
2.4	Interfaces and Connectors	53
	2.4.1	System Bus Connector - P1
	2.4.2	Cartridge Connector - J4
	2.4.3	Cassette I/O
	2.4.4	Joystick
	2.4.5	Composite Monitor Output
	2.4.6	RF Output
	2.4.7	Keyboard Interface Connector - J9
	2.4.8	AC Adapter Power Plug
3.0	SYSTEM SOFTWARE GUIDE	65
3.1	Identifier	65

TABLE OF CONTENTS

(continued)

3.2	ROM Organization and Services	65
3.2.1	Home ROM	
	3.2.1.1	Fixed Entry Points
	3.2.1.2	BASIC AROS Support
	3.2.1.3	General
3.2.2	Extension ROM	
	3.2.2.1	Fixed Entry Points
	3.2.2.2	General
	3.2.2.3	Video Mode Change Service
	3.2.2.4	Extension ROM Interface Routine
3.3	RAM Organization and Services	72
3.3.1	System Variables	
3.3.2	System Configuration Table	
3.3.3	Machine Stack	
3.3.4	OS RAM Routines	
	3.3.4.1	RAM Interruption Handler
	3.3.4.2	RAM Service Routines
	3.3.4.3	Function Dispatcher
4.0	SYSTEM I/O GUIDE	91
4.1	I/O Channels	91
	4.1.1	Keyboard
	4.1.2	Video Screen
	4.1.3	2040 Dot Matrix Printer
4.2	Cassette Tape	102
4.3	Joysticks	104
4.4	Software Generated Sound (BEEP)	105
4.5	Programmable Sound Chip (SOUND)	105
5.0	ADVANCED CONCEPTS	106
5.1	Cartridge Software/Hardware	106
5.2	Advanced Video Modes	117
5.3	Other	125

TABLE OF CONTENTS

(continued)

6.0	KNOWN "BUGS" AND CORRECTIONS	126
6.1	LROS and Machine Code AROS	126
6.2	Machine Code AROS	126
6.3	BASIC AROS	127
6.4	Video Mode Change Service	127
6.5	OS RAM Routines	129
6.6	General	134

APPENDICES

Appendix A	- System ROM Maps/OS RAM Module	136
Appendix B	- System Variables Definition File	150
Appendix C	- Application Development Library	158

C-1	64-Column Mode
C-2	80-Column Mode
C-3	40-Column Mode
C-4	Dual Screen Mode
C-5	Sprites

Appendix D	-	288
------------	---	-----

D-1	TS2068 PCB Assembly Drawing
D-2	TS2068 Parts List
D-3	TS2068 Schematic Diagram

Appendix E	- Expansion Buss Comparisons	295
Appendix F	- Modifications for EPROMs	296

LIST OF FIGURES

<u>FIGURE NO.</u>	<u>TITLE</u>
1.1-1	TS 2068 Block Diagram
1.1-2	Memory Configuration
1.1-3	RAM Mapping
1.1-4	System Initialization Flowchart
2.1.3-1	CPU Timing
2.1.3-2	Op Code Fetch Timing
2.1.3-3	Memory Read/Write Timing
2.1.3-4	I/O Read/Write Timing
2.1.3-5	Interrupt Request/Ack.Cycle
2.1.4-1	Rework for EPROM's
2.1.6-1	PSG Register Block Diagram
2.1.6-2	Tone Period Registers
2.1.6-3	Noise Period Register
2.1.6-4	Mixer Control-I/O Enable Reg.
2.1.6-5	D/A Converter Signal Generation
2.1.6-6	Amplitude Control Registers
2.1.6-7	Variable Amplitude Control
2.1.6-8	Envelope Period Registers
2.1.6-9	Envelope Shape/Cycle Control Reg.
2.1.6-10	Envelope Generator Output
2.1.6-11	Envelope Generator Output Detail
2.1.7-1	Joystick Port Operation
2.1.8-1	Bank Selection Logic
2.1.8-2	Video RAM Address Generation
2.1.9-1	Keyboard Schematic
2.1.10-1	Video RAM Data Organization
2.1.11-1	Composite Video Signal
2.4.1-1	P1 Mating Connector Mechanical Requirements
2.4.1-2	P1 Signal Layout
2.4.1-3	RGB Monitor Connection Schematic
2.4.2-1	J4 Mating Connector Mechanical Requirements
2.4.2-2	J4 Signal Layout
2.4.4-1	Joystick Connector
2.4.8-1	AC Adapter Plug
3.2.2-1	Ext.ROM Interruption Fielder
3.2.2-2	Ext.ROM Interface Routine
4.1.1-1	Keyboard Mode Control
4.1.1-2	Keyboard Support Routines Flowcharts
4.1.2-1	Standard Character Table Locations
4.1.2-2	Screen Row/Column Designations
4.2-1	Tape Header Formats
4.3-1	Joystick Data Format

LIST OF FIGURES
(continued)

<u>FIGURE NO.</u>	<u>TITLE</u>
5.1-1	EPROM Cartridge Board Schematic
5.1-2	Ctdg.Bd.Component Side Artwork
5.1-3	Ctdg.Bd.Solder Side Artwork
5.1-4	EPROM Cartridge Bd. Solder Mask
6.5-1	GET STATUS Corrections
6.5-2	PUT_WORD Corrections
6.5-3	BANK_ENABLE and RESTORE_STATUS Corrections

LIST OF TABLES

<u>TABLE NO.</u>	<u>TITLE</u>
2-1	Z80A Control Signals
2.1.6-1	PSG I/O Enable Truth Table
2.1.6-2	PSG I/O Port Truth Table
2.1.8-1	SCLD I/O Pin Function Definitions
2.1.13-1	I/O Port Map
2.4.1-1	P1 Signal Definitions
2.4.1-2	P1 Signal Electrical Characteristics
2.4.2-1	J4 Signal Definitions
2.4.2-2	J4 Signal Electrical Characteristics
2.4.4-1	Joystick Connector Signal Assignment
3.2.2-1	Inputs to Video Mode Change Service
3.3.4-1	OS RAM Service Routines
3.3.4-2	Function Dispatcher Services

1.0 INTRODUCTION

This manual provides detailed technical information on the Timex Sinclair 2068 Personal Color Computer. In conjunction with the TS2068 User Manual, it is intended to assist the reader in understanding the architecture, hardware and software features, programming techniques and I/O techniques pertaining to the TS2068.

1.1 TS2068 Overview

1.1.1 Hardware Overview

Figure 1.1-1 is a block diagram of the TS2068 showing the major functional components and their logical connections. These components are:

Control Logic - SCLD (Standard Cell Logic Device)
CPU - Z80A Microprocessor
RAM - 48K Random Access Memory
ROM - 24K System Read-Only Memory
(16K plus 8K Extension)

System Bus Connector
Cartridge Connector
Sound Generator/Speaker
Video Circuits
Cassette READ/WRITE
Joystick Connectors

The TS2068 Cartridge Connector provides for the plug-in of cartridges containing programmed ROM's with up to 64K of addressable memory. The full 64K is not normally utilized (e.g., due to need for access to RAM for the machine stack). See Section 5.1 for details.

Figure 1.1-2 shows the standard TS2068 memory configuration comprised of the Home Bank, the ROM Extension Bank and the Dock (Cartridge) Bank. This memory is selectable as eight 8K 'chunks' with the Home Bank being enabled by default, i.e., any chunk not selected in the Extension or Dock Bank is automatically enabled in the Home Bank.

Memory selection and I/O are controlled via the I/O ports. These topics are covered in detail in later sections.

FIGURE 1.1-1

TS 2068 SYSTEM BLOCK DIAGRAM

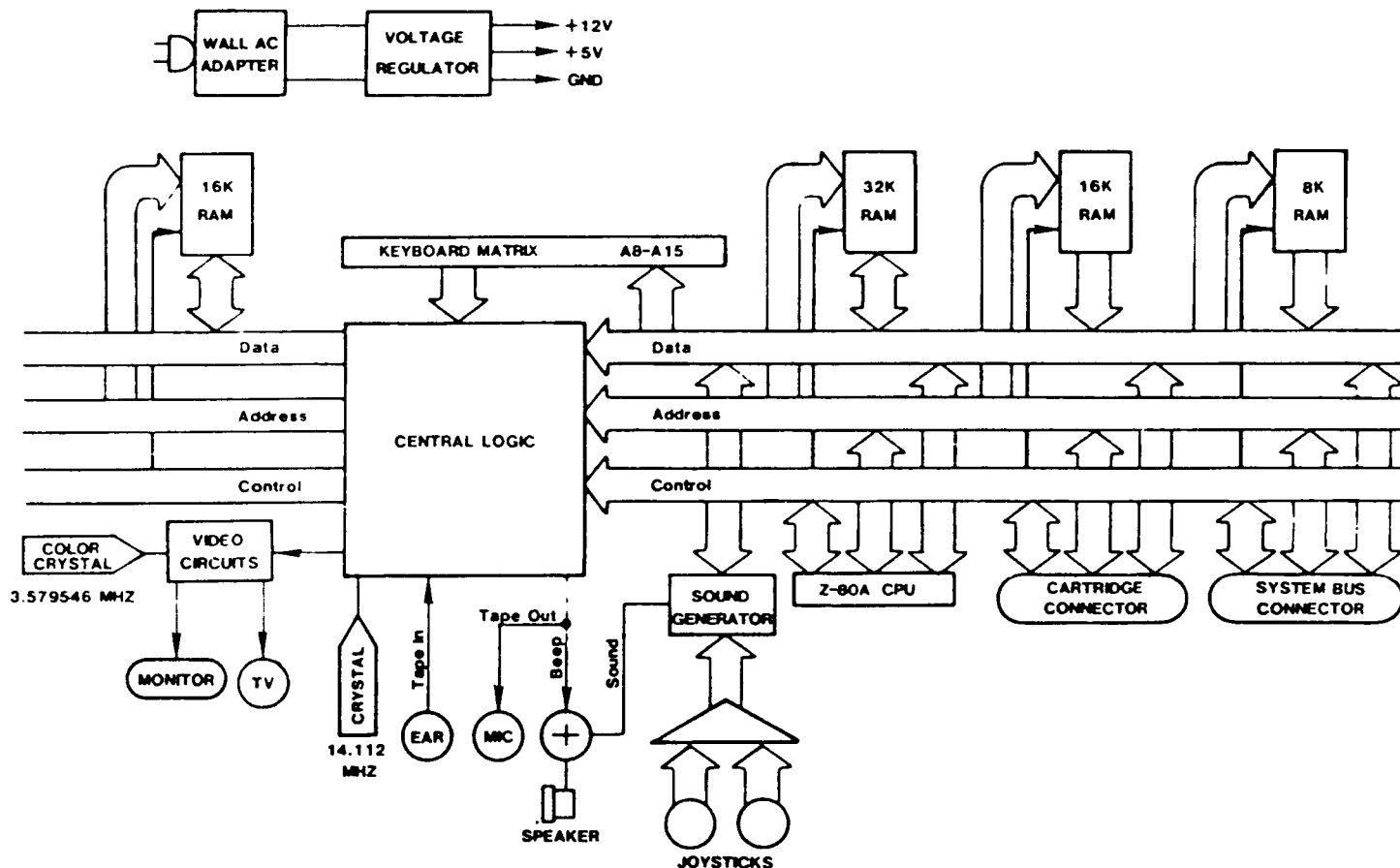
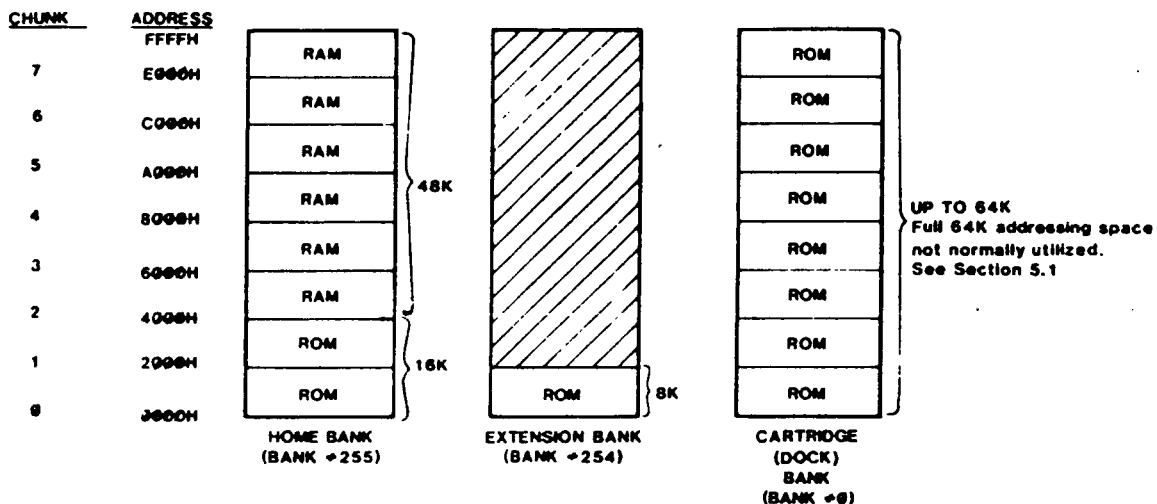


FIGURE 1.1-2

TS 2068 STANDARD MEMORY CONFIGURATION



1.1.2 System Software Overview

The TS2068 System Software resides in the Home ROM, the Extension ROM, and dedicated RAM. It supports the following functions:

- System Initialization
- BASIC Interpreter (including BASIC cartridge support)
- BASIC I/O for Standard Peripherals
 - o keyboard
 - o video screen
 - o 2040 32-col. dot matrix printer
 - o cassette tape
 - o joysticks
 - o software generated sound (BEEP)
 - o programmable sound chip (SOUND)
- Video Mode Change Service
- Interruption Servicing (Z80 Int. Mode 1)
- Bank Switching/Data Transfer Services
- Function Dispatcher (provides access to selected system routines via a Service Code input)

In addition, portions of the Home Bank RAM are used for the machine stack, the BASIC system variables, the Printer Buffer and the Display Files. Figure 1.1-3 shows the standard mapping of the Home Bank RAM and the mapping necessary when the second display file is to be used with the BASIC interpreter still functional. The Video Mode Change Service routine makes these memory modifications. Note that there is no direct support of the second display file via BASIC or in the system ROM I/O routines.

Figure 1.1-4 is a Flowchart of the System Initialization process.

FIGURE 1.1-3
 STANDARD MAPPING OF
 HOME BANK RAM

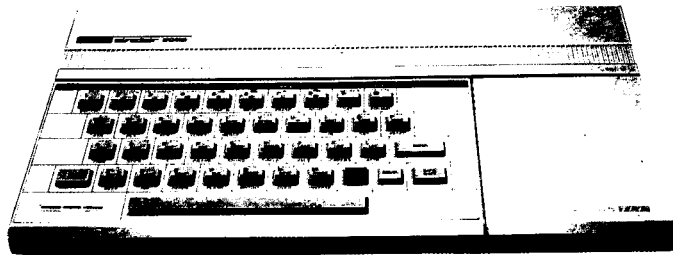
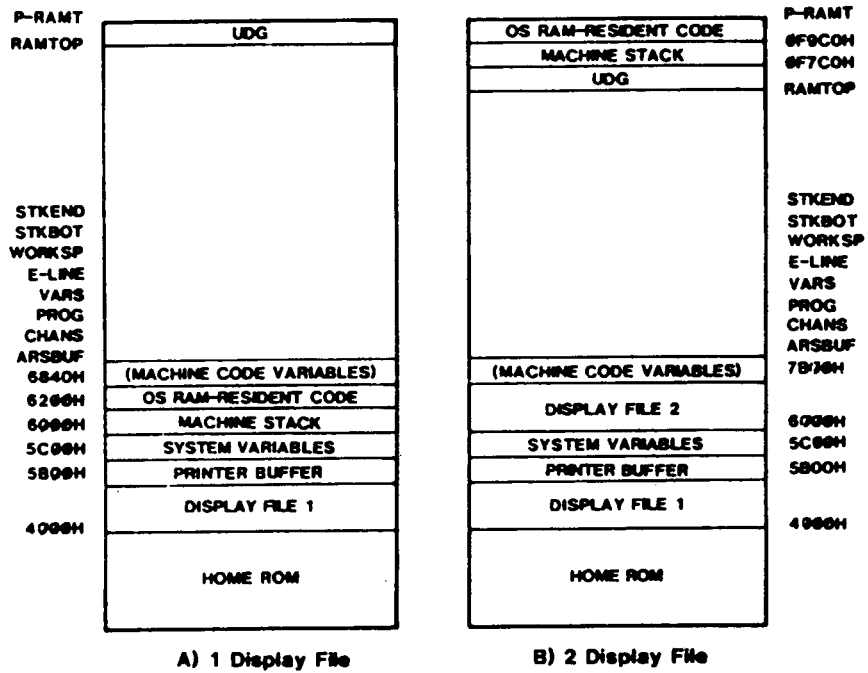
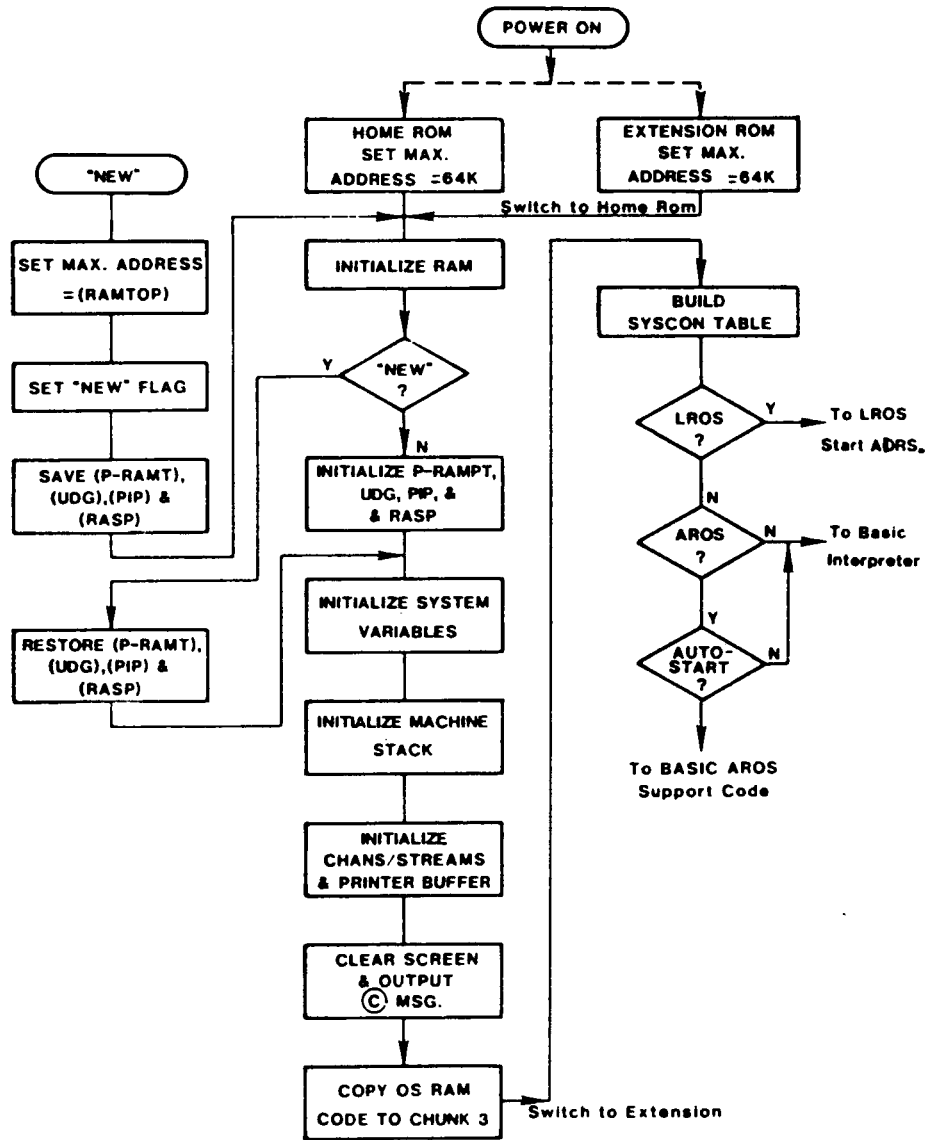


FIGURE 1.1-4
SYSTEM INITIALIZATION



1.1.3 Cartridge Software Overview

The TS2068 supports two basic types of Cartridge or ROM-Oriented Software designated as LROS (Language ROM-Oriented Software) and AROS (Application ROM-Oriented Software) which plug into the cartridge connector. They are identified via overhead bytes at Location 0 for an LROS or 32768 (8000H) for an AROS. The fundamental difference is that an LROS contains Z80 machine code in memory chunk 0 and is in total control of the TS2068 hardware including the RESTART implementation and Interruption Mode setting and handling, while an AROS is dependent on the System ROM or an LROS for these functions if needed. An AROS written in BASIC, which may also include machine code accessed via the USR function, is supported from the System ROM BASIC Interpreter and is mapped beginning in memory chunk 4. An AROS may also be written entirely in Z80 machine code. An AROS written in any other high-level language would require an LROS supporting that language and would have to be integrated with the LROS in a single cartridge.

See Sections 3.2.1.2, BASIC AROS Support and 5.1, Cartridge Software/Hardware, for additional details.

2.0 HARDWARE GUIDE

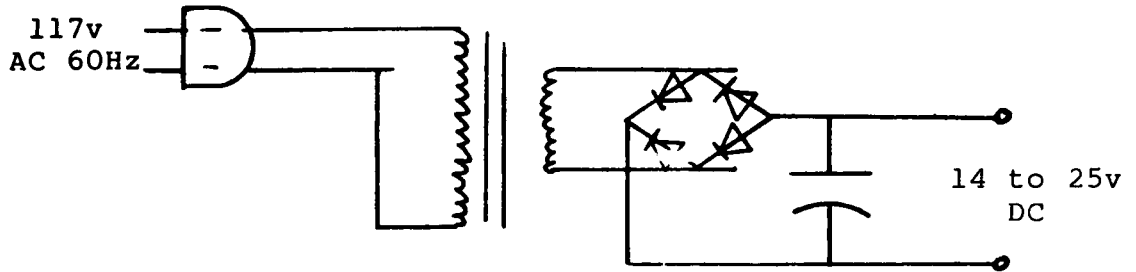
2.1 Description of Major Hardware Functions

Figure 1.1-1 shows a simplified block diagram of the TS2068. The following functional units are described in the following sections:

SECTION	FUNCTIONAL UNIT
2.1.1	AC Adapter
2.1.2	Voltage Regulation
2.1.3	Z-80A CPU
2.1.3.1	Address Bus
2.1.3.2	Data Bus
2.1.3.3	Control Signals
2.1.3.4	OP Code Fetch
2.1.3.5	Memory READ/WRITE
2.1.3.6	I/O READ/WRITE
2.1.3.7	Maskable Interruption
2.1.3.8	Non-Maskable Interruption (NMI)
2.1.4	ROM
2.1.5	32K RAM
2.1.6	Sound Generator
2.1.7	Joystick Port
2.1.8	Control Logic
2.1.8.1	Bank Selection Logic
2.1.8.2	Z80 Clock Generator
2.1.8.3	Display File Access
2.1.8.4	Interruption Generation
2.1.9	Keyboard
2.1.10	16K Video Display RAM
2.1.11	Video Generation
2.1.11.1	Composite Video
2.1.11.2	RF Modulator
2.1.12	Cassette I/O
2.1.13	Port Map

2.1.1 AC Adapter

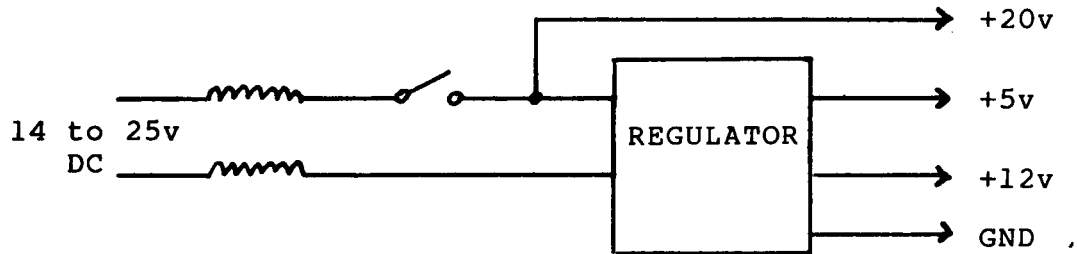
The AC Adapter transforms 117V AC (Nominal) to filtered DC via a step down transformer, full-wave bridge rectifier, and filter capacitor to supply from 14 to 25 volts at 1 amp over the AC voltage variation range of 105 to 130 V AC. Transformer isolation exceeds 1500 volts.



2.1.2 Voltage Regulation

Unregulated DC from the AC Adapter is supplied for regulation through a bi-filar torroidal inductor which reduces conducted line emanation for FCC compliance and through the power-ON/OFF switch located on the left side of the TS2068. This switch voltage is supplied to the System Bus Connector (see Section 2.4) and for regulation to the +12 V regulator and the +5 V regulator. Characteristics are as follows:

SUPPLY	VOLTAGE RANGE	CURRENT RANGE
5V	4.75 - 5.25V	200ma - 1.0 A
12V	11.5 - 12.5V	20ma - 100ma



The 12V regulator is a 78L12 series regulator while the 5V regulator is a switching supply utilizing the 78S40 circuit.

2.1.3 Z-80A CPU

The Z-80A CPU of the TS2068 operates at a clock frequency of 3.53 MHz. Primary features of this CPU are:

- 158 instructions
- Dual register set
- Two index registers
- On-chip refresh logic

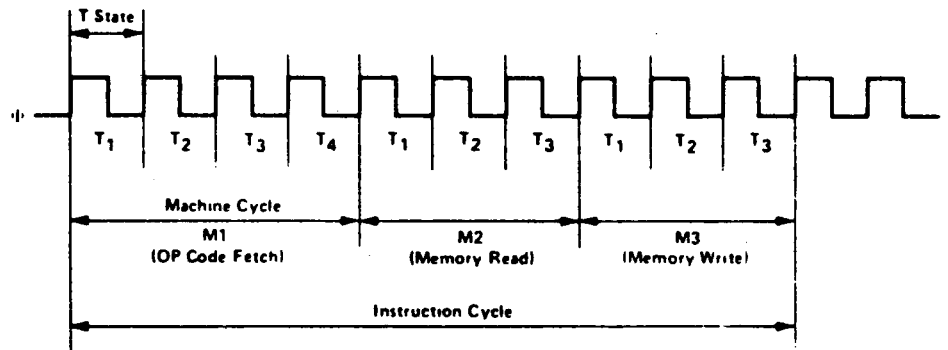
The Z-80 CPU executes instructions by proceeding through a sequence of operations that include:

- a) instruction Op code fetching
- b) READ or WRITE memory
- c) READ or WRITE I/O
- d) Acknowledge an interruption

The basic clock period is referred to as a T time or state and three or more T states make up a machine cycle. In the TS2068, each T-time is approximately 283 nanoseconds (2.83×10^{-7} seconds). Figure 2.1.3-1 illustrates the basic timing.

FIGURE 2.1.3-1

BASIC CPU TIMING EXAMPLE



2.1.3.1 Address Bus

Output from the Z-80 are 16-bits of address information, A0 - A15, which are high-active tri-state signals and address for memory data and I/O device exchanges.

2.1.3.2 Data Bus

These input/output signals from the Z-80, D0 - D7, constitute an 8-bit bi-directional, high-active, tri-state data bus used for data exchanges with memory and I/O devices.

2.1.3.3 Control Bus

Associated with the Z-80 are 13 control lines which are provided by or used by the Z-80 to control system operation. These signals are detailed in Table 2-1.

2.1.3.4 Op Code Fetch

The timing during an M1 cycle (Op Code Fetch) is shown in Figure 2.1.3-2. At the beginning of the M1 cycle the PC (Program Counter) is placed onto the address bus, then one-half clock time later the $\overline{\text{MREQ}}$ signal goes active indicating that the memory address is stable. The $\overline{\text{RD}}$ signal is activated to indicate that memory read data should be gated onto the data bus. At the rising clock edge during the T3 state, the CPU samples the data on the data bus and deactivates the $\overline{\text{RD}}$ and $\overline{\text{MREQ}}$ signals. During the T3 and T4 states, the CPU decodes and executes the fetched instruction and the CPU places on the lower 7 bits of the address bus a memory refresh address and activates the $\overline{\text{RFSH}}$ signal indicating a refresh read is to begin when $\overline{\text{MREQ}}$ is activated.

2.1.3.5 Memory READ/WRITE

Memory read or write cycles other than Op Code Fetches are 3 clock periods long with the $\overline{\text{MREQ}}$ and $\overline{\text{RD}}$ signals used as in the fetch cycle. During a write cycle the $\overline{\text{WR}}$ signal is activated when the write data is stable on the data bus. The address and data bus contents remain stable for one-half T state after the $\overline{\text{WR}}$ signal goes active. Figure 2.1.3-3 illustrates.

FIGURE 2.1.3-2

INSTRUCTION OP CODE FETCH

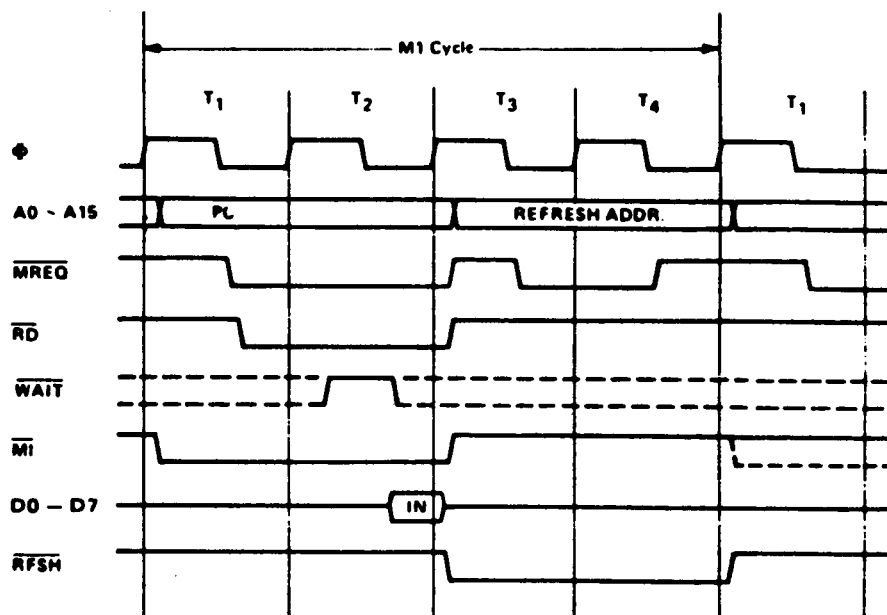
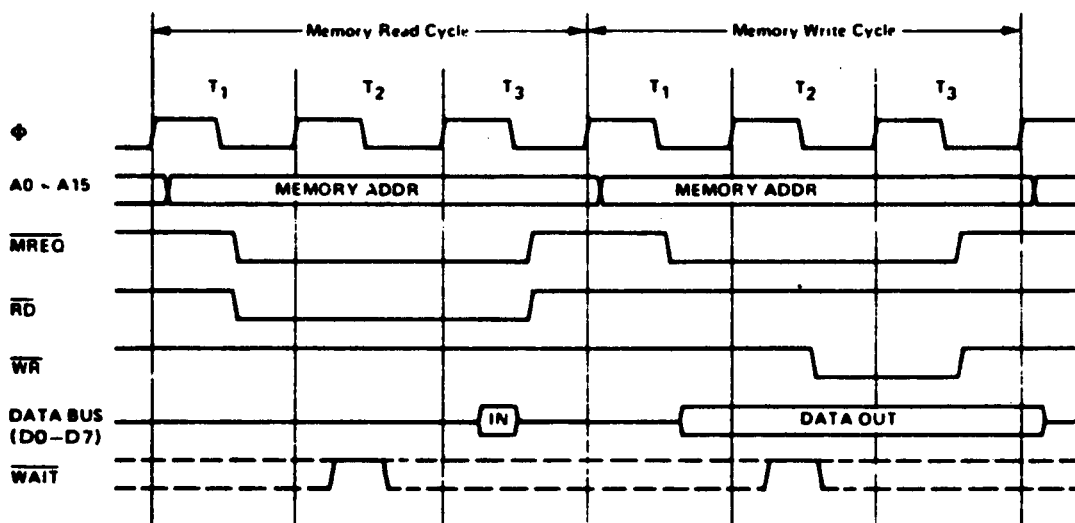


FIGURE 2.1.3-3

MEMORY READ OR WRITE CYCLES

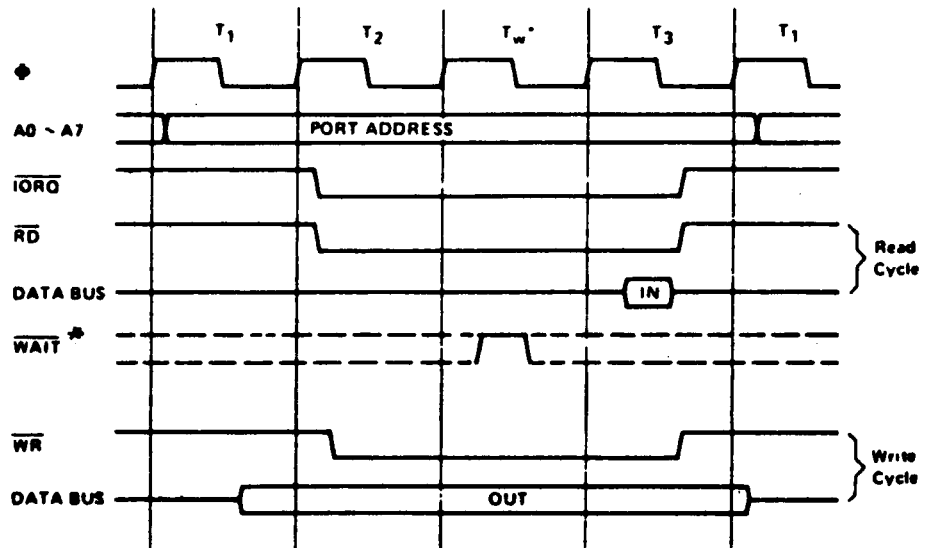


2.1.3.6 I/O READ/WRITE

During I/O operations \overline{IORQ} and \overline{RD} or \overline{WR} are activated on the leading edge of the T2 clock and a single \overline{Wait} state is automatically inserted as illustrated in Figure 2.1.3-4. The \overline{RD} and \overline{WR} signals are used to enable data from the addressed port onto the data bus and to, on the rising edge of \overline{WR} , clock data to the I/O port, respectively. Note that external I/O may stretch the activation period of the \overline{WAIT} line to extend the I/O cycles.

FIGURE 2.1.3-4

INPUT OR OUTPUT CYCLES



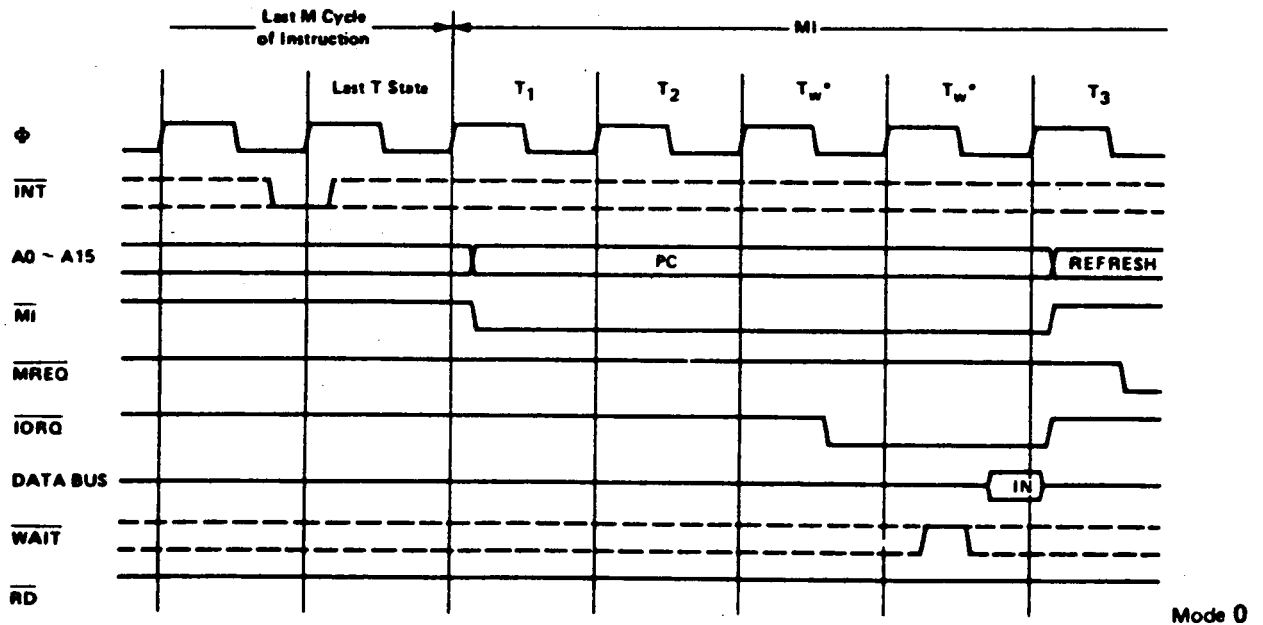
*Inserted by Z80 CPU

2.1.3.7 Maskable Interruption

When enabled by software, when $\overline{\text{BUSRQ}}$ is not active and when INT is active at the rising edge of the last clock of any instruction, a maskable interruption occurs during the subsequent M1 cycle, as illustrated in Figure 2.1.3-5.

FIGURE 2.1.3-5

INTERRUPT REQUEST/ACKNOWLEDGE CYCLE



In Interruption Mode 0, the interrupting I/O device places any instruction on the data bus during the IORQ activation and the CPU executes that instruction. The RESTART instruction is commonly used for this purpose. RESET will automatically set Interruption Mode 0.

In Interruption Mode 1, the CPU executes a RESTART to Location 0038H. This is the mode normally used by the TS 2068 software.

In Interruption Mode 2, the CPU concatenates the 8-bit argument, which must be a 2-byte boundary address, with the 8-bit I Register contents to form a 16-bit pointer to a memory table entry containing the 16-bit service routine address - the first byte in the table

being the low order portion of the address. Once the interrupting device supplies the lower portion of the pointer (for concatenation), the CPU automatically pushes the PC onto the stack, obtains the starting address from the table, and does a jump to that address. 19 clock periods are required to complete this sequence.

2.1.3.8 Non-Maskable Interruption (NMI)

A pulse on the ~~NMI~~ input to the Z80 sets the internal latch which is tested by the CPU at the end of each instruction. The NMI has priority over the maskable interruption and its response is identical to the maskable interruption (Mode 1) except that the call location is 0066H instead of 0038H.

- NOTES: 1. The NMI is not used by the TS 2068.
2. Comments in the ROM listing claiming to "mask the NMI" via the DI instruction are incorrect. The DI instruction masks only the maskable interruption.

TABLE 2-1

Z-80 CONTROL SIGNALS

	<u>ACRONYM</u>	<u>DEFINITION</u>
SYSTEM CONTROL	<u>MT</u>	<u>Machine Cycle 1</u> - Output, active low. This signal indicates that the current machine cycle is the OP code fetch cycle. During execution of instructions having a 2-byte OP code, this signal is generated as each OP code byte is fetched. <u>MT</u> is also used with <u>IORQ</u> to indicate an interrupt acknowledge cycle.
	<u>MREQ</u>	<u>Memory Request</u> - Tri-state output, active low. This signal indicates that the Address Bus holds a valid address for a memory read or write operation.
	<u>IORQ</u>	<u>I/O Request</u> - Tri-state output, active low. This signal indicates that the lower half of the Address Bus holds a valid I/O address for an I/O read or write operation. This signal is also used with <u>MT</u> in connection with acknowledging an interruption, indicating that an interrupt response vector can be placed on the data bus. I/O operations never occur during <u>MT</u> time.
	<u>RD</u>	<u>Memory Read</u> - Tri-state output, active low. This signal indicates that the CPU wants to read data from memory or an I/O device. The addressed memory or device should use this signal to gate the requested data onto the CPU data bus.
	<u>WR</u>	<u>Memory Write</u> - Tri-state output, active low. This signal indicates that the CPU data bus holds valid data to be stored in the addressed memory or I/O device.
	<u>RFSH</u>	<u>Refresh</u> - Output, active low. This signal indicates that the lower 7 bits of the Address Bus contain a refresh address for dynamic memories and the current <u>MREQ</u> signal should be used to do a refresh read to all dynamic memories. A7 is a logic zero and the upper 8 bits of the Address Bus contain the contents of the I Register.

TABLE 2-1
Z80 CONTROL SIGNALS
(continued)

<u>ACRONYM</u>	<u>DEFINITION</u>
CPU CONTROL	<p><u>HALT</u> <u>Halt State</u> - Output, active low. This signal indicates that the CPU has executed a HALT instruction. CPU operations are suspended until a Non-Maskable or a Maskable Interruption (with the mask enabled) occurs. While halted, the CPU executes NOP's to maintain memory refresh.</p>
	<p><u>WAIT</u> <u>Wait</u> - Input, active low. This signal indicates to the CPU that the addressed memory or I/O device is not ready for a data transfer. The CPU will continue to enter wait states as long as this signal is active. This allows for synchronization of the CPU to external devices of varying speeds.</p>
	<p><u>INT</u> <u>Interrupt Request</u> - Input, active low. This signal is generated by external devices and is honored at the end of the current instruction if the interrupt is not masked by the software and if the <u>BUSRQ</u> signal is not active. When the CPU accepts the interruption, an acknowledge signal is sent out at the beginning of the next instruction cycle (<u>IORQ</u> at M1 time). There are three interruption modes selectable by the software.</p>
	<p><u>NMI</u> <u>Non-Maskable Interruption</u> - Input, negative edge triggered. This signal has a higher priority than <u>INT</u> and is always recognized at the end of the current instruction (cannot be masked). The CPU is forced to restart to location 0066H with the program counter saved in the external stack. NOTE: The NMI is not used in the TS2068 ROM software design.</p>

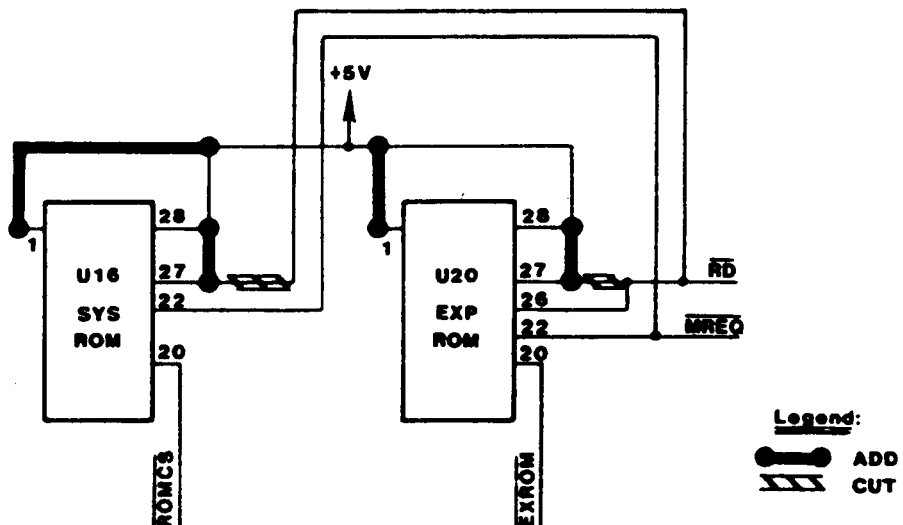
TABLE 2-1

Z80 CONTROL SIGNALS
(continued)

<u>ACRONYM</u>	<u>DEFINITION</u>
<u>RESET</u>	<u>Reset</u> - Input, active low. This signal forces the program counter to zero and initializes the CPU. Address and data buses go to their high impedance state and control output signals to their inactive state. No refresh occurs. Initialization includes: Disable the interrupt enable flip-flop and set Register I, Register R and the Interrupt Mode all to Zero.
CPU BUS CONTROL <u>BUSRQ</u>	<u>Bus Request</u> - Input, active low. This signal is used to request the CPU address bus, data bus and tri-state output control signals to go to a high impedance state permitting other devices to control these buses. The CPU sets these buses to a high impedance state at the termination of the current Machine cycle.
<u>BUSAK</u>	<u>Bus Acknowledge</u> - Output, active low. This signal is used to indicate to the requesting device that the CPU has set its address, data and control bus signals to a high impedance state in response to <u>BUSRQ</u> .

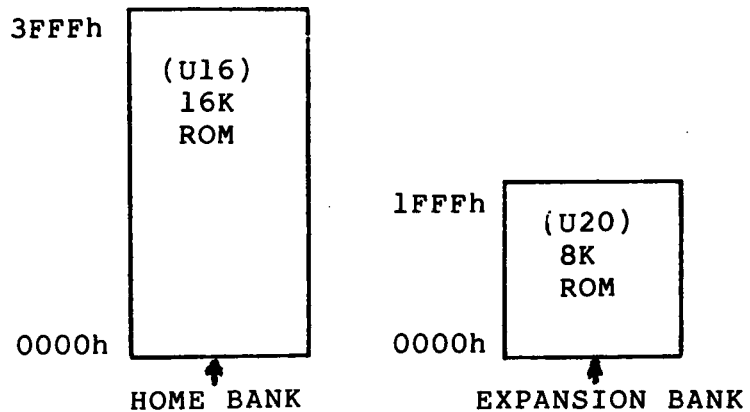
Figure 2.1.4-1

REWORK TO REPLACE ROM's with EPROM's



2.1.4 ROM

The system includes both a 16K byte ROM and an 8K byte ROM mapped into the address space as shown below.



Section 2.1.8.1 describes the selection of the Home Bank and Expansion Bank via the control logic.

The devices involved are a 23128 and a 2364 for the 16K byte (128K-bit) and the 8K byte (64K-bit) ROM's respectively. Direct replacement of these devices with 27128 and 2764 EPROM's is not possible since pins 1 and 27 must be maintained in the high state for those devices (see schematic in Section 2.2). To replace U16 and U20 with 27128 and 2764 EPROM's requires the rework shown in Figure 2.1.4-1.

- (1) Cut input to pin 27 on each chip.
- (2) Wire +5V to pins 1 and 27 on each chip to pull high.

If U20 is to be a 27128, then replace the RD input to pin 26 with address A13 from pin 26 on U16.

2.1.5 32K RAM (Address 8000-FFFFH)

The upper 32K of RAM is composed of four 200ns 4416's (16K x 4 dynamic RAMs).

2.1.6 Sound Generator

The Programmable Sound Generator (GI 8912) is accessed via Ports OF5H (Address) and OF6H (Data). The basic registers in the PSG which produce the programmed sounds include:

Tone Generators: Produce the basic square wave tone frequencies for each channel (A, B, C).

Noise Generator: Produces a frequency modulated pseudo-random pulse width square wave output.

Mixers: Combine the outputs of the Tone Generators and the Noise Generator. One for each channel (A, B, C).

Amplitude Control: Provides the D/A Converters with either a fixed or variable amplitude pattern. The fixed amplitude is under direct CPU control; the variable amplitude is accomplished by using the output of the Envelope Generator.

Envelope Generator: Produces an envelope pattern which can be used to amplitude modulate the output of each Mixer.

D/A Converters: The three D/A Converters each produce up to a 16-level output signal as determined by the Amplitude Control.

An additional register is shown in the PSG Block Diagram (Figure 2.1.6-1) which has nothing directly to do with the production of sound -- this is the I/O Port (A). Data to/from the CPU may be read/written to/from the 8-bit I/O Port without affecting any other function of the PSG. The TS 2068 uses the I/O Port to access the joysticks.

2.1.6.1 Tone Generator Control (Registers R0-R5)

The frequency of each square wave generated by the three Tone Generators (one each for Channels A, B, and C) is obtained in the PSG by first counting down the input clock by 16, then by further counting down the result by the programmed 12-bit Tone Period value. Each 12-bit value is obtained in the PSG by combining the contents of the relative Coarse and Fine Tune registers, as illustrated by Figure 2.1.6-2.

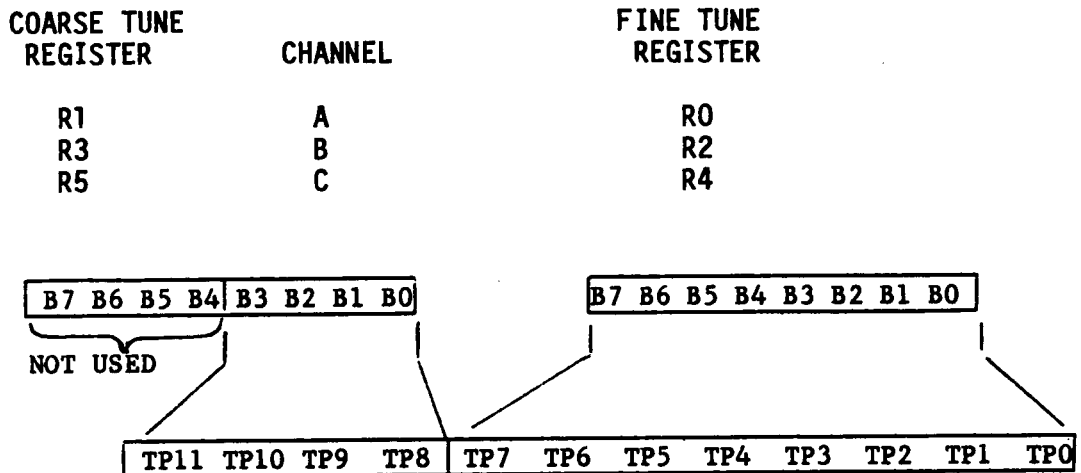
Note that the 12-bit value programmed in the combined Coarse and Fine Tune registers is a period value -- the higher the value in the registers, the lower the resultant tone frequency.

Note also that due to the design technique used in the Tone Period countdown, the lowest period value is 000000000001 (divide by 1) and the highest period value is 111111111111 (divide by 4095).

FIGURE 2.1.6-1
PSG REGISTER BLOCK DIAGRAM

REGISTER				B I T							
DEC	HEX	OCT		B7	B6	B5	B4	B3	B2	B1	B0
R0	R0	R0	Channel A	8 Bit Fine Tune							
R1	R1	R1	Tone Period	////////////////////////////////////				4 Bit Coarse Tune			
R2	R2	R2	Channel B	8 Bit Fine Tune							
R3	R3	R3	Tone Period	////////////////////////////////////				4 Bit Coarse Tune			
R4	R4	R4	Channel C	8 Bit Fine Tune							
R5	R5	R5	Tone Period	////////////////////////////////////				4 Bit Coarse Tune			
R6	R6	R6	Noise Period	5 Bit Period Control							
				IN/OUT		NOISE			TONE		
R7	R7	R7	Enable	IOB	IOA	C	B	A	C	B	A
R8	R8	R10	Ch.A Amplitude	////////////////////////////////////			M	L3	L2	L1	L0
R9	R9	R11	Ch.B Amplitude	////////////////////////////////////			M	L3	L2	L1	L0
R10	RA	R12	Ch.C Amplitude	////////////////////////////////////			M	L3	L2	L1	L0
R11	RB	R13	Envelope	8 Bit Fine Tune E							
R12	RC	R14	Period	8 Bit Coarse Tune E							
			Envelope	////////////////////////////////////							
R13	RD	R15	Shape/Cycle	////////////////////////////////////				CONT.	ATT.	ALT.	HOLD
			I/O Port A								
R14	RE	R16	Data Store	8 Bit Parallel I/O on Port A							

FIGURE 2.1.6-2
12-BIT TONE PERIOD (TP) TO TONE GENERATOR



2.1.6.1 (continued)

The equations describing the relationship between the desired output tone frequency and the input clock frequency and Tone Period value are:

$$(a) \quad fT = \frac{fCLOCK}{16TP_{10}}$$

$$(b) \quad TP_{10} = 256CT_{10} + FT_{10}$$

Where:

- fT = Desired tone frequency
- $fCLOCK$ = Input clock frequency
- TP_{10} = Decimal equivalent of the Tone Period bits TP11 to TP0
- CT_{10} = Decimal equivalent of the Coarse Tune register bits B3 to B0 (TP11 to TP8)
- FT_{10} = Decimal equivalent of the Fine Tune register bits B7 to B0 (TP7 to TP0)

From the above equations, it can be seen that the tone frequency can range from a low of:

$$fCLOCK/65520 \text{ (wherein } TP_{10} = 4095 \text{)}$$

to a high of:

$$fCLOCK/16 \text{ (wherein } TP_{10} = 1 \text{)}$$

The TS 2068 uses a 1.76475 MHz input clock, so it can produce a range of 26.9 Hz to 110 kHz.

2.1.6.1 (continued)

To calculate the values for the contents of the Tone Period Coarse and Fine Tune registers, given the input clock and the desired output tone frequencies, we simply rearrange the above equations, yielding:

$$(a) \quad TP_{10} = \frac{f_{CLOCK}}{16 \cdot FT}$$

$$(b) \quad CT_{10} + \frac{FT_{10}}{256} = \frac{TP_{10}}{256}$$

Example 1: $FT = 1 \text{ kHz}$ $f_{CLOCK} = 1.76475 \text{ MHz}$

$$TP_{10} = \frac{1.76475 \times 10^6}{16(1 \times 10^3)} = 110.3$$

Substituting this result into equation (b):

$$CT_{10} + \frac{FT_{10}}{256} = \frac{110.3}{256}$$

resulting in:

$$CT_{10} = 0 = 0000 \text{ (B3-B0)}$$

$$FT_{10} = 110 = 01101110 \text{ (B7-B0)}$$

Example 2: $FT = 100 \text{ Hz}$ $f_{CLOCK} = 1.76475 \text{ MHz}$

$$TP_{10} = \frac{1.76475 \times 10^6}{16(100)} = 1103$$

Substituting this result into equation (b):

$$CT_{10} + \frac{FT_{10}}{256} = \frac{1103}{256} = 4 + \frac{79}{256}$$

resulting in:

$$CT_{10} = 4 = 0100 \text{ (B3-B0)}$$

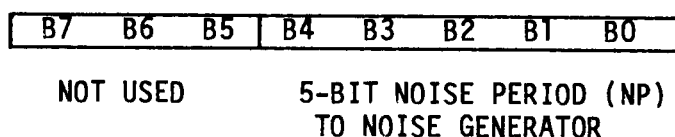
$$FT_{10} = 79 = 01001111 \text{ (B7-B0)}$$

2.1.6.2 Noise Generator Control (Register R6)

The frequency of the noise source is obtained in the PSG by first counting down the input clock by 16, then by further counting down the result by the programmed 5-bit Noise Period value. This 5-bit value consists of the lower 5 bits (B4-B0) of Register R6 as illustrated by Figure 2.1.6-3.

FIGURE 2.1.6-3

NOISE PERIOD REGISTER R6



Note that the 5-bit value in R6 is a period value -- the higher the value in the register, the lower the resultant noise frequency. Note also that, as with the Tone Period, the lowest period value is 00001 (divide by 1); the highest period value is 11111 (divide by 31₁₀).

The noise frequency equation is:

$$fN = \frac{fCLOCK}{16 \cdot NP_{10}}$$

Where:

- fN = Desired noise frequency
- fCLOCK = Input clock frequency
- NP₁₀ = Decimal equivalent of the Noise Period register bits B4-B0.

From the above equation it can be seen that the noise frequency can range from a low of $fCLOCK/496$ (wherein $NP_{10} = 31_{10}$) to a high of $fCLOCK/16$ (wherein $NP_{10} = 1$). Using a 1.76475 MHz clock, for example, would produce a range of noise frequencies from 3.6 kHz to 110.3 kHz.

To calculate the value for the contents of the Noise Period register, given the input clock and the desired output noise frequencies, we simply rearrange the above equation, yielding:

$$NP_{10} = fCLOCK/16fN$$

2.1.6.3 Mixer Control I/O Enable (Register R7)

Register 7 is a multi-function Enable register which controls the three Noise/Tone Mixers and the two general purpose I/O ports.

The Mixers, as previously described, combine the noise and tone frequencies for each of the three channels. The determination of combining neither/either/both noise and tone frequencies on each channel is made by the state of bits B5 thru B0 of R7.

The direction (input or output) of the two general purpose I/O ports (IOA and IOB) is determined by the state of bits B7 and B6 of R7. Note that in the TS 2068 there is no second I/O Port B.

These functions are illustrated by Figure 2.1.6-4 and Tables 2.1.6-1 and 2.1.6-2 below.

FIGURE 2.1.6-4

MIXER CONTROL - I/O ENABLE REGISTER R7

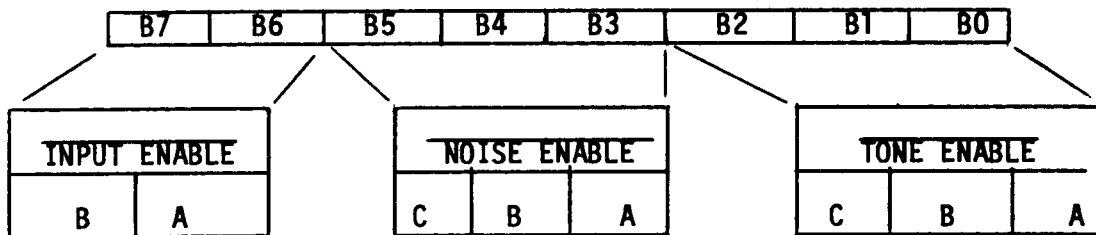


TABLE 2.1.6-1

I/O ENABLE TRUTH TABLE

NOISE ENABLE TRUTH TABLE				TONE ENABLE TRUTH TABLE			
R7 BITS B5 B4 B3			Noise Enabled on Channel	R7 BITS B2 B1 B0			Tone Enabled on Channel
0	0	0	C B A	0	0	0	C B A
0	0	1	C B -	0	0	1	C B -
0	1	0	C - A	0	1	0	C - A
0	1	1	C - -	0	1	1	C - -
1	0	0	- B A	1	0	0	- B A
1	0	1	- B -	1	0	1	- B -
1	1	0	- - A	1	1	0	- - A
1	1	1	- - -	1	1	1	- - -

TABLE 2.1.6-2
I/O PORT TRUTH TABLE

R7 BITS	I/O Port Status
B6	IOA
0	Input
1	Output

NOTE

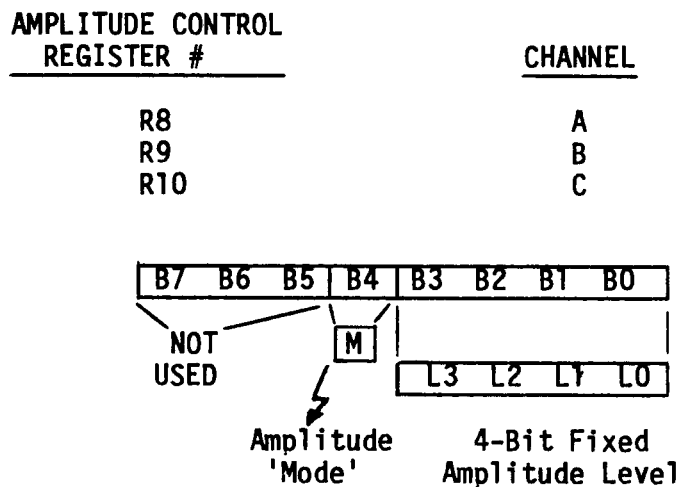
Disabling noise and tone does not turn off a channel. Turning a channel off can only be accomplished by writing all zeroes into the corresponding Amplitude Control register, R8, R9 or R10 (refer to Paragraph 2.1.6.4).

2.1.6.4 Amplitude Control (Registers R8, R9, R10)

The amplitudes of the signals generated by each of the three D/A Converters (one each for Channels A, B, and C) is determined by the contents of the lower 5 bits (B4-B0) of Registers R8, R9 and R10 as illustrated by Figure 2.1.6-5.

FIGURE 2.1.6-5

D/A CONVERTER SIGNAL GENERATION



2.1.6.4 (continued)

The amplitude 'mode' (Bit M) selects either fixed level amplitude (M=0) or variable level amplitude (M=1). It follows then that Bits L3-L0 defining the value of a 'fixed' level amplitude, are only active when M=0. When fixed level amplitude is selected, it is 'fixed' only in the sense that the amplitude level is under the direct control of the system processor (via bits L3-L0). Varying the amplitude when in this 'fixed' amplitude mode requires in each instance the direct intervention of the system processor via an address latch/write data sequence to modify the L3-L0 data.

When M=1 (select 'variable' level amplitudes), the amplitude of each channel is determined by the envelope pattern as defined by the Envelope Generator's 4-bit output E3-E0 (refer to Paragraph 2.1.6.5).

The amplitude 'mode' (Bit M) can be thought of as an 'envelope enable' bit, i.e. when M=0 the envelope is not used, and when M=1 the envelope is enabled.

Figure 2.1.6-6 illustrates all combination of the 5-bit Amplitude Control.

FIGURE 2.1.6-6

AMPLITUDE CONTROL REGISTERS

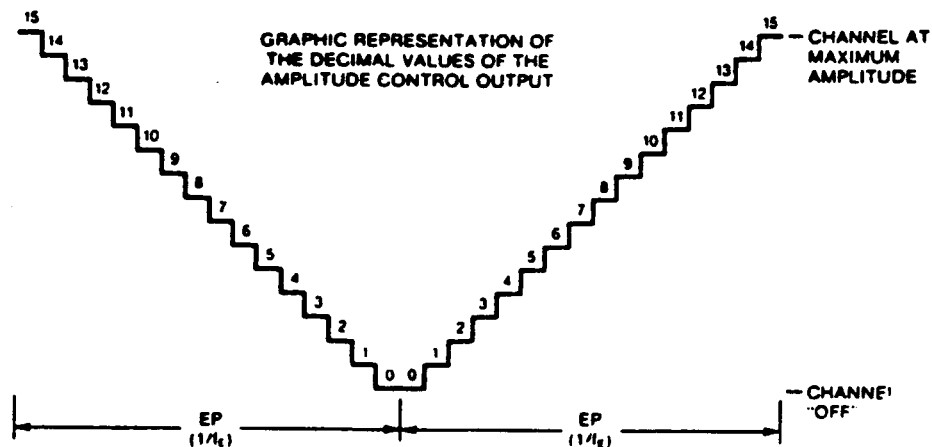
AMPLITUDE CONTROL REGISTER #								CHANNEL					
R8								A					
R9								B					
R10								C					
B7	B6	B5	B4	B3	B2	B1	B0	Amplitude Control Output					
NOT USED			M	L3	L2	L1	L0						
0	0	0	0	0	0	0	0	*0	0	0	0	0	The amplitude is fixed at 1 of 16 levels as determined by L3-L0.
0	
0	
0	
0	
0	1	1	1	1	1	1	1	1	1	1	1	1	
1	X	X	X	X	X	X	X	E3	E2	E1	E0		The amplitude is variable at 16 levels as determined by the output of the Envelope Gen.
(X=Don't Care)													

*The all zeros code is used to turn a channel "off".

2.1.6.4 (continued)

Figure 2.1.6-7 graphically illustrates a selection of variable level (envelope-controlled) amplitude where the 16 levels directly reflect the output of the Envelope Generator. A fixed level amplitude would correspond to only one of the levels shown, with the level directly determined by the decimal equivalent of Bits L3-L0.

FIGURE 2.1.6-7
VARIABLE AMPLITUDE CONTROL (M=1)



2.1.6.5 Envelope Generator Control (Registers R11, R12, R13)

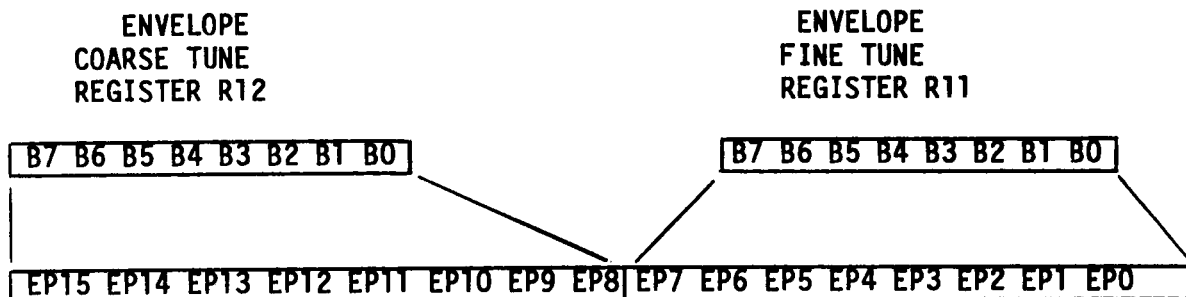
To accomplish the generation of fairly complex envelope patterns, two independent methods of control are provided in the PSG; first, it is possible to vary the frequency of the envelope using registers R11 and R12; and second, the relative shape and cycle pattern of the envelope can be varied using register R13. The following paragraphs explain the details of the envelope control functions, describing first the envelope period control and then the envelope shape/cycle control.

2.1.6.5.1 Envelope Period Control (Registers R11, R12)

The frequency of the envelope is obtained in the PSG by first counting down the input clock by 256, then by further counting down the result by the programmed 16-bit Envelope Period value. This 16-bit value is obtained in the PSG by combining the contents of the Envelope Coarse and Fine Tune registers, as illustrated by Figure 2.1.6-8.

FIGURE 2.1.6-8

16-BIT ENVELOPE PERIOD (EP) TO ENVELOPE GENERATOR



Note that the 16-bit value programmed in the combined Coarse and Fine Tune registers is a period value - the higher the value in the registers, the lower the resultant envelope frequency.

Note also that, as with the Tone Period, the lowest period value is 0000000000000001 (divide by 1); the highest period value is 1111111111111111 (divide by $\frac{65,535}{2}$).

The envelope frequency equations are:

$$(a) \quad fE = \frac{fCLOCK}{256 \cdot EP} \quad (b) \quad EP = \frac{256 \cdot CT}{10} + \frac{FT}{10}$$

Where:

- fE = Desired envelope frequency
- fCLOCK = Input clock frequency
- EP = Decimal equivalent of the Envelope Period bits EP15-EPO
- CT = Decimal equivalent of the Coarse Tune register bits B7-B0 (EP15-EP8)
- FT = Decimal equivalent of the Fine Tune register bits B7-B0 (EP7-EPO)

From the above equation it can be seen that the envelope frequency can range from a low of $\frac{fCLOCK}{16,766,960}$ (wherein $EP = \frac{65,535}{10}$) to a high of $\frac{fCLOCK}{256}$ (wherein $EP = 1$). Using a 1.76475 MHz clock, for example, would produce a range of envelope frequencies from 0.105 Hz to 6893.6 Hz.

To calculate the values for the contents of the Envelope Period Coarse and Fine Tune registers, given the input clock and the desired envelope frequencies, we rearrange the above equations, yielding:

$$(a) \quad EP_{10} = \frac{fCLOCK}{256fE}$$

$$(b) \quad CT_{10} + FT_{10} = \frac{EP_{10}}{256}$$

Example:

$$fE = 0.5 \text{ Hz}$$

$$fCLOCK = 1.76475 \text{ MHz}$$

$$EP_{10} = \frac{1.76475 \times 10^6}{256(0.5)} = 13787$$

Substituting this result into equation (b):

$$CT_{10} + FT_{10} = \frac{13787}{256} = 53 + \frac{219}{256}$$

$$CT_{10} = 53 = 00110101_2 \quad (B7-B0)$$

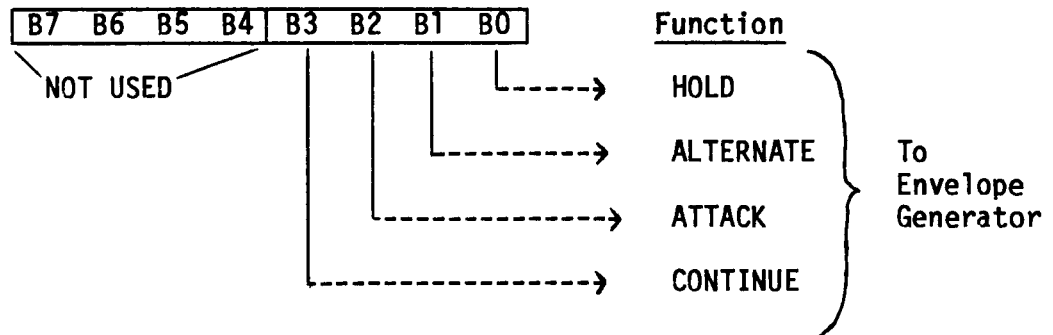
$$FT_{10} = 219 = 11011011_2 \quad (B7-B0)$$

2.1.6.5.2 Envelope Shape/Cycle Control (Register R13)

The Envelope Generator further counts down the envelope frequency by 16, producing a 16-state per cycle envelope pattern as defined by its 4-bit counter output, E3-E0. The particular shape and cycle pattern of any desired envelope is accomplished by controlling the count pattern (count up/count down) of the 4-bit counter and by defining a single-cycle or repeat-cycle pattern. This envelope shape/cycle control is contained in the lower 4 bits (B3-B0) of register R13. Each of these 4 bits controls a function in the envelope generator, as illustrated in Figure 2.1.6-9.

FIGURE 2.1.6-9

ENVELOPE SHAPE/CYCLE CONTROL REGISTER (R13)



The definition of each function is as follows:

HOLD When set to logic "1", limits the envelope to one cycle, holding the last count of the envelope counter (E3-E0 = either 0000 or 1111, depending on whether the envelope counter was in countdown or countup mode respectively).

ALTERNATE When set to logic "1", the envelope counter reverses count direction (up-down) after each cycle.

NOTE

When both the Hold bit and the Alternate bit are ones, the envelope counter is reset to its initial count before holding.

ATTACK When set to logic "1", the envelope counter will count up (attack) from E3-E0 = 0000 to E3-E0 = 1111; when set to logic "0", the envelope counter will count down (decay) from 1111 to 0000.

CONTINUE When set to logic "1", the cycle pattern will be as defined by the Hold bit; when set to logic "0", the envelope generator will reset to 0000 after one cycle and hold at that count.

To further describe the above functions, numerous charts of the binary count sequence of E3-E0 could be used, showing each combination of Hold, Alternate, Attack and Continue. However, since these outputs are used (when selected by the Amplitude Control registers) to amplitude modulate the output of the Mixers, a better understanding of their effect can be accomplished via a graphic representation of their value for each condition selected, as illustrated in Figures 2.1.6-10 and 2.1.6-11.

FIGURE 2.1.6-10
 ENVELOPE GENERATOR OUTPUT

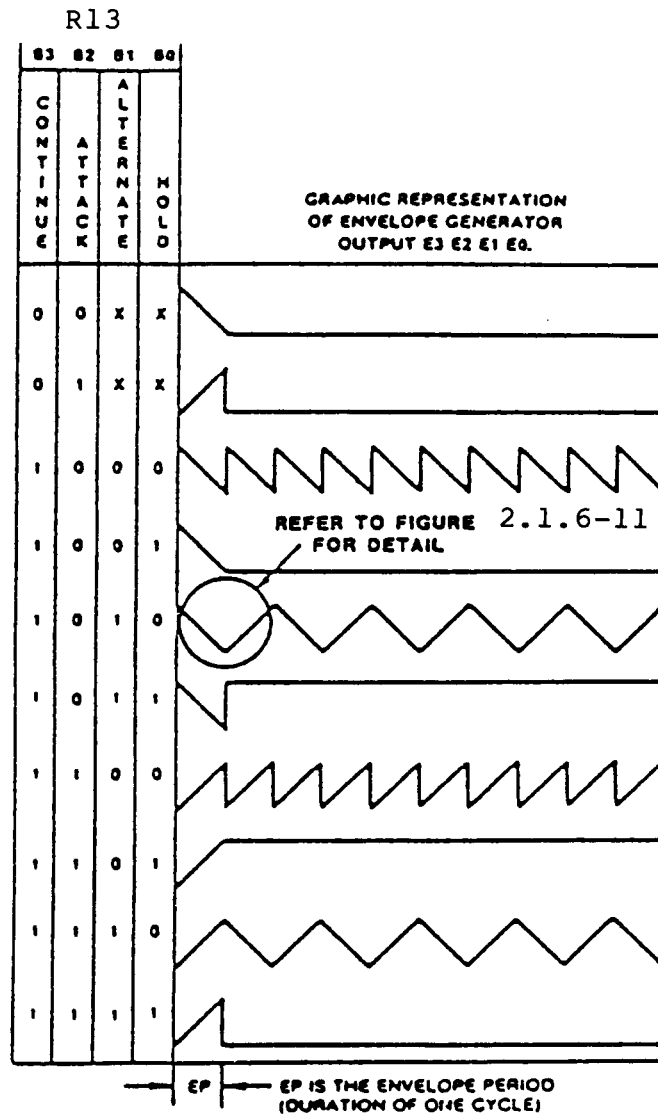
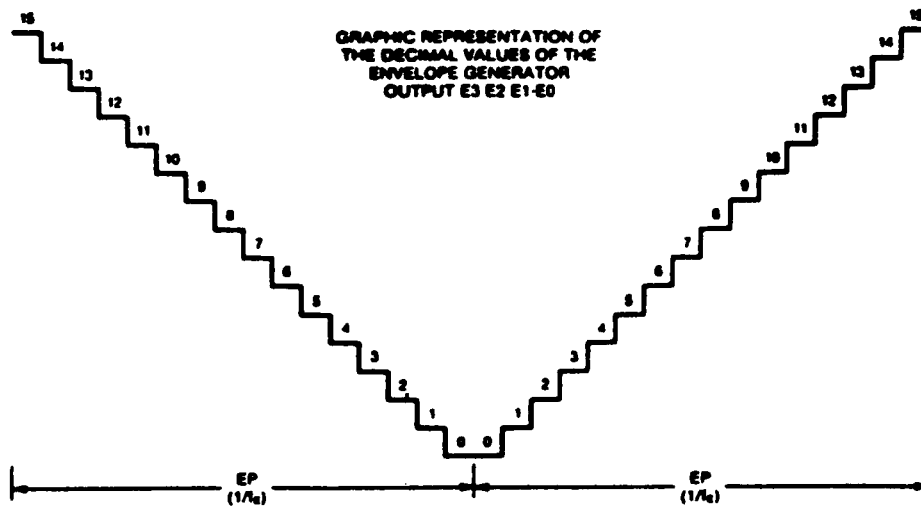


FIGURE 2.1.6-11

DETAIL OF TWO CYCLES OF FIGURE 2.1.6-10



2.1.6.6 I/O Port Data Store (Register R14)

Register R14 functions as an intermediate data storage register between the PSG/CPU data bus (DA7-DA0) and the I/O Port (IOA7-IOA0). This port is available for reading the joysticks. Using register R14 for the transfer of I/O data has no effect at all on sound generation.

To output data from the CPU bus to a peripheral device connected to I/O Port A would require the following steps:

1. Latch address R7 (select Enable register)
2. Write data to PSG (setting R7, B6=1)
3. Latch address R14 (select IOA register)
4. Write data to PSG (data to be output on I/O Port A)

To input data from I/O Port A to the CPU bus would require the following:

1. Latch address R7 (select Enable register)
2. Write data to PSG (setting R7, B6=0)
3. Latch address R14 (select IOA register)
4. Read data from PSG (data from I/O Port A)

Note that once loaded with data in the output mode, the data will remain on the I/O port until changed either by loading different data, by applying a reset (grounding the Reset pin), or by switching to the input mode.

Note also that when in the input mode, the contents of register R14 will follow the signals applied to the I/O port. However, transfer of this data to the CPU bus requires a "read" operation as described above.

2.1.7 Joystick Port Operation

The joystick port (Register 14 of the Sound Chip - Section 2.1.6.6) is read via an IN-instruction directed at port F6H with selection of activating data from the left (player 1) or right (player 2) determined by Address bits 8 and 9 as shown in Figure 2.1.7-1. In order to address Register 14, a OEH must be written to port F5H (Sound Generator Address) prior to reading joystick data. Section 4.4 describes the software sequence necessary to control this hardware.

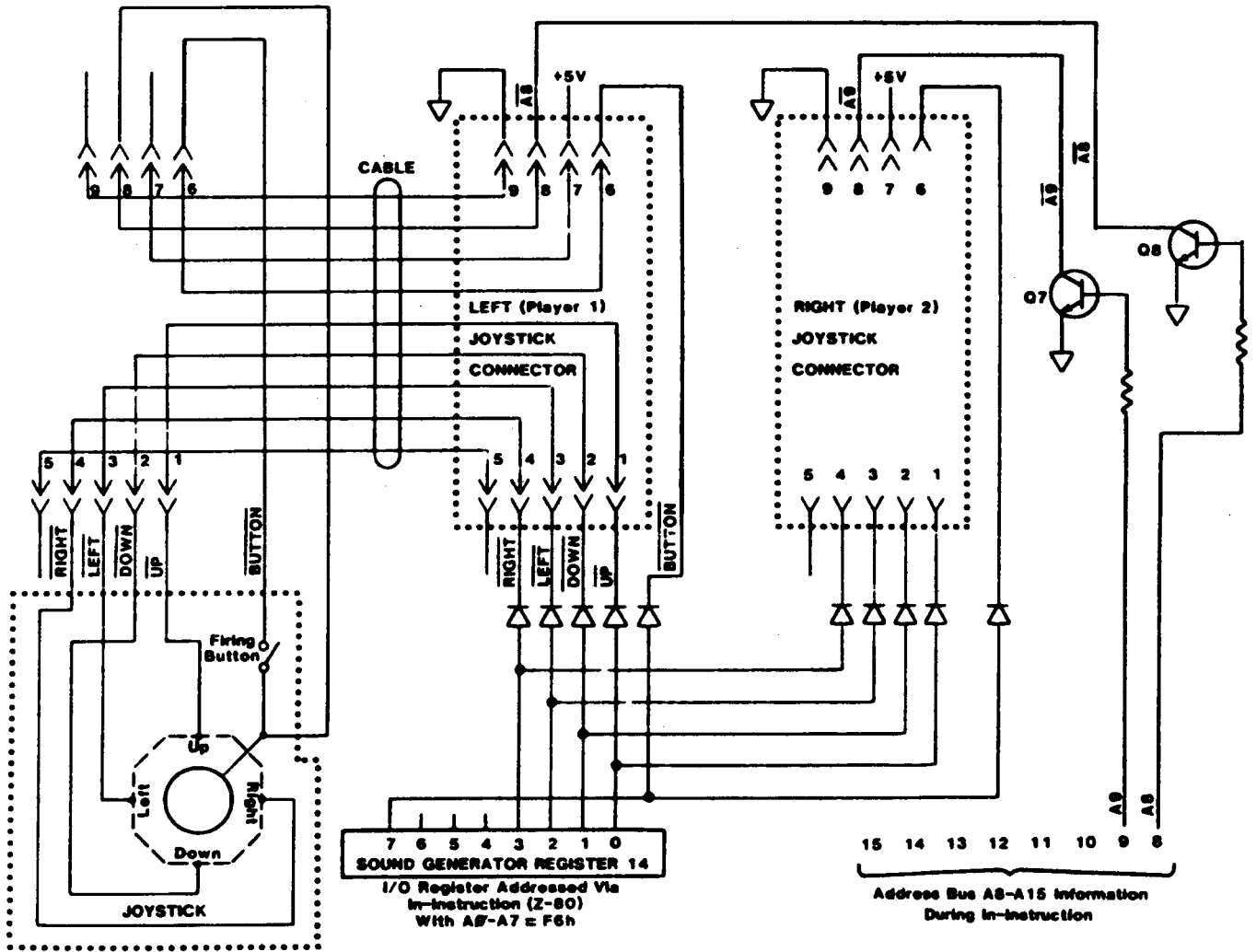
In the example of Figure 2.1.7-1, the joystick, shown schematically in the lower left of the drawing, is composed of a movable center stick which is pushed up to touch the up-contact and, therefore, electronically connects pin-8 to pin-1. In this state, a read of port F6H with address bit A8 high, causes actions as follows:

- (1) Address A8 high turns on transistor Q8
- (2) Q8 drives cable pin-8 low
- (3) The movable center stick of the joystick in contact with the up-contact results in a conductive path from cable pin-8 to cable pin-1.
- (4) Pin-1 low results in a 0 in bit position 0 of the I/O register via the isolation diode.

The various positions of the stick similarly result in various bits being read from the I/O register.

Note that +5 volts and ground are available on the connector so +5V logic could be attached to the joystick port.

FIGURE 2.1.7-1
JOYSTICK PORT OPERATION



2.1.8 Control Logic

The control logic of the TS2068 is primarily a Standard Cell Logic Device in a 68-pin JEDEC leaded carrier package and includes the following major functions:

SECTION	FUNCTION
2.1.8.1	Bank Selection Logic
2.1.8.2	Z-80 Clock Generation
2.1.8.3	Display Timing, DMA Display File Access, Attribute Control, and Pixel Data Serial Shift
2.1.8.4	Interruption Generation
	BEEP Output (See Section 2.1.13.2)
	CASSETTE I/O (See Section 2.1.12).

Additionally, Table 2.1.8-1 provides a description of the function of each SCLD I/O pin. See the System Schematic in Appendix D for pin numbering.

2.1.8.1 Bank Selection Logic

The TS2068 is a Z-80 based computer, therefore it can directly address only 64K bytes of memory via its 16-bit address. Additionally, since the Z-80 has no relocation or indirection capability, the conventional technique of extending the memory space available to the Z-80 is bank switching. The TS2068 provides extended bank switching by allowing selection of memory in 8K "chunks" which are identified by bank number and chunk number as illustrated in Figure 2.1.8-1 for the internal bank selection logic. The externally sourced \overline{BE} (Bank Enable) signal can be used by external logic to disable the internally controlled memories.

As shown in Figure 2.1.8-1:

(1) The cartridge is selected on a memory access with:

- a. Port FF bit 7 = 0
- b. The HSR at port F4h has a "1" in the bit selected by a decode of Address bits A13-A15. and
- c. \overline{BE} is high

causing activation of \overline{ROSCS} (ROS Chip Select).

(2) The EXROM bank is selected on a memory access with:

- a. Port FF bit 7 = 1
- b. The HSR at port F4H has a "1" in the bit selected by a decode of Address bits A13 - A15.
- c. \overline{BE} is high

causing the activation of \overline{EXROM} (Ext. ROM Enable)

(3) The Home Bank is selected on a memory access with

- a. The HSR at Port F4H has a "0" in the bit selected by a decode of Address bits A13 - A15.
- b. \overline{BE} is high.

causing the activation of the appropriate enable signal as detailed below.

To understand the details of the schematic of Section 2.2 (Appendix D):

- (1) SELECT CARTRIDGE of Figure 2.1.8-1 involves activating $\overline{R0SCS}$ to its low active state
- (2) SELECT EXROM of Figure 2.1.8-1 involves activating \overline{EXROM} to its low active state
- (3) SELECT HOME BANK of Figure 2.1.8-1 involves
 - a. Activating \overline{ROMCS} to its low active state when A15=0 and A14=0
 - b. Activating $\overline{CAS1}$ to its low active state when A15=0 and A14=1
 - c. Activating $\overline{CAS2}$ to its low active state when A15=1 and A14=0
 - d. Activating $\overline{CAS3}$ to its low active state when A15=1 and A14=1.

FIGURE 2.1.8-1
BANK SELECTION LOGIC

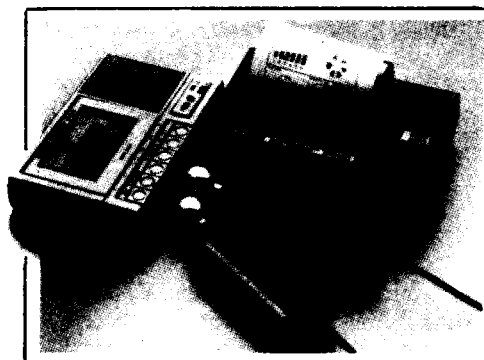
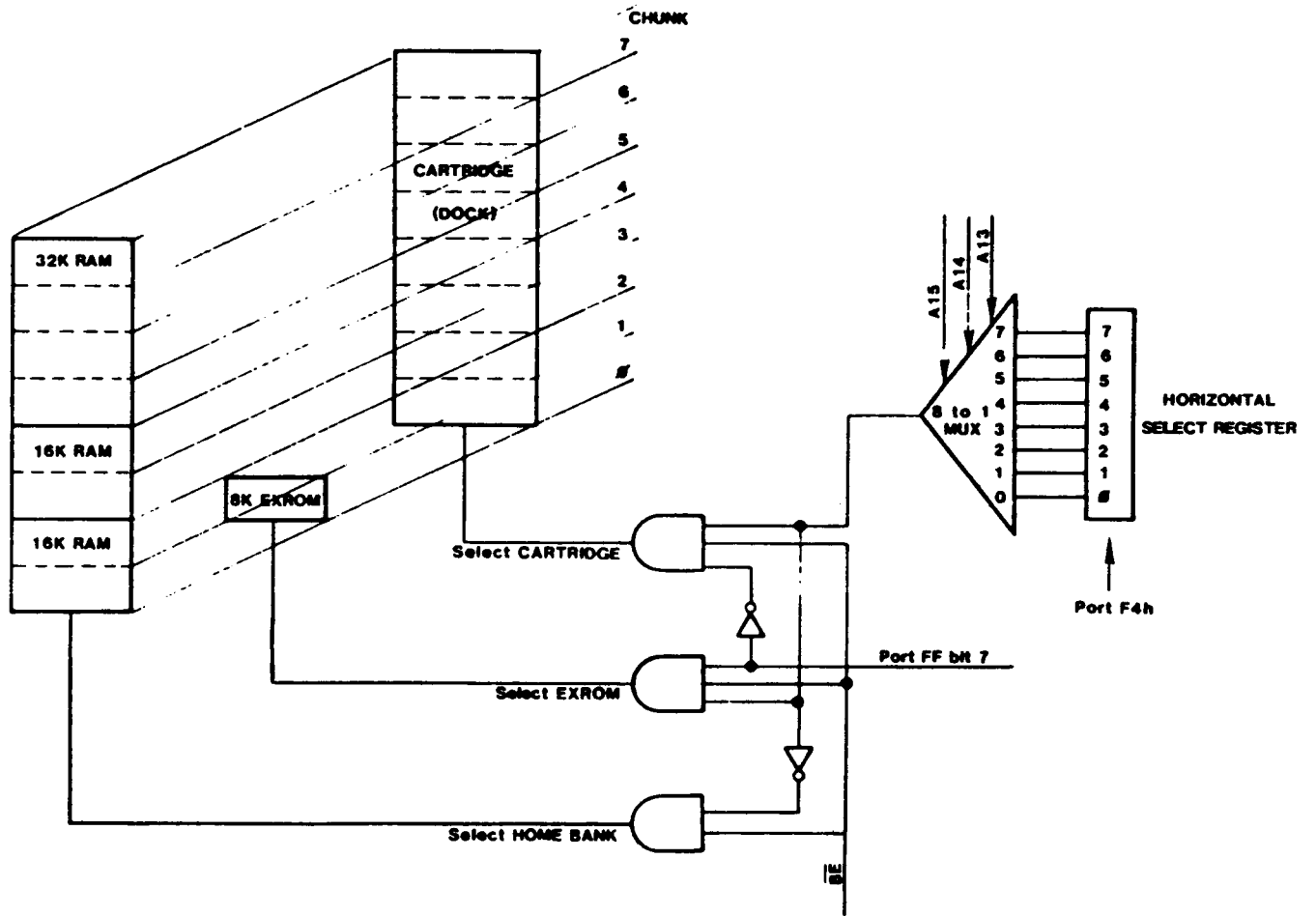


TABLE 2.1.8-1

SCLD I/O PIN FUNCTION DEFINITIONS

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
A0-A7 A13-A15	Address Bus	In	Address Bus lines Input from Z80A
D0-D7	Data Bus	In/Out	Data Bus inputs/outputs from/to Z80A through U9-74LS245 or inputs from display RAM (16K) - U6 and U7
KB0-KB4	Keyboard Outputs	In	Inputs from 5 lines of keyboard matrix - goes low at one of 8 address line (active low) sequences on I/O Request
A7R	A7+Refresh	Out	To refresh and address 8th bit address line input of RAM memory (not display) of 32K of 4416 RAM's (Home Bank 8000H to FFFFH)
MA0-MA7	Muxed Adrs.Bus	Out	Display memory muxed address bus and refresh
TS	Tri-State Display Memory Ctl.	Out	Tri-State control for address and data buffers when CPU is addressing display memory at same time display controller is addressing the display memory
OCPU	Clock to CPU	Out	CLK - Clock to Z80A CPU which is interrupted to stop CPU when CPU wants to address display RAM at same time as display controller
\overline{RD}^*	Read Direction Control to SCLD	Out	To control read/write direction of 74LS245 Data Bus Buffer between CPU and SCLD
\overline{ROMCS}	Home ROM Chip Select	Out	To activate the 16K Home ROM (first 16K) when memory selection (MS) is set to Home Bank
\overline{RAST}	Row Address Strobe #1	Out	To activate row address strobe for display memory only during memory read/write, refresh and display read

TABLE 2.1.8-1

SCLD I/O PIN FUNCTION DEFINITIONS
(continued)

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
$\overline{\text{CAS1}}$	Column Address Strobe #1	Out	To activate column address strobe for display memory only (2nd 16K) during memory read/write and display read
$\overline{\text{CAS2}}$	Column Address Strobe #2	Out	To activate column address strobe for Home Bank RAM (3rd 16K)
$\overline{\text{CAS3}}$	Column Address Strobe #3	Out	To activate column address strobe for Home Bank RAM (4th 16K)
$\overline{\text{DRAMWE}}$	Dynamic RAM Write Enable	Out	When active low, enables a write into the display RAM only
MUX	Mux Control of RAM Address	Out	Mux control to 74LS157 (U10 & U11) to multiplex the row and column addresses to all dynamic RAM's
V	Chroma Vector V	Out	Color vector level for quadrature (R-Y) input to video modulator
Y	Luminance Y	Out	Luminance (brightness) control level
$\overline{\text{RD}}$	Read to CPU	In	CPU is reading from a memory or I/O location
$\overline{\text{WR}}$	Write from CPU	In	CPU is writing to a memory or I/O location
$\overline{\text{MREQ}}$	Memory Request	In	CPU is requesting access to a memory location to read or write
$\overline{\text{IORQ}}$	I/O Request	In	CPU is requesting access to an I/O location to read or write

TABLE 2.1.8-1

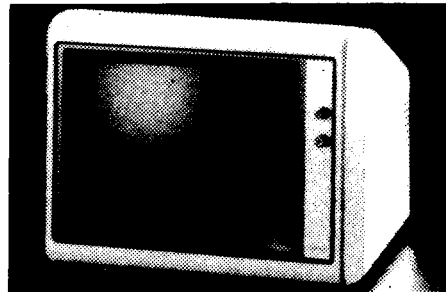
SCLD I/O PIN FUNCTION DEFINITIONS
(continued)

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
RFSH	Refresh	In	CPU is generating a refresh address to refresh dynamic RAM's
Tape In	Tape Input	In	Magnetic tape signal input
BE	Bank Enable	In	When active low, indicates that internal memory is disabled (Home, Extension and Dock Banks) and an external memory is in use
EXROM	Extension ROM Select	Out	Active low chip select signal for Extension ROM
VCC	+5 Volt Power	In	Power (+5V) input to SCLD
INT	Interrupt to CPU	Out	Interrupts CPU to handle keyboard strobing and timer for PAUSE command. Open drain N channel with internal pull-up
ROSCS	ROS Chip Select	Out	ROM-Oriented Software (Cartridge Bank) Chip Select
SPKR/TAPE OUT	Speaker and Tape Output	Out	Digital output to magnetic tape and to sound amplifier for speaker output
OC	Clock "C"	Out	Clock for sound chip @1.764 MHz.
BDIR	Bus Direction to Sound Chip	Out	A bus direction control signal to the PSG. When high the sound chip either receives a write to PSG or latches addresses from the data bus
BC1	Bus Control to Sound Chip	Out	A bus control signal to the PSG. When high the sound chip either is read to data bus or latches addresses from the data bus

TABLE 2.1.8-1

SCLD I/O PIN FUNCTION DEFINITIONS
(continued)

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
OSC Out	Oscillator Out	Out	Xtal Oscillator amplifier output to drive crystal
OSC In	Oscillator In	In	Xtal Oscillator amplifier input to sense crystal signal
U	Chroma Vector U	Out	Color vector level for quadrature (B-Y) input to video modulator
GND	Ground	In	Ground return of SCLD
\bar{O}	Buffered Clock	Out	Buffered CPU clock to outside (J1 - connector)
R	Red Color Output	Out	Produce color signals to RGB monitor (TTL level)
G	Green Color Output	Out	Produce color signals to RGB monitor (TTL level)
B	Blue Color Output	Out	Produce color signals to RGB monitor (TTL level)



2.1.8.2 Z-80 Clock Generation

The oscillator circuit utilizes an AT-cut quartz crystal at 14.112 MHz. This oscillator feeds a divide by 4 chain to generate the 3.528 MHz clock for the CPU (0 CPU). This clock runs continuously except when the CPU addresses the 16K bytes of RAM containing the video display file at the same time the video display processor logic requires access to that same RAM. For this contention case the CPU clock is stopped in the high state until the video display processor access has been completed, then the CPU clock continues in its normal manner.

2.1.8.3 Display File H/W Control and Timing

The 14.112 MHz oscillator is also used to drive the counter chain deriving video timing. By dividing the 14.112 MHz. signal by 896 a 15.75 KHz horizontal sweep frequency is generated. The 15.75 KHz signal feeds a 9-stage counter which counts from 0 to 106H (262 decimal) developing the 60.1145 Hz vertical sync. See Figure 2.1.8-2.

During each horizontal scan the video display processor accesses, in the standard video mode, 32 bytes of pixel data plus 32 bytes of attributes by 32 memory accesses reading 2 bytes per access in RAM page mode, i.e. the low order address bits are provided to the RAM once via RAS activation, then the data byte is read during the first activation of CAS and the attribute byte is read during the second activation of CAS. The page mode operation is completed by deactivating RAS. (See Fig. 2.1.8-2.)

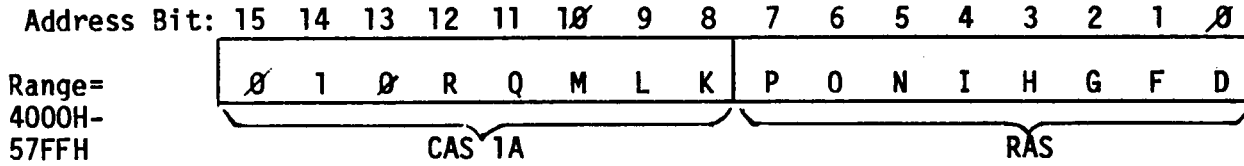
The accessed pixel data is serially shifted out to the video generation circuitry at a rate of 1 bit each 142 nanoseconds (7.056 MHz) resulting in the need to fetch a new data/attribute pair each 1.134 microseconds during the horizontal scan time. The shifted out pixel information is used to control the selection of the 3 paper color (pixel=0) or 3 ink color (pixel=1) bits to be gated out as the R, G, and B signals. When FLASH is enabled by the attribute byte, the INK and PAPER field information is swapped at the 1.879 Hz. flash rate. The R, G, and B signals control the D-to-A converter which generates the proper U, V, and \overline{Y} outputs for use by the 1889 to create composite video.

The address information provided to the RAM's during RAS and CAS times is as shown in Figure 2.1.8-2. This address generation logic explains the non-sequential nature of the video display as described in Section 2.1.10.

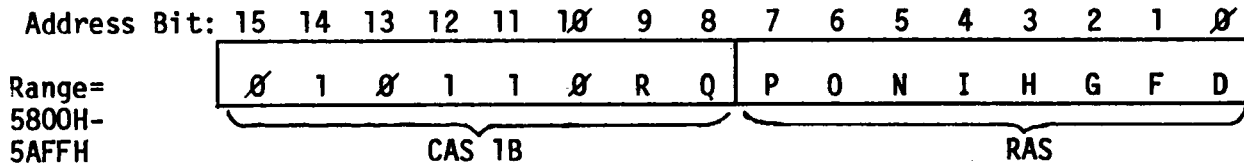
FIGURE 2.1.8-2

VIDEO DISPLAY PROCESSOR RAM ADDRESS GENERATION
(Normal Video Mode)

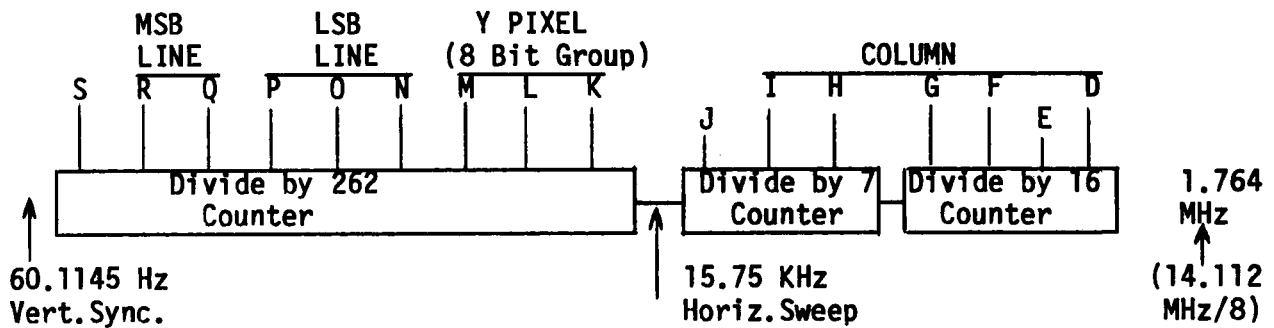
DISPLAY PIXEL DATA ADDRESS



DISPLAY ATTRIBUTE ADDRESS



VIDEO TIMING COUNTER CHAIN



2.1.8.4 Interruption Generation (17 ms)

During the vertical blanking interval (once each 15.635 ms) the SCLD, if enabled by the INTEN bit (Bit 6) of I/O Port FFH, activates the INT signal which directly connects to the INT input to the Z80. A CPU maskable interruption can then occur, as described in Section 2.1.3.7, if enabled.

2.1.9 Keyboard

The keyboard for the TS 2068 has forty-two (42) hard keys (typewriter style) with tactile feel utilizing an over-dead-center type of rubber spring pad and a carbon pill that hits the P.C. board, just under the keyboard, to short-out a pair of closely placed precious metal contacts. The read-out matrix is an eight by five cross point switching as shown in Figure 2.1.9-1.

Each switch closure connects one of the eight high order address lines (by going low through a diode) to one of the five input lines to the SCLD (KB0 through KB4).

Scanning is by software algorithm as described in Section 4.1.1. During the IN instruction, address bits A0-A7=FEH select the Keyboard I/O port while bits A8-A15 select the particular 5 keys to be sampled during the particular IN instruction execution. For example, an IN instruction directed at the keyboard I/O port with address bit A8 low and A9-A15 high will supply 0's on KB0, KB1, KB2, KB3, and/or KB4 if the CAP SHIFT, Z, X, C, and/or V keys are respectively depressed.

Note that when reading the I/O port FEH, data bits D5-D7 are not part of the keyboard information.

Section 2.4.7 details the connection of the keyboard to the main P.C. board.

2.1.10 16K Video Display RAM

The 16K-byte video display RAM, composed of two 4416's, is isolated from the Z80A CPU by the SCLD control logic and buffers to allow the video display processor to access pixel and attribute data from the display files independent of the CPU (see Section 2.1.8.3).

The Video Display RAM is located in Chunks 2 and 3 of the Home Bank, beginning at 4000H and 6000H respectively. Figure 2.1.10-1 illustrates the organization of the Primary Display File located at 4000H. The second display file utilizes the same organization. Based on the video mode set via Port FFH, the video hardware accesses the RAM for pixel data and attribute control information.

Figure 2.1.9. KEYBOARD SCHEMATIC

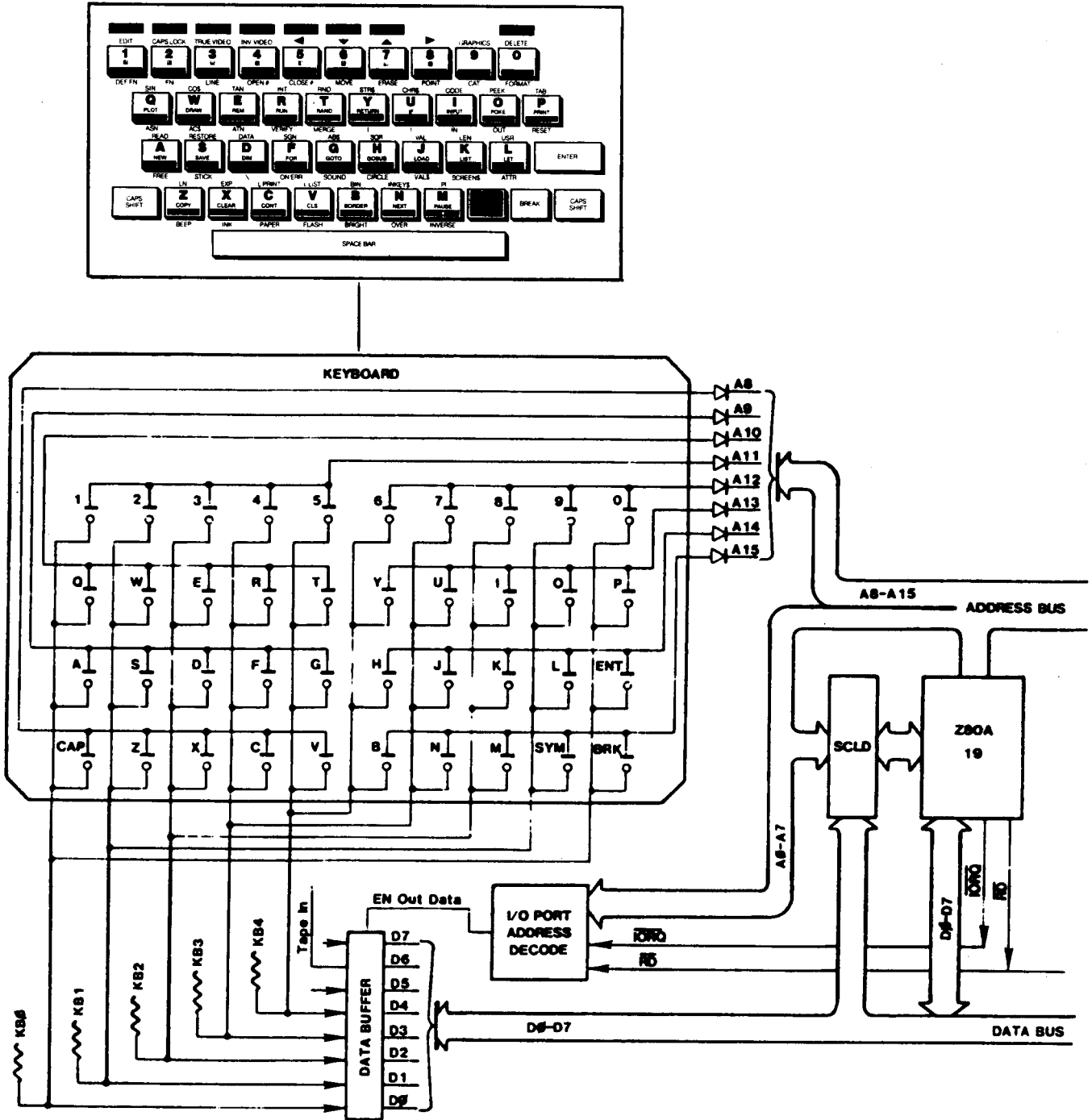


FIG. 2.1.10-1
 DISPLAY FILE ORGANIZATION (NORMAL MODE)

		32 BYTES			32 BYTES			32 BYTES		
		LINE 0			LINE 1			LINE 7		
B L O C K 0	Scan 0	4000	4001.....401F	4020.....403F	40E0	40E1	40FF	
	1	4100	4101.....411F	4120.....413F	41E0	41E1	41FF	
	2	4200	4201.....421F	4220.....423F	42E0	42E1	42FF	
	3	4300	4301.....431F	4320.....433F	43E0	43E1	43FF	
	4	4400	4401.....441F	4420.....443F	44E0	44E1	44FF	
	5	4500	4501.....451F	4520.....453F	45E0	45E1	45FF	
	6	4600	4601.....461F	4620.....463F	46E0	46E1	46FF	
	7	4700	4701.....471F	4720.....473F	47E0	47E1	47FF	
		CHAR.	CHAR.	CHAR.		CHAR.	CHAR.		CHAR.	
		POS.	POS.	POS.		POS.	POS.		POS.	
		0/0	0/1	0/31		7/0	7/1		7/31	

		32 BYTES			32 BYTES			32 BYTES		
		LINE 8			LINE 9			LINE 15		
B L O C K 1	Scan 0	4800	4801.....481F	4820.....483F	48E0	48E1	48FF	
	1	4900	4901.....491F	4920.....493F	49E0	49E1	49FF	
	2	4A00	4A01.....4A1F	4A20.....4A3F	4AE0	4AE1	4AFF	
	3	4B00	4B01.....4B1F	4B20.....4B3F	4BE0	4BE1	4BFF	
	4	4C00	4C01.....4C1F	4C20.....4C3F	4CE0	4CE1	4CFF	
	5	4D00	4D01.....4D1F	4D20.....4D3F	4DE0	4DE1	4DFF	
	6	4E00	4E01.....4E1F	4E20.....4E3F	4EE0	4EE1	4EFF	
	7	4F00	4F01.....4F1F	4F20.....4F3F	4FE0	4FE1	4FFF	
		CHAR.	CHAR.	CHAR.		CHAR.	CHAR.		CHAR.	
		POS.	POS.	POS.		POS.	POS.		POS.	
		8/0	8/1	8/31		15/0	15/1		15/31	

		32 BYTES			32 BYTES			32 BYTES		
		LINE 16			LINE 17			LINE 23		
B L O C K 2	Scan 0	5000	5001.....501F	5020.....503F	50E0	50E1	50FF	
	1	5100	5101.....511F	5120.....513F	51E0	51E1	51FF	
	2	5200	5201.....521F	5220.....523F	52E0	52E1	52FF	
	3	5300	5301.....531F	5320.....533F	53E0	53E1	53FF	
	4	5400	5401.....541F	5420.....543F	54E0	54E1	54FF	
	5	5500	5501.....551F	5520.....553F	55E0	55E1	55FF	
	6	5600	5601.....561F	5620.....563F	56E0	56E1	56FF	
	7	5700	5701.....571F	5720.....573F	57E0	57E1	57FF	
		CHAR.	CHAR.	CHAR.		CHAR.	CHAR.		CHAR.	
		POS.	POS.	POS.		POS.	POS.		POS.	
		16/0	16/1	16/31		23/0	23/1		23/31	

ATTRIBUTE FILE:

BLOCK 0	LINE 0 5800.....581F	LINE 1 5820.....583F	LINES 2 - 6 5840.....58DF	LINE 7 58E0.....58FF
BLOCK 1	LINE 8 5900.....591F	LINE 9 5920.....593F	LINES 10-14 5940.....59DF	LINE 15 59E0.....59FF
BLOCK 2	LINE 16 5A00.....5A1F	LINE 17 5A20.....5A3F	LINES 18-22 5A40.....5ADF	LINE 23 5AE0.....5AFF

2.1.11 Video Generation

2.1.11.1 Composite Video

The U, V, and \bar{Y} signals from the SCLD are supplied to the LM1889 and associated circuitry to produce composite video and modulated RF. This circuitry produces color vectors at approximately the following angles:

PHASE	TS 2068 (Degrees)	NTSC STANDARD (Degrees)
Blue	350	350
Magenta	64	62
Red	116	112
Green	242	240
Cyan	284	284
Yellow	170	170
Reference	224	180

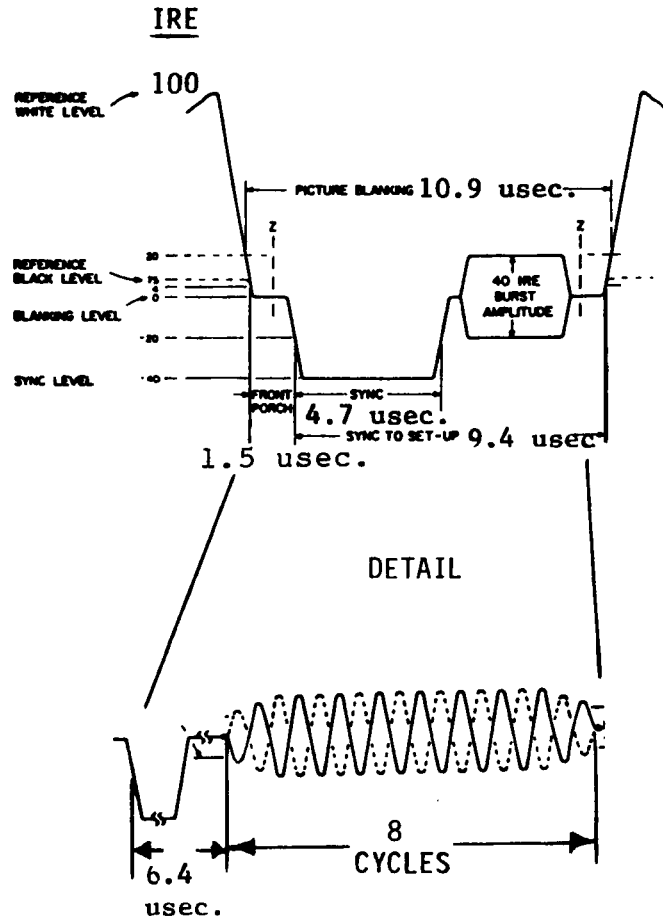
The Front Porch, Sync Pulse, Back Porch, and Color Burst portions of the composite video signal are illustrated in Figure 2.1.11-1. In proper adjustment the following should be observed:

Sync Pulse = 40 +/- 2 IRE units
Color Burst = 35 to 45 IRE units
Color Burst Freq. = 3.579545 MHz. +/- 70 Hz

The following three facts may aid in understanding problems with certain monitors.

1. The color burst is not synchronous with the waveform since it is generated from the 3.579545 MHz crystal and the waveform is derived from the 14.112 MHz crystal. The result is observed ripples at color boundaries, e.g. green to magenta.
2. The color burst duration is 8 cycles while standard TV broadcast stations provide 9 cycles. This "short" burst is a problem for some monitors.
3. The color burst starts 6.4 microseconds from the leading edge of sync. Many monitors are designed to expect this start as early as 5.3 microseconds, thus these monitors may not produce color when attached to the TS 2068.

FIGURE 2.1.11-1
COMPOSITE VIDEO SIGNALS



2.1.11.2 RF Modulator

The composite video information is used to AM modulate the selected channel frequency via the LM1889 and associated Channel 2/3 tank circuitry. The modulated output is filtered through the output filter network to reduce harmonic generation to comply with FCC requirements. The RF circuitry is physically contained inside the RF-can at the rear left corner of the PCB (at the RF output jack). 75 ohms is the output impedance.

2.1.12 Cassette I/O

See Sections 2.1.13.2, 2.4.3 and 4.2.

2.1.13 Port Map

Table 2.1.13-1 summarizes the I/O addressing of ports utilized by the TS 2068. Details of the data bits of each of these ports is provided by the following sections.

2.1.13.1 Display Enhancement Control (Port FFH)

The display enhancement control register within the SCLD controls:

- a) Selection of Enhanced Video Modes
- b) Ink selection for 64-Column Mode
- c) Enable/Inhibit the 17 ms interruption to the Z80
- d) Selection of Extension ROM or Cartridge (see Section 2.1.8.1)

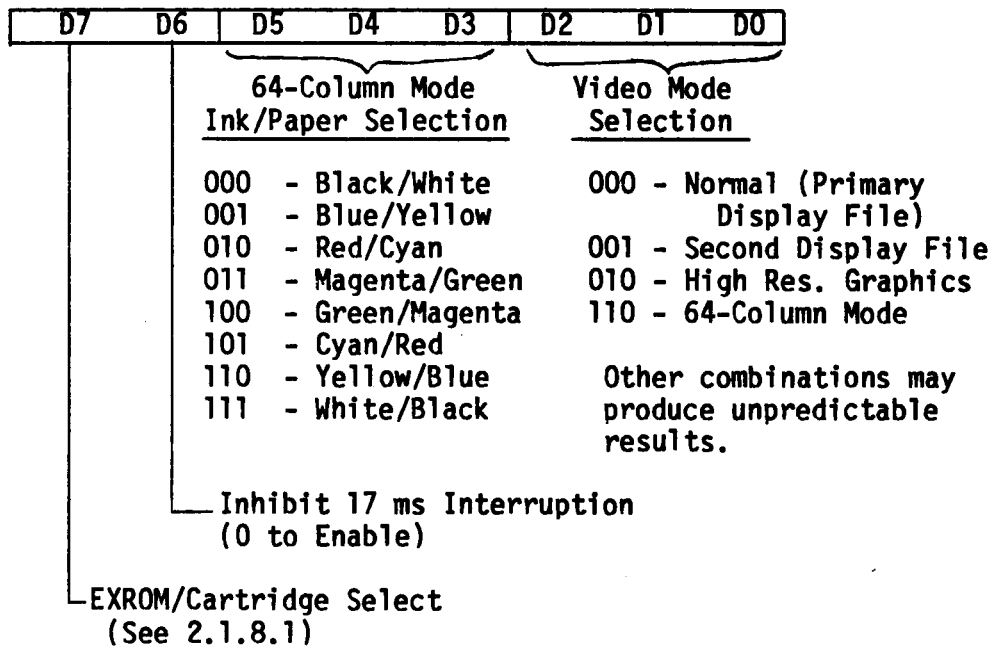


TABLE 2.1.13-1

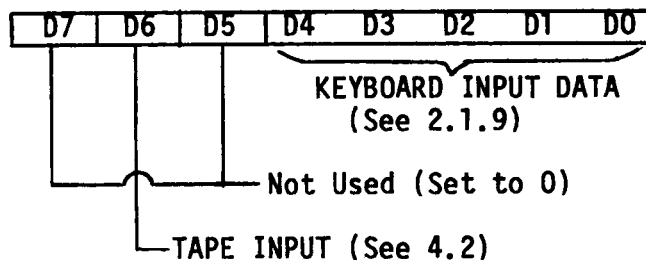
I/O PORT MAP

FUNCTION	PORT ADDRESS			OPERATION	REFERENCE
	(HEX)	(DECIMAL)	(BINARY)		
Display Enhancement Control	FF	255	11111111	R/W	2.1.10, 2.1.13.1, 3.2.2.3, 5.2
Keyboard/Tape I/O	FE	254	11111110	R/W	2.1.9, 2.1.13.2, 2.4.3, 4.1.1, 4.2
Reserved	FD	253	11111101		
Reserved	FC	252	11111100		
TS 2040 Printer	FB	251	11111011	R/W	2.1.13.3, 4.1.3
Sound Chip & Joystick Data	F6	246	11110110	R/W	2.1.6, 2.1.7, 2.1.13.4, 2.4.4, 4.3, 4.5
Sound Chip Address	F5	245	11110101	W	Same
Horizontal Select Register	F4	244	11110100	R/W	2.1.8.1

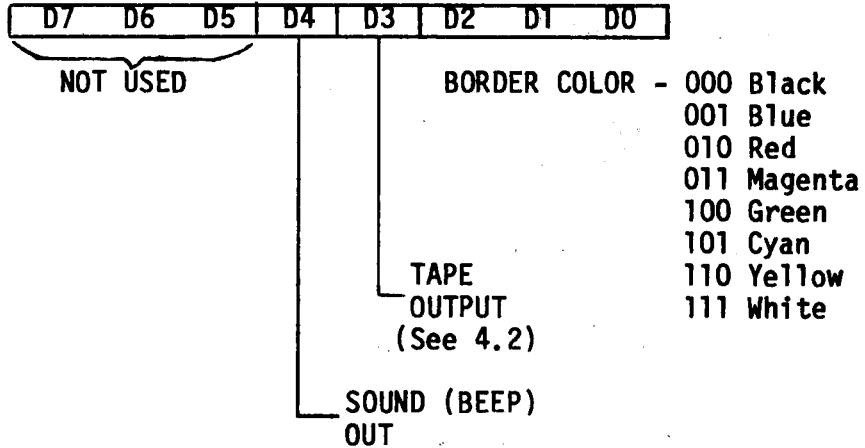
2.1.13.2 Keyboard/Tape I/O (Port FEH)

Port FEH is used to input Keyboard and Tape data and to output Border color, Tape data, and Sound (BEEP) tones.

READ (IN)



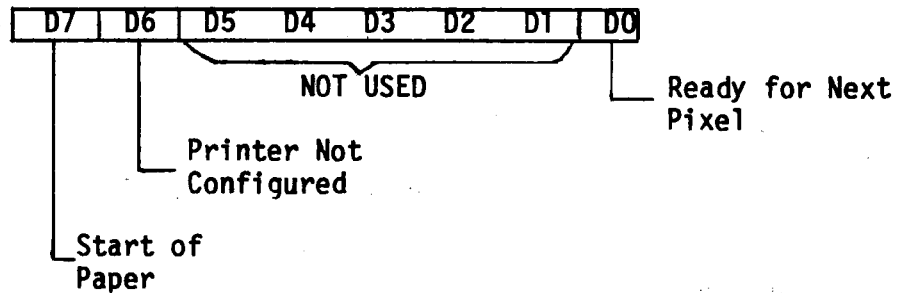
WRITE (OUT)



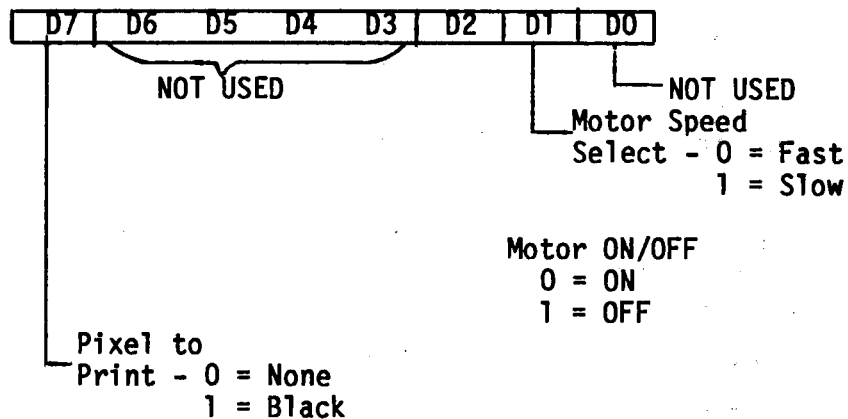
2.1.13.3 TS 2040 Printer (Port 1XXXXOXX)

The TS 2040 Printer peripheral is written to and status read from via OUT and IN instructions with Bit 7 = 1 and Bit 2 = 0 (other bits are not decoded by the printer).

READ (IN)



WRITE (OUT)



2.1.13.4 Sound Chip & Joystick (Ports F5H and F6H)

Ports F5H and F6H are used to control and access the Sound Generator and the Joysticks. Details of the registers available via these ports is contained in Sections 2.1.6 and 2.1.7.

2.1.13.5 Horizontal Select Register (Port F4H)

The HSR addressed via Port F4H is used in the control of the Bank Switching logic as detailed in Section 2.1.8. Each bit, when set, enables the corresponding 8K memory "chunk" in either the Dock Bank (Port FF, Bit 7=0) or the Extension ROM Bank (Port FF, Bit 7=1). The HSR must be set to all zeroes in order to enable the entire Home Bank.

2.2 Schematic Diagram

Appendix D contains a detailed schematic diagram of the TS 2068.

2.3 Unit Absolute Ratings

FUNCTION	DESCRIPTION	MIN	MAX
TS	Storage Temperature	-40°C	+65°C
VAC	AC Line Voltage	105V	130V
Ta	Operating Ambient Temp	0°C	40°C
Vin	Voltage on any Logic Pin	-0.3V	+5.3V
Vin (EAR)	EAR input Peak AC	-2.0V	+5.0V
Vdc (IN)	Input DC Voltage	14.75V	26V

2.4 Interfaces and Connectors

The TS2068 has a number of specialized interfaces that are accessible via the following connectors:

CONNECTOR	TYPE	LOCATION
System Bus	2X32 Card Edge	Right Rear
Cartridge	2X18 Card Edge	Under TCC door
MIC	1/8" Mini Phone	Rear
EAR	1/8" Mini Phone	Rear
Player 1 Joystick	9-pin "D"	Left Side
Player 2 Joystick	9-pin "D"	Right Side
Monitor	RCA Phono	Rear
TV	RCA Phono	Rear
Keyboard	14-pin SIP	Inside-Left Rear
AC Adapter		Rear

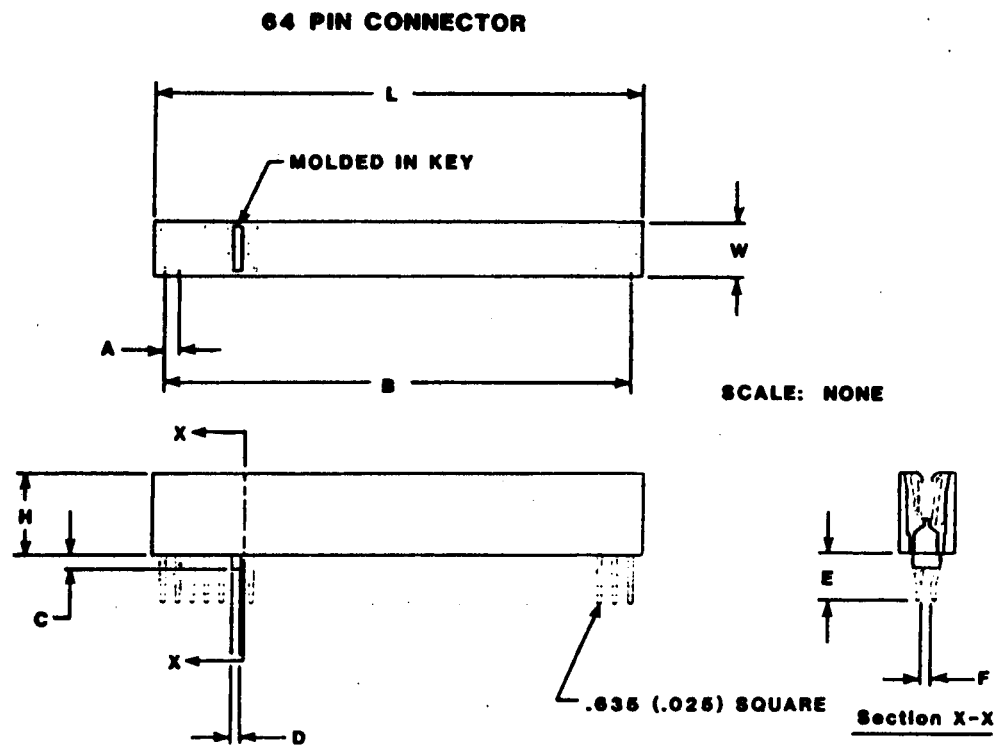
2.4.1 System Bus Connector - P1

The TS2068 provides a 2 X 32 pin connector, which is designated as P1, at the right rear corner of the console. The mechanical, functional, and electrical requirements of the system buss connector are detailed in the following tables and figures:

FIGURE/TABLE	TITLE
Figure 2.4.1-1 P1	Mating Connector Mechanical Requirements
Figure 2.4.1-2 P1	Signal Layout
Table 2.4.1 - 1 P1	Signal Definition
Table 2.4.1 - 2 P1	Signal Electrical Characteristics

FIGURE 2.4.1-1

P1 MATING CONNECTOR MECHANICAL REQUIREMENTS



SPECIFICATIONS:

LTR	DIMENSION *
L	82.55 (3.25)
W	9.525 ± 0.127 (.375 ± .005)
H	13.97 ± 0.254 (.550 ± .010)
A	2.54 (.100)
B	31 EQUAL SPACES AT 2.54 (.100) = 78.74 (3.100)
C	2.54 (.10)
D	1.727 (.068) MAX
E	8.382 ± 0.508 (.330 ± .020)
F	FOR 1.575 (.062) BOARD

*All dimensions are in millimeters, dimensions shown (X.X) are in inches.

NOTES:

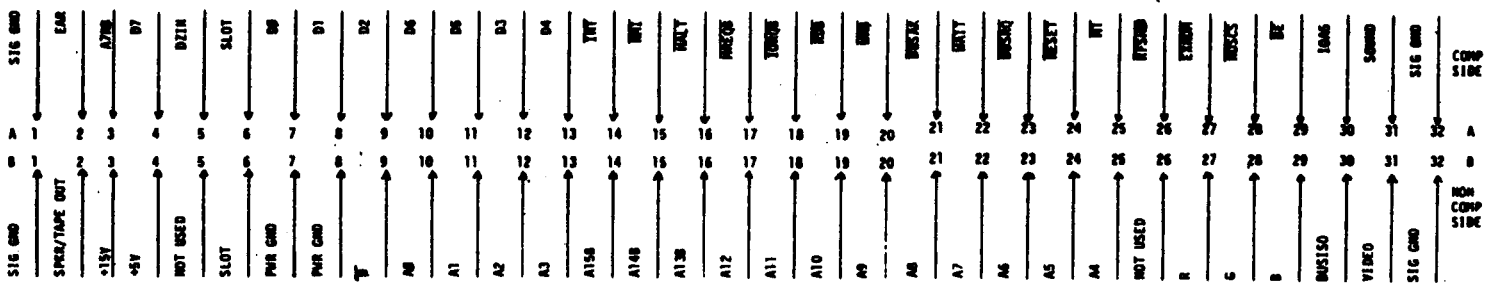
1. **INSULATOR MATERIAL:** Insulator body shall be 30% glass-filled polyester and shall meet UL94V-0 requirements.
2. **CONTACT MATERIAL:** Contact material shall be phosphor bronze.
3. **CONTACT FINISH:** Contacts shall be selectively plated with gold, 0.00038 (.00015) thick over nickel on contact surfaces.
4. **INSERTION FORCE:** Insertion forces shall be 170.1-283.5 grams (6-10 oz) per contact pair using a 1.575 (.062) flat steel test blade.
5. **WITHDRAWAL FORCE:** Withdrawal forces shall be 226.8-340.2 grams (8-12 oz) per contact pair using a 1.575 (.062) flat test blade.
6. **NORMAL FORCE:** Normal force shall be 85.05 grams (3 oz) minimum when mated with a 1.37 (.054) thick test board.
7. **PURCHASE FROM:** San Diego Microtronics INC. San Diego, CA 92123.

FIGURE 2.4.1-2

P1 CONNECTOR SIGNAL LAYOUT

COMPONENT SIDE

TS1000 COMPATIBLE



NON-COMPONENT SIDE

(VIEW FROM FRONT OF COMPUTER)

TABLE 2.4.1 - 1

P1 SIGNAL DEFINITION

PIN #	SIGNAL NAME	DESCRIPTION
1A	GND	Signal Ground
1B	GND	Signal Ground
2A	EAR	EAR Input
2B	SPKR/TAPE OUT	Speaker/Tape Output
3A	A7RB	Refresh Address Bit 7 Buffered
3B	+15V	+15 Volts DC
4A	D7	Data Bus Bit 7
4B	+5V	+5 Volts
5A	DZIN	Daisy In (Not Connected)
5B	Not Used	--
6A	Slot	--
6B	Slot	--
7A	D0	Data Bus Bit 0
7B	GND	Power Ground
8A	D1	Data Bus Bit 1
8B	GND	Power Ground
9A	D2	Data Bus Bit 2
9B	0	CPU Clock (Inverted)
10A	D6	Data Bus Bit 6
10B	A0	Address Bus Bit 0
11A	D5	Data Bus Bit 5
11B	A1	Address Bus Bit 1
12A	D3	Data Bus Bit 3
12B	A2	Address Bus Bit 2
13A	D4	Data Bus Bit 4
13B	A3	Address Bus Bit 3
14A	INT	Interrupt Request (Active Low)
14B	A15B	Address Bus Bit 15, Buffered
15A	NMI	Non-Maskable Int.(Active Low)
15B	A14B	Address Bus Bit 14, Buffered
16A	HALT	CPU HALT Indicator (Active Low)
16B	A13B	Address Bus Bit 13, Buffered
17A	MREQB	Memory Request (Active Low),Bfrd.
17B	A12	Address Bus Bit 12
18A	IORQB	I/O Request (Active Low), Bfrd.
18B	A11	Address Bus Bit 11
19A	RDB	Read (Active Low), Buffered
19B	A10	Address Bus Bit 10
20A	WRB	Write (Active Low), Buffered
20B	A9	Address Bus Bit 9
21A	BUSAK	Bus Acknowledge (Active Low)
21B	A8	Address Bus Bit 8
22A	WAIT	CPU WAIT (Active Low)
22B	A7	Address Bus Bit 7
23A	BUSRQ	Bus Request (Active Low)
23B	A6	Address Bus Bit 6
24A	RESET	CPU Reset (Active Low)
24B	A5	Address Bus Bit 5
25A	M1	CPU M1 State (Active Low)
25B	A4	Address Bus Bit 4
26A	RFSHB	Refresh (Active Low),Buffered
26B	DZOUT	Daisy Out (Not Connected)
27A	EXROM	Extension ROM Enable (Active Low)
27B	R	Color Signal - Red
28A	ROSCS	ROS Chip Select (Active Low) (Dock Bank Enable)
28B	G	Color Signal - Green
29A	BE	Bank Enable (Active Low)
29B	B	Color Signal - Blue
30A	IOAS	
30B	BUSISO	
31A	SOUND	Analog Sound Signal Output(0-5V)
31B	VIDEO	Composite Video Signal Output
32A	GND	Signal Ground
32B	GND	Signal Ground

NOTE: All A Pins are on component side of board
All B Pins are on non-component (soldering) side of board

TABLE -2.4.1-2

P1 SIGNAL ELECTRICAL CHARACTERISTICS

MNEMONIC	----- OUTPUTS FROM TS2068 -----				----- INPUTS TO TS2068 -----			
	CAPACITIVE LOADING MAX (PF)	V(OL) MAX VOLTS	I(LOAD) MAX (MA)	V(OH) MIN VOLTS	V(IL) MAX VOLTS	V(IH) MIN VOLTS	I IN (MAX) μ A	INPUT CAPACITIVE LOADING MAX (PF)
A15B	30	0.5	1.8	2.4	0.8	2.0	1800	40
A14B	30	0.5	1.8	2.4	0.8	2.0	1800	40
A13B	30	0.5	1.8	2.4	0.8	2.0	1800	40
A12	30	0.4	1.8	2.4	0.8	2.0	1800	74
A11	30	0.4	1.8	2.4	0.8	2.0	1800	74
A10	30	0.4	1.8	2.4	0.8	2.0	1800	74
A9	30	0.4	1.8	2.4	0.8	2.0	1800	76
A8	30	0.4	1.8	2.4	0.8	2.0	1800	76
A7	30	0.4	1.8	2.4	0.8	2.0	1800	72
A6	30	0.4	1.8	2.4	0.8	2.0	1800	72
A5	30	0.4	1.8	2.4	0.8	2.0	1800	72
A4	30	0.5	1.8	2.4	0.8	2.0	1800	72
A3	30	0.4	1.8	2.4	0.8	2.0	1800	72
A2	30	0.4	1.8	2.4	0.8	2.0	1800	72
A1	30	0.4	1.8	2.4	0.8	2.0	1800	72
A0	30	0.4	1.8	2.4	0.8	2.0	1800	98
A7RB	30	0.5	0.35	2.7	0.8	2.0	----	120
IORQB	30	0.5	12	2.4	0.8	2.0	20	10
WRB	30	0.5	12	2.4	0.8	2.0	20	10
RFSHB	30	0.5	12	2.4	0.8	2.0	20	10
EXROM	30	0.5	12	2.4	---	---	----	--
ROSCS	30	0.5	12	2.4	---	---	----	--
MREQB	30	0.5	12	2.4	0.8	2.0	20	10
RDB	30	0.5	12	2.4	0.8	2.0	20	10
MI	30	0.4	1.8	2.4	0.8	2.0	20	10
BE	--	---	---	---	0.8	2.0	10	12
BUSAK	30	0.4	1.8	2.4	---	---	----	--
WAIT	--	---	---	---	0.8	2.0	----	10
HALT	30	0.4	1.8	2.4	0.8	2.0	----	10
NMI	--	---	---	---	0.8	2.0	----	10
INT	----- OPEN COLLECTOR WITH PULL-UP -----							
R	50	0.4	1.8	2.4	---	---	----	--
G	50	0.4	1.8	2.4	---	---	----	--
B	50	0.4	1.8	2.4	---	---	----	--
VIDEO	----- TO 75 ohm COAX -----							
D0	30	0.4	1.8	2.4	0.8	2.0	20	120
D1	30	0.4	1.8	2.4	0.8	2.0	20	120
D2	30	0.4	1.8	2.4	0.8	2.0	20	120
D3	30	0.4	1.8	2.4	0.8	2.0	20	120
D4	30	0.4	1.8	2.4	0.8	2.0	20	120
D5	30	0.4	1.8	2.4	0.8	2.0	20	120
D6	30	0.4	1.8	2.4	0.8	2.0	20	120
D7	30	0.4	1.8	2.4	0.8	2.0	20	120
SPKP/TAPE OUT	500	0.2	0.04	0.3-0.5	---	---	----	--
EAR	15	0.5	1.6	2.4	+/- 1.3	+/- 5.0	----	--
SOUND	100	0	---	2.5	-0.3	+5.0	----	--
BUSRQ	--	---	---	---	0.8	2.0	----	10
RESET	----- 1 μ F WITH 220K PULL-UP -----							
IOA5	--	---	---	---	---	---	----	--

2.4.1.1 Attachment of an RGB Monitor

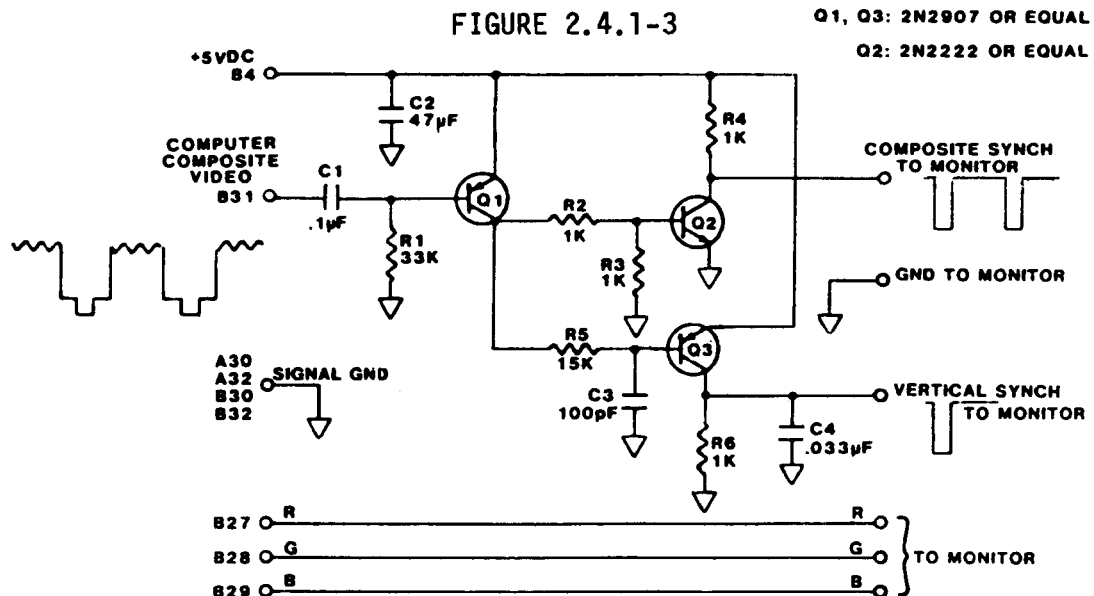
The TS 2068 provides via the P1 rear-edge connector the ability to attach an RGB monitor for excellent picture clarity and resolution. The TTL-level logic signals appear directly on the rear-edge connector of the TS 2068 -- the necessary synch signals can be derived from the simple synch stripper/separator circuit described here.

The Schematic of Figure 2.4.1-3 shows the required connections and electronics. Attachment is via the 64-pin keyed P1 connector. Shielding should not normally be required, but ferrite beads are recommended on each wire to minimize EMI, TVI, etc.

Circuit Operation - R1 and the base-emitter junction of Q1 operate as a DC restoration circuit with current flowing only when the composite video input signal from connector pin B31 is at the synch level. With the charge maintained on C1, Q1 conducts only during the synch pulse interval (not during the color burst time). During this conduction interval, the composite synch signal appears in inverted form on the collector of Q1. The Q2 stage simply re-inverts the signal, providing at its collector a composite synch signal for the connected monitor.

To provide a separated Vertical synch pulse, R5 and C3 filter the output of Q1 to partially eliminate the Horizontal synch pulses which are shorter than the Vertical synch pulses. The partially filtered inverted signal is re-inverted by Q3, then R6 and C4 complete the elimination of the Horizontal synch pulses so that a separate Vertical synch pulse is supplied for the attached monitor.

Signals R, G, and B from connector pins B27, B28, and B29 can be supplied directly to the attached monitor.



2.4.2 Cartridge Connector - J4

The TS2068 provides a 2 X 18 pin connector (designated J4 on the schematic) under the door at the front right of the console. The table and figures listed below detail the mechanical, functional, and electrical requirements and limits of the J4 Cartridge Connector.

FIGURE/TABLE	TITLE
Figure 2.4.2-1	J4 Mating PCB Mechanical Requirements
Figure 2.4.2-2	J4 Signal Layout
Table 2.4.2-1	J4 Signal Definition
Table 2.4.2-2	J4 Signal Electrical Characteristics

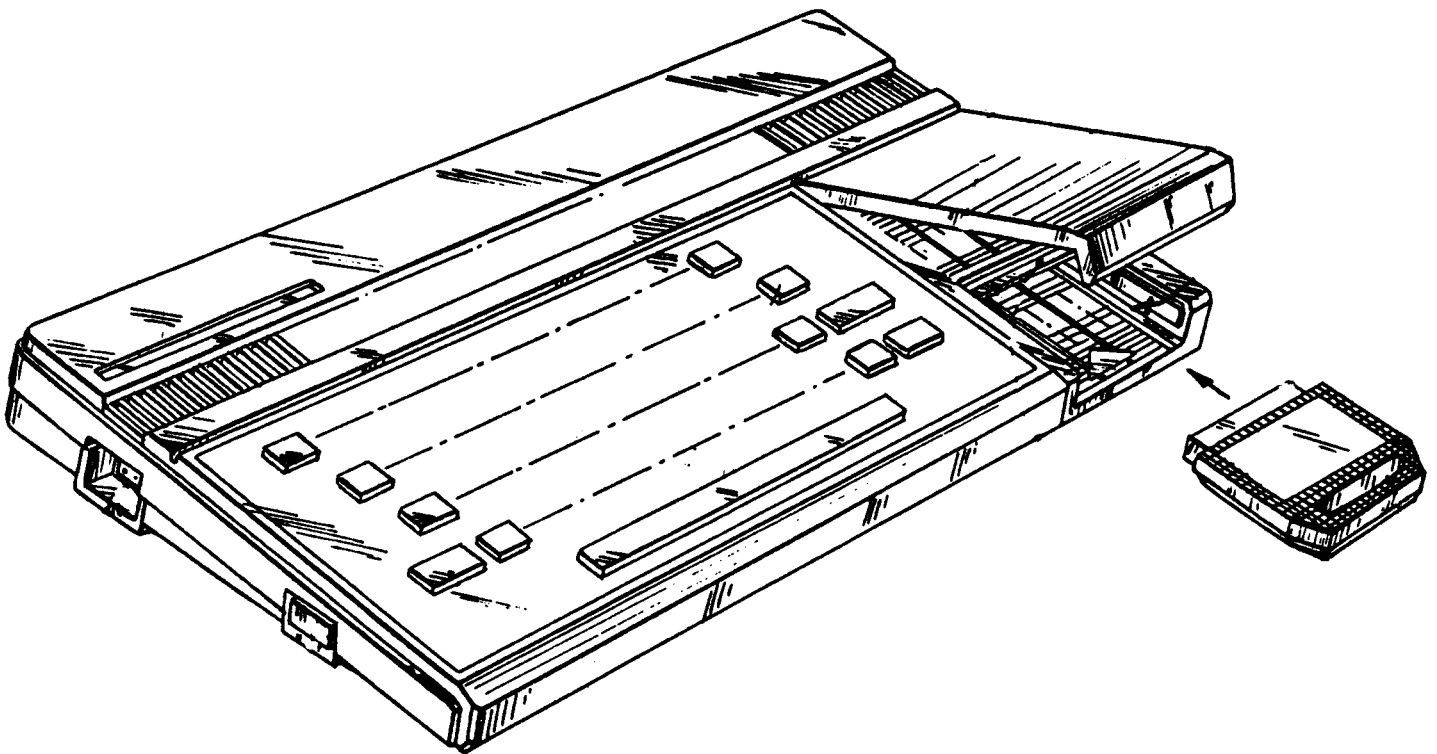
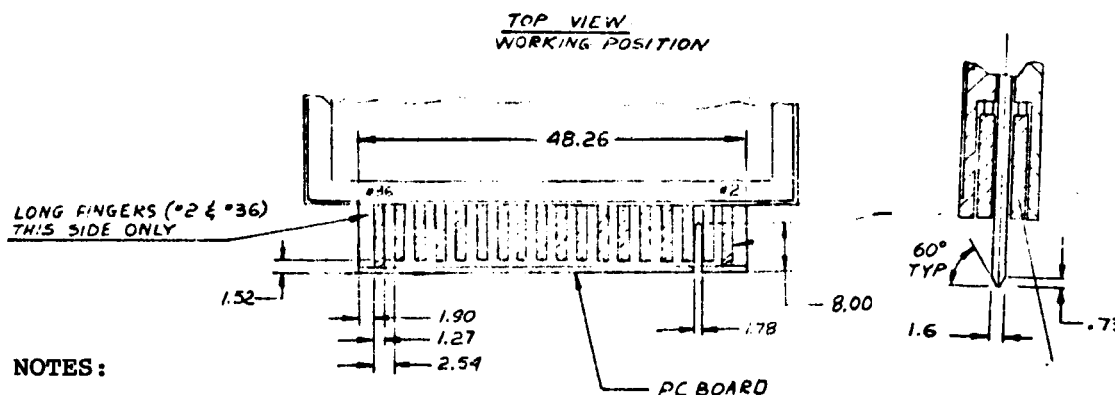


FIGURE 2.4.2-1

J4 MATING PCB MECHANICAL REQUIREMENTS



NOTES:

- (1) Circuit Board Material:
 FLGPN C62
 C1/1A2A (94V-0)
 Copper 1 or 2 sides
- (2) Contact Fingers: Min. 10
 millionth MIL-G - 45204 Gold over
 .00005 to .00010 inch low stress
 nickel.
- (3) Contact Fingers 2 and 36
 should be longer than other
 fingers to latch-up when inserted
 with power on.

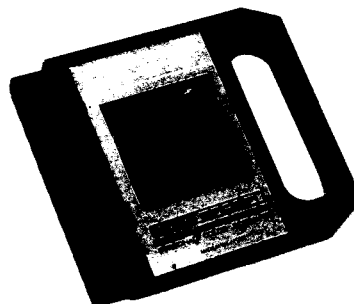


FIGURE 2.4.2-2

J4 SIGNAL LAYOUT

(View from Front)

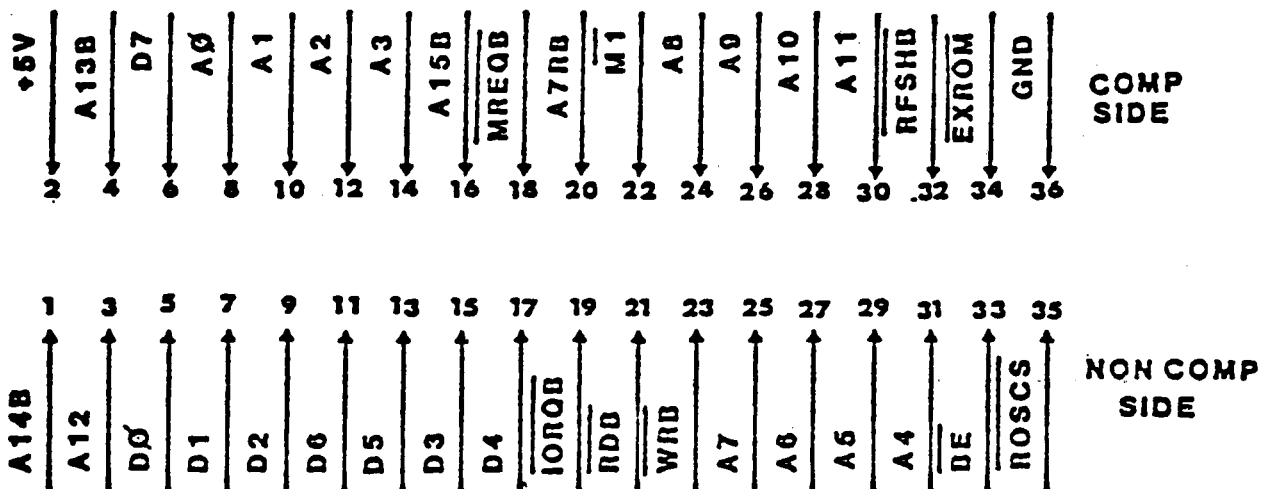


TABLE 2.4.2-1

J4 CONNECTOR SIGNAL DEFINITIONS

PIN #	SIGNAL NAME	DESCRIPTION
1	A14B	Address Bus Bit 14, Buffered
2	+5V	+5 volts DC
3	A12	Address Bus Bit 12
4	A13B	Address Bus Bit 13, Buffered
5	D0	Data Bus Bit 0
6	D7	Data Bus Bit 7
7	D1	Data Bus Bit 1
8	A0	Address Bus Bit 0
9	D2	Data Bus Bit 2
10	A1	Address Bus Bit 1
11	D6	Data Bus Bit 6
12	A2	Address Bus Bit 2
13	D5	Data Bus Bit 5
14	A3	Address Bus Bit 3
15	D3	Data Bus Bit 3
16	A15B	Address Bus Bit 15, Buffered
17	D4	Data Bus Bit 4
18	MREQB	Memory Request (Active Low), Bfrd.
19	IORQB	I/O Request (Active Low), Buffered
20	A7RB	Refresh Address Bit 7, Buffered
21	RDB	Read (Active Low), Buffered
22	MT	CPU M1 State (Active Low)
23	WRB	Write (Active Low), Buffered
24	A8	Address Bus Bit 8
25	A7	Address Bus Bit 7
26	A9	Address Bus Bit 9
27	A6	Address Bus Bit 6
28	A10	Address Bus Bit 10
29	A5	Address Bus Bit 5
30	A11	Address Bus Bit 11
31	A4	Address Bus Bit 4
32	RFSHB	Refresh (Active Low), Buffered
33	BE	Bank Enable (Active Low)
34	EXROM	Extension ROM Enable (Active Low)
35	ROSCS	ROS Chip Select (Active Low) (Dock Bank Enable)
36	GND	Ground

TABLE 2.4.2-2

J4 SIGNAL ELECTRICAL CHARACTERISTICS

MNEMONIC	----- OUTPUTS FROM TS2068 -----				----- INPUTS TO TS2068 -----				
	CAPACITIVE LOADING MAX (PF)	V(OL) MAX VOLTS	I(LOAD) MAX (MA)	V(OH) MIN VOLTS	I(LOAD)*V(IL) MIN (uA)	V(IH) MIN VOLTS	I IN (MAX) μ A	INPUT CAPACITIVE LOADING MAX (PF)	
A15B	30	0.5	1.8	2.4	10				
A14B	30	0.5	1.8	2.4	10				
A13B	30	0.5	1.8	2.4	10				
A12	30	0.4	1.8	2.4	10				
A11	30	0.4	1.8	2.4	10				
A10	30	0.4	1.8	2.4	10				
A9	30	0.4	1.8	2.4	10				
A8	30	0.4	1.8	2.4	10				
A7	30	0.4	1.8	2.4	10				
A6	30	0.4	1.8	2.4	10				
A5	30	0.4	1.8	2.4	10				
A4	30	0.4	1.8	2.4	10				
A3	30	0.4	1.8	2.4	10				
A2	30	0.4	1.8	2.4	10				
A1	30	0.4	1.8	2.4	10				
A0	30	0.4	1.8	2.4	10				
A7RB	30	0.5	0.35	2.7					
ROSCS	30	0.4	1.8	2.4	10				
MREQB	30	0.5	1.8	2.4	10				
RDB	30	0.5	1.8	2.4	10				
TORQB	30	0.5	12	2.4	10				
WRB	30	0.5	12	2.4	10				
RFSHB	30	0.5	12	2.4	10				
EXROM	30	0.5	12	2.4	10				
MI	30	0.5	12	2.4	10				
D0	30	0.4	1.8	2.4		0.8	2.0	15	120
D1	30	0.4	1.8	2.4		0.8	2.0	15	120
D2	30	0.4	1.8	2.4		0.8	2.0	15	120
D3	30	0.4	1.8	2.4		0.8	2.0	15	120
D4	30	0.4	1.8	2.4		0.8	2.0	15	120
D5	30	0.4	1.8	2.4		0.8	2.0	15	120
D6	30	0.4	1.8	2.4		0.8	2.0	15	120
D7	30	0.4	1.8	2.4		0.8	2.0	15	120
Vcc (+5V)	--	5.25	300	4.75					
GND	--	--	---	---					

2.4.3 Cassette I/O

The EAR and MIC connectors provided on the rear of the TS2068 are 1/8" mini-phone jacks requiring 1/8" mini-phone plugs as mating connectors.

The MIC output is filtered by a low-pass filter with a breakpoint of 2.5KHz and provides a signal output of 0.15 to 0.67 V p-p.

The EAR input is filtered by a low-pass filter with a breakpoint of 23 KHz. Input voltages should be between 4.0 and 10.0 V p-p.

2.4.4 Joystick

The joystick input connectors, one on each side of the TS2068 case, are standard 9-pin "D" type connectors for use with 5-switch type joysticks.

Connector layout and the function of each pin is given in Figure 2.4.4-1 and Table 2.4.4-1, respectively.

FIGURE 2.4.4-1

JOYSTICK CONNECTOR

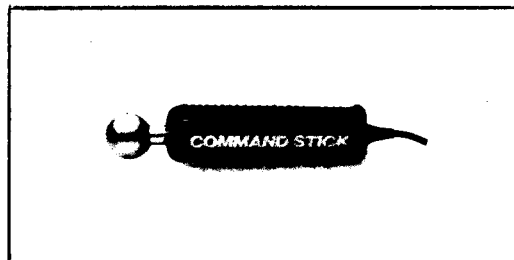
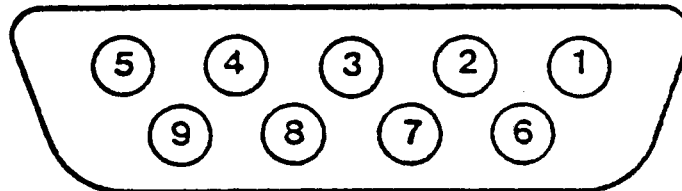


TABLE 2.4.4-1

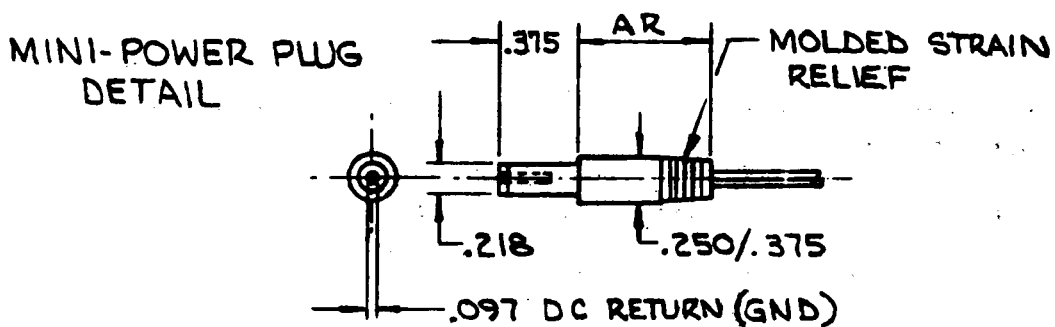
JOYSTICK CONNECTOR SIGNAL ASSIGNMENT

P/N	SIGNAL NAME	I/O PORT BIT	FUNCTION
1	DIR1	0	STICK UP
2	DIR2	1	STICK DOWN
3	DIR3	2	STICK LEFT
4	DIR4	3	STICK RIGHT
5	---	----	not used
6	BUTTON	7	PUSH BUTTON
8	5V	---	5 VOLT POWER
8	READ STROBE	---	ADDRESS BIT 8 OR 9*
9	GND	---	POWER GROUND

*When Address Bit 8 is high, the READ strobe to the left joystick is driven low. When address Bit 9 is high, the READ strobe to the right joystick is driven low.

2.4.5 AC Adapter Power Plug

The AC Adapter provided with the TS 2068 provides unregulated DC to the unit as described in Section 2.1.1 Mechanical details of the plug which mates to the TS 2068 are shown below:



2.4.6 Composite Monitor Output

The MONITOR output on the rear of the TS2068 provides a 1 V p-p (+/- 20%) composite color video signal output to an RCA phono jack which is mated by a standard phono plug into a 75 ohm coax cable. See Section 2.1.11.1.

2.4.7 RF Output

The TV output on the rear of the TS2068 provides a modulated color video signal on VHF Channel 2 or Channel 3 as selected by the channel select switch on the bottom of the unit. Connection to the RCA phono jack output should be via a standard phono plug and 75 ohm coax cable. See Section 2.1.11.2.

Channel frequencies provided are

Channel 2 55,250 +/- 100 KHz
Channel 3 61,250 +/- 100 KHz

Output levels are less than 3 milliwatts as limited by the Federal Communications Commission.

2.4.8 Keyboard Interface - J9 Connector

Located on the PCB inside the TS 2068 is a 14-pin single-in-line flex cable connector (AMP TRIO-MATE P/N 1-520315-4 or equivalent). Signals are as listed below:

<u>PIN</u>	<u>SIGNAL</u>
0	GND
1	KB0
2	KB1
3	KB2
4	KB3
5	KB4
6	CR6/A11
7	CR7/A10
8	CR8/A9
9	CR9/A12
10	CR10/A13B
11	CR11/A8
12	CR12/A14B
13	CR13/A15B

Any modification to or replacement of the keyboard supplied must consider the following:

- (1) Contact resistance less than 200 ohms.
- (2) Bounce less than 10 ms.
- (3) Capacitance per line less than 20 pF (0 or 1 key depressed); less than 40 pF (more than 1 key depressed).

3.0 SYSTEM SOFTWARE GUIDE

3.1 Identifier

Location 19 (13H) of the Home Bank ROM is used to identify the revision level of the System Software. The initial version is identified by this location having a value of 255 (FFH). Any subsequent versions will decrement this value by 1, e.g., the first revision would be identified by a value of 254 (FEH). This identifier should be used to conditionally apply patches or execute "work-arounds" identified as necessary with a particular version of the System Software.

3.2 ROM Organization and Services

3.2.1 Home ROM

3.2.1.1 Fixed Entry Points

Home ROM Location 0 is the entry to the system initialization code upon power-up (Ref. Figure 1.1-4). Locations 8 through 48 (8H through 30H) are the Z80 RESTART entry points for the following functions:

RESTART	FUNCTION
8	ERROR - Error exit from BASIC (Address on Stack points to Error Number)
16	WRCH - Write Character (Code in A) to Current Output Channel as established by SELECT (Address of output routine pointed to by System Variable CURCHL). (See Section 4.0).
24	IGN_SP - Return in A the current significant character in the Program Line (Address in System Variable CH_ADD) skipping over spaces and control characters except End-of-Line (ODH=ENTER)

- 32 NXT_IS - Like IGN_SP but returns
 in A the Next Significant
 Character.

- 40 CALCTR - Entry to Calculator
 Routines .

- 48 COPYUP - Make room for BC Bytes
 of temporary workspace just
 before address in System Variable
 STKBOT by copying up memory
 between there and the address in
 STKEND, adjusting affected
 pointers. Returns DE=1st Byte of
 Space; HL=Last.

Location 56 (38H) is the entry to service the hardware generated interruption which occurs approximately every 1/60 of a second (16.67 ms). Z80 Int. Mode 1 is used. This interruption is used to scan the keyboard (call to routine UPD K - see Section 4.1.1). It is also used to update the Frame Counter (3 bytes pointed to by the System Variable FRAMES) used by the RANDOMIZE instruction.

Location 102 (66H) is the entry point for the NMI interruption, but this interruption is not used in the TS2068 design. (See Section 2.1.3.8 NMI Interruption.)

3.2.1.2 BASIC AROS Support

BASIC Application Cartridges are supported by special code in the Home ROM. A program line is copied from the cartridge to a buffer in the Home RAM (ARSBUF) and is then executed from there by the BASIC Interpreter. When a READ command is executed, the line containing the appropriate DATA statement is also copied from the cartridge to the RAM. The cartridge memory is enabled only for search and copy operations for both program lines and DATA statements, and when executing a USR function, otherwise the entire Home Bank is enabled while executing in the BASIC Interpreter. There is no support for User-Defined Functions which insert the expanded definition parameters directly into the program and then require search of the program area to find these parameters whenever a function is invoked.

See Section 5.1, Cartridge Software/Hardware, for additional details on BASIC AROS.

3.2.1.3 General

The balance of the Home ROM contains the BASIC Interpreter and standard I/O routines with the exception of the cassette I/O which is in the Extension ROM. The bit map table for the standard character set is located at the end of the Home ROM from location 15616 to 16383 (3D00H to 3FFFH). The address of this table minus 256 (100H) is contained in the System Variable CHARS (=3C00H).

The Home ROM routines accessible via the Function Dispatcher are described in Table 3.3.4-2. See Appendix A for the ROM Maps giving the ROM addresses of these routines.

3.2.2 Extension ROM

3.2.2.1 Fixed Entry Points

Extension ROM Location 0 contains code to pass control to the initialization code in the Home ROM. (Figure 1.1-4).

Extension ROM Location 56 (38H) is the interruption fielder. Control is passed to the System RAM code (See Section 3.3.3) to bank switch to the Home Bank and call the interruption service routines after which the state of the machine is restored and control returns to the interrupted process. Figure 3.2.2-1 shows the Extension ROM Interruption Fielder code.

3.2.2.2 General

The balance of the Extension ROM contains the following major components:

- Final Phase of System Initialization
(See Figure 1.1-4)
- Cassette tape I/O (see Section 4.2)
- Change Video Mode Service
- OS RAM routines including the Function Dispatcher (copied to RAM at System Initialization) (see Section 3.3.3)
- Function Dispatcher Jump Table

FIGURE 3.2.2-1

Extension ROM Interruption Fielder

<u>LOCATION</u>	<u>OBJECT CODE</u>	<u>SOURCE CODE</u>	<u>COMMENTS</u>
0038	F5	PUSH AF	Save AF
0039	F3	DI	Disable Ints.
003A	3AC25C	LD A,(VIDMOD)	Test Vidmod
003D	A7	AND A	
003E	00	NOP	
003F	2804	JR Z,CHK3	Vidmod=0
0041	F1	POP AF	Restore AF
0042	C36EFA	JP INT7	Chunk 7 if Vidmod not 0
0045	F1	CHK3 POP AF	Restore AF
0046	C3AE62	JP INT3	Chunk 3 if Vidmod = 0

3.2.2.3 Video Mode Change Service

The routine CHNG VID takes as input a single byte in Register A which designates the desired video mode as shown in Table 3.2.2-1. All non-zero values involve access to the second display file located at 6000H-7AFFH. When the mode change requires remapping of the RAM (see Figure 1.1-3), the necessary relocation (BASIC program, machine stack, OS RAM code, UDG area, etc.) and modifications (system variables, RAM code internal addresses, stack pointer, etc.) are done by this service. The desired video mode is written to Port OFFH, Bits 0-5, and the System Variable VIDMOD (5CC2H) is updated. The second display file is cleared to zeros on initial access (for Dual Screen Mode and High Resolution Graphics Mode, this results in a black screen since 0 yields attributes of black ink on black paper). If there is not enough free memory to do the necessary remapping, Error 4, Out of Memory is given.

Access to this service via the Function Dispatcher cannot be made consistently for various reasons. An Interface Routine is given in Section 3.2.2.4, to be executed from the Home RAM, which provides access to the Video Mode Change Service as well as other Extension ROM routines.

See Sections 4.1.2 and 5.2 for discussion of video screen support software. See Section 6.4 for details on known problems and corrections related to the Video Mode Change Service.

TABLE 3.2.2-1

INPUT TO VIDEO MODE CHANGE SERVICE

<u>VALUE IN A</u>	<u>VIDEO MODE</u>	<u>DESCRIPTION</u>
0	Normal	Primary Display File Only(Close 2nd Display File if Open)
128 (80H)	Dual Screen	Two Display Files Available. Primary Display File Active at Screen.
1	Dual Screen	Two Display Files Available. Second Display File Active at Screen
2	High Resolution Graphics	Primary Display File contains data for 256X192 pixels. Second Display File contains 6144 Attribute Bytes, each one controlling 8X1 pixels. NOTE 1.
	<u>64-Column</u>	
	<u>Ink</u>	<u>Paper</u>
6	Black	White
14 (0EH)	Blue	Yellow
22 (16H)	Red	Cyan
30 (1EH)	Magenta	Green
38 (26H)	Green	Magenta
46 (2EH)	Cyan	Red
54 (36H)	Yellow	Blue
62 (3EH)	White	Black

NOTE 1: The areas of memory normally used for Attribute Bytes are not accessed by the video hardware in this mode.

3.2.2.4 Extension ROM Interface Routine

The Extension ROM routines W TAPE (Write from RAM to Tape), R-TAPE (Read from Tape to RAM) (see Section 4.2) and CHNG VID (see Section 3.2.2.2) may be of interest to the machine code programmer. Because of a conflict with the use of the IX Register, the tape routines cannot be successfully accessed via the Function Dispatcher. Because the Change Video Mode Service may involve relocating the OS RAM routines (including the Function Dispatcher), and for other reasons, it also cannot be consistently accessed using the Function Dispatcher. Figure 3.2.2-2 gives a sample routine, to be executed from the Home RAM, which can be used to bank switch to the Extension ROM and call directly to the desired service. Appendix A contains an Extension ROM Map giving the addresses of these and other routines.

FIGURE 3.2.2-2

EXTENSION ROM INTERFACE ROUTINE

```

1      ; ; EXTENSION ROM INTERFACE ROUTINE
2      R_TAPE EQU 00FCH ; READ TAPE ROUTINE
3      W_TAPE EQU 0068H ; WRITE TAPE ROUTINE
4      CHNG_VID EQU 0E8EH ; CHANGE VIDEO MODE ROUTINE
5      VIDMOD EQU 5CC2H ; VIDEO MODE SYSTEM VARIABLE
6      ;
7      ;
8      ; ; CALL READTP WITH REGISTERS SET
9      ; ; UP FOR R_TAPE ROUTINE
10     ;
11     READTP LD HL,R_TAPE ; ADDRESS TO HL
12           CALL IFRTN ; ENABLE EXT./EXECUTE RTN
13           JR EXIT ; RESTORE HOME BANK AND RETURN
14     ;
15     ; ; CALL WRITTP WITH REGISTERS SET
16     ; ; UP FOR W_TAPE ROUTINE
17     ;
18     WRITTP LD HL,W_TAPE ; ADDRESS TO HL
19           CALL IFRTN ;
20           JR EXIT
21     ;
22     ; ; CALL CHGVID WITH DESIRED VIDEO
23     ; ; MODE IN A
24     ;
25     CHGVID LD HL,CHNG_VID ; ADDRESS TO HL
26           PUSH AF ; SAVE VIDEO MODE
27           CALL IFRTN
28     ;
29     ; ; COMPENSATE FOR "BUG" IN
30     ; ; CHNG_VID RTN.WHICH SETS
31     ; ; VIDMOD=0 INSTEAD OF 80
32     ; ; WHEN BOTH DISPLAY FILES
33     ; ; ARE OPEN
34     ;
35     ;
36     POP AF ; TEST VIDEO MODE
37     CP 80H ; TEST IF 80
38     JR NZ,EXIT
39     LO (VIDMOD),A ; SET VIDMOD=80H
40     EXIT LD A,(HSSAVE) ; GET PREV. MGR.SEL.
41           OUT (OF4H),A ; RESTORE
42           IN A,(OFFH) ; READ PORT FF
43           RES 7,A ; TURN OFF RCM SEL.
44           OUT (OFFH),A
45           EI
46           RET
47     ;
48     HSSAVE DEFB 0 ; SAVE MGR.SEL. (PORT OF4H)
49     ;
50     ;
51     IFRTN DI ; MASK INTERRUPTIONS
52           PUSH AF ; PRESERVE REG. A
53           IN A,(OFFH) ; EXT.ROM SELECT BIT
54           SET 7,A ; SEL. EXT.ROM
55           OUT (OFFH),A
56           IN A,(OF4H) ; HORIZONTAL SELECT FOR DOCK/EXT
57           LD (HSSAVE),A ; SAVE
58           LD A,1 ; SELECT CHUNK 0 IN EXT.ROM
59           OUT (OF4H),A ;
60           POP AF ; RESTORE REG. A
61           JP (HL) ; EXECUTE TARGET ROUTINE AND
62           ; RETURN TO CALLER OF IFRTN
63     ;
64     ;
65     END

```

3.3 RAM Organization and Services

3.3.1 System Variables

RAM beginning at 23552 (5C00H) is dedicated to the BASIC System Variables as defined in Appendix D of the TS 2068 User Manual and in Appendix B of this document. The area from the end of the defined variables (STRMNM - 23755 (5CCB)) to 24297 (5EE9H) is reserved for expansion of the System Variables, but is not used by the Operating System in the current TS 2068.

3.3.2 System Configuration Table

The area from 24298 (5EEAH) to 24575 (5FFFH) is reserved for the System Configuration Table (SYSCON). This table is built at system initialization time and is comprised of an 8 byte entry for AROS, a 4 byte entry for LROS, followed by eleven 24-byte entries for proposed expansion banks and an End-of-Table marker. In the original TS 2068 the actual usage of this table is limited to the 12 bytes for software cartridge identification (see Section 5.1 for details of the LROS and AROS Overhead Bytes).

3.3.3 Machine Stack

The TS 2068 reserves 512 (200H) bytes of RAM for the Machine Stack. The Machine Stack pointer is initialized to a value of 6200H (value also in System Variable MSTBOT); the pointer is decremented as items are pushed onto the stack (the pointer may also be modified directly by software). While the area reserved for the stack extends to 6000H, there is no actual check made to enforce this limit.

Note that the Machine Stack is located in the same memory area as the second display file. The CHNG VID routine relocates the stack to the memory area from 0F7COH to 0F8BFH, and modifies the Stack Pointer and MSTBOT (0F8COH), as well as other affected system variables, when initializing the second display file. (See Section 3.2.2.3.)

3.3.4 OS RAM Routines

The code for the following Operating System functions is copied from the Extension ROM to Chunk 3 of the RAM at System initialization time. Since this is in the same memory area as the second display file, this code must be relocated, along with the machine stack, if the second display file is to be used. The CHNG VID routine does the necessary relocation and modifications. (Section 3.2.2.3.)

Because this code is not in a fixed location, access to the OS RAM routines is conditional on the current video mode. The standard technique employed is to test the value in the System Variable VIDMOD at location 23746 (5CC2H). A zero indicates that the second display file is not in use and that the OS RAM routines are therefore in Chunk 3; any non-zero value indicates that the routines are in Chunk 7.

NOTE: This design implies that Chunks 2, 3 and 7 are always enabled in the Home Bank RAM whenever the System ROM and/or RAM routines are being used.

The OS RAM routines are contained in Module "Dispatch" which is included in Appendix A.

3.3.4.1 RAM Interruption Handler

Chunk 3 Entry: 62AEH

Chunk 7 Entry: FA6EH

The user must enter with bank status and Z80 registers intact, with address from point of interruption on the stack.

The RAM interruption handler saves state, including memory selection, enables the Home Bank, updates the Frame Counter, calls the keyboard scan routine in the Home ROM, restores state, and returns to the interrupted process.

The RAM Interruption handler is used whenever the interruption occurs while the Extension ROM is enabled. See Figure 3.2.2-1, Extension ROM Interruption Fielder. This same technique can be used for interruption processing in another bank, e.g. if an LROS wanted to use the standard system ROM keyboard scanning routines.

3.3.4.2 RAM Service Routines

Table 3.3.4-1 lists the RAM service routines which are designed to facilitate communication between memory banks. Those with Service Codes are accessible via the Function Dispatcher.

TABLE 3.3.4-1

OS RAM SERVICE ROUTINES

LABEL	SERVICE CODE (Decimal)	LOCATION		DESCRIPTION
		CH.3	CH.7	
GET_WORD	-	6316	FAD6	Returns in HL the word from the address in HL in the bank specified in B.
PUT_WORD	-	633B	FAFB	Writes the word in DE to the address in HL in the bank specified in B.
GET_STATUS	14	6405	FBC5	Returns current memory selection (Horizontal Select byte - low active) in C for the bank specified in B. Preserves Bank # in B for Home, Ext. or Dock.
GET_CHUNK	-	644D	FCOD	Returns a single byte mask in A with all bits 0 except the one corresponding to the chunk for the address in HL.
GET_NUMBER	15	645E	FC1E	Returns in Reg. A the bank number currently controlling the address in HL.
BANK_ENABLE	-	6499	FC59	Enables the memory selected (Horizontal Select byte - low active) in the specified bank. (Bank # in B; Mem.Sel.in C)
GOTO_BANK	-	6572	FD32	Transfers control to the specified address after enabling the memory selected in the specified bank. Parameters passed on stack by pushing target address, then Bank #/Mem.Select prior to calling GOTO BANK. (Return address is discarded.).
CALL_BANK	-	65D0	FD90	Like GOTO BANK except saves current bank status, calls target address, and restores status prior to returning to user. Two additional parameters are passed on stack prior to doing call to CALL BANK. These are PRM OUT (16-bits) following by PRM IN (16 bits) as described for the Function Dispatcher.

TABLE 3.3.4-1

OS RAM SERVICE ROUTINES
(continued)

LABEL	SERVICE CODE (Decimal)	LOCATION		DESCRIPTION
		CH.3	CH.7	
XFER_BYTES	-	6722	FEE2	<p>Copies n byte(s) from specified source to specified destination in either ascending or descending order. Source and destination can be in the same or different banks and can be in shadowing chunks, but neither source nor destination can pass a "chunk" (8K) boundary since only the chunks containing the starting source and destination addresses are explicitly enabled.</p> <p>Parameters passed on stack by pushing:</p> <ul style="list-style-type: none"> Source Bank/Dest.Bank Source Address Dest. Address Length 0/Direction: <ul style="list-style-type: none"> (0=Ascending -1=Descending)

NOTE: See Appendix A for listing of these routines. See Section 6.0 for known corrections to the routines.

3.3.4.3 Function Dispatcher

Chunk 3 Entry: 6200H

Chunk 7 Entry: F9C0H

The Function Dispatcher provides a common interface to a number of system routines via a Service Code and Jump Flag parameter passed on the machine stack. Table 3.3.4-2 lists the routines in Service Code order. Codes for routines that are known to not be successfully accessible via the Function Dispatcher have been deleted (marked Reserved). However, there is no guarantee that those on the list can be accessed without problems. Some ROM routines require data in a particular format, e.g. BASIC floating point number(s), both standard and special integer format, on the Calculator Stack which is located between (STKBOT) and (STKEND) (see Appendix C of the TS 2068 User Manual). An effort has been made to include information on register usage and functionality, but some of the ROM routines are so tightly tied to the BASIC Interpreter that they would require analysis which is beyond the scope of this document. These have been flagged with an Asterisk, but included in the list for documentation purposes only. Most of the routines which are directly implementing a BASIC command or function have two different action sequences based on the INTPT Flag (Bit 7 of FLAGS) which distinguishes syntax checking (Flag=0) from actual execution (Flag=1).

In order to use the Function Dispatcher, first set up any memory and stack (both machine and/or calculator) locations as if invoking the desired service directly. Then push the parameter(s) for the Dispatcher on the machine stack in the order outlined below. Finally, set up the registers as if invoking the desired service directly and call the Dispatcher based on its current location (Chunk 3 if VIDMOD=0 or Chunk 7 if VIDMOD has a non-zero value).

1. PRM_OUT 16 bits - Number of bytes of parameter data being passed on the stack to the specified Service (number of stack "pushes" * 2). Zero if no parameters being passed. E.g., to pass 4 bytes:

LD HL,4
PUSH HL

This parameter is passed to the Dispatcher only if the Jump Flag (SVC_CODE) Bit 15) is not set. NOTE: This parameter refers to machine stack entries only, not to the Calculator Stack.

2. PRM_IN 16 bits - Number of bytes of parameter data to be passed back from the specified Service (number of stack "pushes" * 2). Zero if no parameters to be passed back.

This parameter is passed to the Dispatcher only if the Jump Flag (SVC_CODE Bit 15) is not set. NOTE: This parameter refers to machine stack entries only, not to the Calculator Stack.

3. SVC_CODE 16 bits - Bits 0-14 identify the Service to be invoked. Bit 15 (Jump Flag) is set if no return is desired (jump to Service rather than call). Bit 15 is zero if return is desired. E.g, to call K_SCAN using Service Code 136:

LD HL,136 or LD HL,88H
PUSH HL PUSH HL

Addendum To TS 2068 Function Dispatcher Services:
On page 84, COLOR and HIFLSH (service codes 85 and 86) cannot always be accessed through the Function Dispatcher, due to resetting of the carry flag by the FD. COLOR may be accessed by setting the registers as described in the manual, and then coding CALL #23DE. HIFLSH can be accessed similarly by coding CALL #2410.

TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES

SERVICE	SERVICE CODE	DESCRIPTION
	1 - 13 (1-0DH)	Reserved
GET_STATUS	14 (0EH)	Returns Memory Selection (Low Active) in C for Bank # in B
GET_NUMBER	15 (0FH)	Returns Bank # in A for Address in HL
	16-24 (10-18H)	Reserved
UPD_K	25 (19H)	Process Keyboard Input (See Section 4.1.1)
PARP	26 (1AH)	Generates DE+1 Cycles of a Tone having the Period 8N+236 to 8N+246 T-States. HL=N. (See 4.4)
BEEP	27 (1BH)	BEEP Command - processes parameters on Calculator Stack. Exits via PARP. (See 4.4)
K_DUMP	28 (1CH)	COPY Command. Dumps Primary Display File to Printer. (See 4.1.3)
SENDTV	29 (1DH)	Char.Output to Screen/Printer. Character Code in A. (See 4.1.2)
SETAT	30 (1EH)	Set Print Position to value in BC. B=Line No. (0-23); C=Column No. (0-31)
ATTBYT	31 (1FH)	Set Attribute Byte for Display File Adrs. in HL using ATTR_T, MASK_T and P_FLAG.
R_ATTS	32 (20H)	Permanent Attribute Info. to Temporary Attribute Variables
CLLHS	33 (21H)	Clear Lower Screen (Primary Display File)
CLS	34 (22H)	Clear Entire Screen (Primary Display File)
DUMPPR	35 (23H)	Print/Clear Print Buffer. (See 4.1.3)

TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
PRSCAN	36 (24H)	Send Scan (32 bytes) to Printer. Pixel Data Address in HL. Number of Scans remaining in B (=1-8). (See 4.1.3)
DESLUG	37 (25H)	Remove Number Slugs from Edit Line Buffer (Address in HL)
K_NEW	38 (26H)	NEW command. See Fig. 1.1-4
INIT	39 (27H)	Initialize: DE=Maximum RAM Address. A=0 for Power-On; = -1 (FFH) for NEW. (See Fig.1.1-4)
INCH	40 (28H)	Input Character to A from currently Selected Channel. Returns NC if no input.
SELECT	41 (29H)	Select Channel (Stream) - # in A. (See 4.1)
INSERT	42 (2AH)	Insert BC Bytes before byte whose address is in HL. Copies up all from HL to (STKEND) and updates affected system variables. Returns BC=0; DE=adrs.of last byte of inserted space; HL=adrs.of byte before first.
RESET	43 (2BH)	Reset Calculator Stack. Sets (STKEND) = (STKBOT) and (MEM)=MEMBOT (5C92H).
CLOSE	44 (2CH)	CLOSE # Command. Channel # on Calculator Stack.
CLCHAN	45 (2DH)	Close Channel. BC=Value from STRMS (Index into CHANS).
OPEN	46 (2EH)	OPEN # Command. Channel # and Device Spec. on Calculator Stack
OPCHAN	47 (2FH)	Open Channel. Device Spec. on Calculator Stack. DE=pointer into STRMS based on Ch.#.

(See 4.1 for more info. on OPEN and CLOSE)

TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CAT	48 (30H)	CAT Command (Not Applicable)
ERASE	49 (31H)	ERASE Command (Not Applicable)
FORMAT	50 (32H)	FORMAT Command (Not Applicable)
MOVE	51 (33H)	MOVE Command (Not Applicable)
FLASHA	52 (34H)	Flash Char.in A to Screen. (Calls SENDTV; assumes Lower Screen selected. Used to Flash Cursor.)
FIND_L	53 (35H)	Find BASIC Program Line with the number in HL. If Line found, returns Z and Address of Line in HL, else returns NZ and HL contains either address of line with next larger line number or points to the Variables area if there is no larger line number. Requested Line No. returned in BC and Address of Preceding Line in DE (DE=HL if no preceding line).
SUBLIN	54 (36H)	Finds either the D'th statement (D=Statement #; E=0) or 1st statement whose keyword token matches E (D=0), in a line pointed to by HL. If the D'th statement is found, returns Z and HL and (CH_ADD) both point to 1 byte before statement. (If line contains exactly D-1 statements, then the next line counts as the D'th.). If match on E is found, then returns NZ,NC and both HL and (CH_ADD) point to keyword. D is decremented by the number of statements looked at (e.g. D= -2 if two statements). If no match on E then returns NZ,C with both HL and (CH_ADD) pointing to End-of-Line byte (ODH).

TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
RECLEN	55 (37H)	Returns in BC the length of the record pointed to by HL. Sets DE to HL+BC. The record can be a program line, or a string or numeric variable or array.
DELREC	56 (38H)	Delete record pointed to by HL having length BC from Program or Variables memory. Updates affected system variables.
PUT_BC	57 (39H)	Converts number in BC from binary to ASCII and outputs to currently selected channel. If BC less than 0, outputs a 0.
SYNTAX	58 (3AH)	Check syntax of command or program line in Edit Line Buffer (E LINE). ERR_NR= -1 if no errors, otherwise contains Error Number-1.
EXCUTE	59 (3BH)	Execute command(s) from Edit Line buffer.
FOR	60 (3CH)	FOR command. *
STOP	61 (3DH)	STOP command. Does RESTART 8 with Error No. 9.
NEXT	62 (3EH)	NEXT command. *
READ	63 (3FH)	READ command. *
DATA	64 (40H)	DATA statement. *
RESTBC	65 (41H)	RESTORE command - Line No. in BC
RAND	66 (42H)	RANDomize command. Sets seed for Random Number Generator based on Parameter on Calculator Stack. If parameter is non-zero, value is loaded to SEED; if zero, value in FRAMES is loaded to SEED.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CON'T	67 (43H)	CONT command. Loads values from OLDPPC and OSPPC to NEWPPC and NSPPC and returns. Inside the BASIC Interpreter, this results in executing from Line No. in NEWPPC, Statement No. in NSPPC.
JUMP	68 (44H)	Jump to Line - Loads Line Number from Calculator Stack to NEWPPC and sets NSPPC to 0 and returns.
FIX_U1	69 (45H)	Converts Floating Point number on Calculator Stack to a single byte unsigned binary value in A (uses FP2A). Does RESTART 8 for Error B if number out of range.
FIX_U	70 (46H)	Converts Floating Point number on Calculator Stack to a 2-byte unsigned binary value in BC (uses FP2BC). Error B if number out of range.
CLEAR	71 (47H)	CLEAR command. Processes parameter on Calculator Stack to value in BC for CLR_BC.
CLR_BC	72 (48H)	Value in BC is new RAMTOP. Deletes Variables, clears screen, and Calculator Stack, etc.
GO_SUB	73 (49H)	GO SUB command. Inserts a 3-byte GO SUB Block into the machine stack above the 2 most recent entries. The Block consists of current Line No. (2 bytes) and Statement No. (1 byte) to be used when RETURN is executed. Then calls JUMP to process GO SUB parameter and returns. At return to caller, machine stack consists of top of stack at point GO SUB was called, followed by 3-byte entry (Line No. MSB/Line No. LSB/Statement No.).

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CHK_SZ	74 (4AH)	Checks if room for BC + 80 (50H) bytes between (STKEND) and (RAMTOP). Addition of 80 bytes is "left-over" from Spectrum to guarantee minimum machine stack where the stack was at the top of RAM. Error 4 if not enough room.
RETURN	75 (4BH)	RETURN command. Retrieves most recent GO SUB Block from Machine Stack (SP+4), loads data to NEWPPC and NSPPC and returns. Error 7 if MSB Line No.=3EH (End of Stack Marker).
PAUSE	76 (4CH)	PAUSE command. Processes parameter on Calculator Stack to BC then waits BC frames or until key is depressed. (Uses HALT instruction, so interruptions must be enabled.)
BREAK?	77 (4DH)	Reads BREAK key. Returns NC if it is pressed and ON ERROR is not active.
DEF	78 (4EH)	Define Function.*
K_LPR	79 (4FH)	LPRINT - Selects Channel 3 and processes items in LPRINT statement for output via WRCH.
K_PRIN	80 (50H)	PRINT - Selects Channel 2 and processes items in PRINT statement for output via WRCH (same code used for K_LPR).
P_SEQ	81 (51H)	Code used by K_LPR and K_PRIN to process output data and controls in BASIC statement (address in CH ADD).
INPUT	82 (52H)	INPUT command. Selects Channel 1 and processes I/O for Keyboard/Lower Screen using a buffer at (WORKSP) for input. *

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
I_SEQ	83 (53H)	Code used by INPUT to process input items and controls in BASIC statement (address in CH_ADD).
NOTKB?	84 (54H)	Returns Z if current channel is Keyboard/Lower Screen (device specification="K").
COLOR	85 (55H)	Adjusts system variables ATTR_T, MASK_T and P_FLAG for color code in D (0-9). Enter with C set to set Ink or NC set to set Paper. Error K if D is invalid.
HIFLSH	86 (56H)	Adjusts system variables (ATTR_T and MASK_T) for Flash/Bright code in D (0, 1 or 8) else Error K. Enter with C for Flash or NC for Bright.
SCRMBL	87 (57H)	Returns in HL the primary display file address for the pixel with coordinates in BC (B=Y;C=X). Returns in A the bit no (0-7) where 0=lefthand or most significant bit. Error B if Y is greater than 175.
PLOT	88 (58H)	PLOT command. Processes X/Y parameters on the Calculator Stack to BC for plotting of pixel via PLOTBC.
PLOTBC	89 (59H)	Deals with pixel for coordinates in BC (B=Y; C=X). Processes using P_FLAG for Inverse and Over attributes. Updates Attribute File and sets COORDS=BC.
GET_XY	90 (5AH)	Converts a pair of numbers from the Calculator Stack to 2 single byte numbers. Top number goes to B and second to C. D=sign of B and E=sign of C (+1 or -1). Used by PLOT and other routines.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CIRCLE	91 (5BH)	CIRCLE command. Calculates successive plot positions from the parameters in the BASIC statement. *
DRAW	92 (5CH)	DRAW command. Calculates successive plot positions from the parameters in the BASIC statement. *
DRAW_L	93 (5DH)	Plots a straight line from current position (COORDS) based on parameters from Calculator Stack (X,Y). *
EXPRN	94 (5EH)	Evaluates expression in BASIC program line (CH_ADD), putting value on Calculator Stack. *
F_SCRN	95 (5FH)	SCREEN\$ function. Matches screen line/col. position (parameters on Calculator Stack) against standard ASCII character set. Returns BC=0 if no find. BC=1 and DE points to Char. Code byte if match found.
F_ATTR	96 (60H)	ATTR function. Returns attribute byte value controlling screen pixel position based on parameters on Calculator Stack (X,Y).
RND	97 (61H)	RND function. Uses value in SEED to generate a pseudo-random number which is placed on the Calculator Stack (Floating Point number).
F_PI	98 (62H)	PI function. Places value of PI on Calculator Stack.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
F_INKY	99 (63H)	INKEY\$ function. Scans keyboard and puts character code byte in (WORKSP) if key detected. In any case, pushes Regs. AEDCB onto Calculator Stack - BC=0 if no input; =1 if char. code stored; DE=address of char. code byte.
FIND_N	100 (64H)	Find Variable. Searches Variables area for match against identifier pointed to by CH ADD. Adjusts bit NO of FLAGS (Bit 6) for type (1=numeric; 0=string). Also used to find formal parameters for User Defined Functions. *
PSHSTR	101 (65H)	Push String - Clears bit NO of FLAGS and pushes Regs. AEDCB onto Calculator Stack adjusting (STKNXT) upwards. DE contains address of string; BC contains length.
PAEDCB	102 (66H)	Same code as for PSHSTR but preserves state of bit NO of FLAGS (Bit 6).
LET	103 (67H)	LET command. Processes existing or creates new variables. *
POPSTR	104 (68H)	Pop String - Pops end of Calculator Stack ((STKNXT)-1 through (STKNXT)-5) to Regs. BCDEA, adjusting (STKNXT) downwards.
DIM	105 (69H)	DIM statement. Creates or initializes numeric or string arrays. *
STKUSN	106 (6AH)	Stack Unsigned Number - inputs a floating point number onto the Calculator Stack from a series of ASCII characters addressed by (CH ADD). The first character is already in Reg. A (either decimal point, binary token or digit).

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
STK_A	107 (6BH)	1-byte unsigned integer in A to top of Calculator Stack (binary to floating point). Loads O to B and A to C, then executes STK_BC.
STK_BC	108 (6CH)	2-byte unsigned integer in BC to top of Calculator Stack (binary to floating point).
ININT	109 (6DH)	Converts a series of ASCII digits pointed to by (CH_ADD) into an unsigned floating point integer on the Calculator Stack. First character is in A on entry. Terminates when non-digit found.
FP2BC	110 (6EH)	Pops top of Calculator Stack (floating point number) and puts in BC, rounded to nearest integer. Returns NZ if value is negative. Returns C if number exceeded maximum 2-byte value (65535). Range: -65535 to +65535.
FP2A	111 (6FH)	Pops top of Calculator Stack (floating point number) and puts in A, rounded to nearest integer. Returns NZ if value is negative. Returns C if number exceeded maximum 1-byte value (255). Range: -255 to +255.
OUTPUT	112 (70H)	Outputs number on top of Calculator Stack to currently selected channel via WRCH. (Converts from floating point to ASCII.)
Full explanation of the following Calculator Routines is beyond the scope of this document.		
SUB	113 (71H)	Subtract floating point format numbers (HL) minus (DE). (DE) assumed to be (HL) + 5.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
ADD	114 (72H)	Add (HL) + (DE). See SUB.
MULT	115 (73H)	Integer multiply HL * DE. Returns C if overflow.
TIMES	116 (74H)	Floating Point Multiply (HL) * (DE).
DIVIDE	117 (75H)	Floating Point Divide (HL)/(DE).
TRUNC	118 (76H)	Truncates a floating point number (HL) towards zero to an integer. Assumes (DE) = (HL) + 5.
FLOAT	119 (77H)	Converts number (HL) to floating point format. Assumes HL points to an integer in 5-byte format.
INTDIV	120 (78H)	Replaces top two numbers on Calculator Stack (X and Y) by X Mod Y and the integer quotient INT (X/Y). Returns with DE and HL = Calc.Stack Pointers.
INT	121 (79H)	Replaces the top of the Calculator Stack by its integer part. Returns with HL = top of Calc. Stack and DE = next free space.
EXP	122 (7AH)	Replaces the top of the Calculator Stack, X, by EXP(X). Returns with DE and HL = Calc.Stack Pointers.
LN	123 (7BH)	Replaces the top of the Calculator Stack by its natural logarithm. Returns DE and HL = Calc.Stack Pointers.
ANGLE	124 (7CH)	Replaces the top of the Calculator Stack (X) by Y where Y is greater than or equal to -1 and less than or equal to +1 and the SIN X = SIN (PI/2 * Y).

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
COS	125 (7DH)	Replaces the top of the Calculator Stack by its COSINE.
SIN	126 (7EH)	Replaces the top of the Calculator Stack by its SINE.
TAN	127 (7FH)	Replaces the top of the Calculator Stack by its TANGENT.
ATN	128 (80H)	Replaces the top of the Calculator Stack by its inverse TANGENT.
ASN	129 (81H)	Replaces the top of the Calculator Stack by its inverse SINE.
ACS	130 (82H)	Replaces the top of the Calculator Stack by its inverse COSINE.
ROOT	131 (83H)	Replaces the top of the Calculator Stack by its Square Root.
TO_THE	132 (84H)	Replaces the top two numbers on the Calculator Stack (X, Y) by $X^{**}Y$.
RDCH	133 (85H)	Wait for character from currently selected channel (calls INCH). Returns character code in A. See 4.1.1.
SENDCH	134 (86H)	Write character whose code is in A to currently selected output channel. See 4.1.2.
WRCH	135 (87H)	See 3.2.1.1, RESTART 16.
K_SCAN	136 (88H)	Keyboard Scan. See 4.1.1

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
P_LFT	137 (89H)	Backspace. Sets current column position back 1 for selected device. (System Variable updated is S_POSN, SPOSNL, or P_POSN for Screen, Lower Screen or Printer respectively.)
P_RT	138 (8AH)	Outputs a space to currently selected device.
P_NL	139 (8BH)	End-of-Line. Sets current position to start of next line if screen, or outputs printer buffer if printer.
PUTMES	140 (8CH)	Output message to currently selected device. DE points to base of message table which contains variable length ASCII coded messages. The first byte of the table and the last byte of each message must have the most significant bit set. Register A contains the message number, numbered from 0 upwards.
K_CLS	141 (8DH)	CLS command. Executes both CLS and CLLHS.
SCRL	142 (8EH)	Scrolls entire screen (primary display file) up 1 line.
F_PNT	143 (8FH)	POINT function. Processes X,Y parameters from Calculator Stack to BC. Returns unsigned integer value = 0 or 1 on Calculator Stack reflecting state of pixel at coordinates X/Y.
DRAWLN	144 (90H)	Same as DRAW L but enter with BC register containing coordinates, B=Y and C=X.
PUT_LN	145 (91H)	Output Line Number as 4 digits, right aligned and space filled to currently selected output channel. HL points to MSB of 2-byte Line Number.

4.0 SYSTEM I/O GUIDE

4.1 I/O Channels

The TS 2068 software architecture supports up to 19 I/O Channels or "Streams", numbered from -3 through 15. Those numbered less than 0 are "hidden" or reserved for system use; Channels 0 through 15 are available for assignment via the OPEN # command which has the following format:

OPEN # n,s

where n is the Channel number (0-15) and s is the Device Specification, e.g. "K" (keyboard), "S" (screen) or "P" (printer).

Channels 0 through 3 are initialized at power-on or execution of a NEW command to support the standard system devices and character I/O functions as shown in Figure 4.1-1. Channels 4-15 are considered "Closed". You can re-assign the standard I/O, e.g. OPEN # 2,"P" will direct all PRINT and LIST commands to the 2040 Printer instead of the screen. You can also assign Channels 4-15 and then direct I/O by including the Channel number (or a variable equated to the channel number) in the I/O statement, e.g. PRINT # n. Support for other than the standard system devices described above is not implemented in the original version of the TS 2068 and attempts to OPEN Channels or "Streams" using other than the standard device specifications ("K", "S" or "P") will result in an error message. One possibility for adding BASIC support for new devices is to intercept the I/O error on OPEN and other commands such as CAT and FORMAT via ON ERR and interpret the BASIC program line using your own machine code routines.

	<u>Channel/ Stream #</u>	<u>Device Specification</u>	<u>Command/Function</u>
RESERVED	-3	"K"	Keyboard/Lower Screen
	-2	"S"	Main Screen
	-1	"R"	RAM Write (not used)
	0	"K"	Output to Lower Screen
	1	"K"	INPUT command
	2	"S"	PRINT/LIST commands
	3	"P"	LPRINT/LLIST commands

FIGURE 4.1-1

The Channel architecture is implemented by a number of tables located in both ROM and RAM.

- A. STRMS STRMS is a 38 byte table (2 bytes for each of the 19 channels) located in the System Variables area beginning at 23568 (5C10H). It is initialized at power-on or NEW to the following values:

<u>LOCATION</u>	<u>VALUE</u>	
5C10	0100 (Channel -3)	} (Copied from SMINIT in module EDIT of the Home ROM)
5C12	0600 (Channel -2)	
5C14	0800 (Channel -1)	
5C16	0100 (Channel 0)	
5C18	0100 (Channel 1)	
5C1A	0600 (Channel 2)	
5C1C	1000 (Channel 3)	
5C1E	0000 (Channel 4)	
.	.	
.	.	
5C34	0000 (Channel 15)	

This table is accessed using ((Ch.# * 2) + 16H) as an index added to 5C00H. The 2-byte value in the table is an index into the CHANS area of memory which contains the addresses of the I/O routines for the selected channel. If the 2-byte value is zero, the Channel is closed. The STRMS table is modified via the OPEN # and CLOSE # commands. When a Channel is OPENed, the device specification is used to obtain the 2-byte value to be inserted. This value is taken from the table STRMINIT in module EDIT of the Home ROM. When Channels 0 through 3 are CLOSEed, the values are restored to those used at power-on time. All others are cleared to zero.

- B. CHANS The CHANS System Variable at 23631 (5C4FH) contains the address of a 21-byte table initialized at power-on or execution of a NEW command to support "stream" I/O to the four standard system devices ("K", "S", "R" and "P"). Each table entry is 5 bytes long and is indexed by the value obtained from the STRMS table added to (CHANS)-1. Each entry has the following format:

Output Routine Address	2 Bytes
Input Routine Address	2 Bytes
Device Specification	1 Byte

This table is copied from CHINIT in module EDIT of the Home ROM. The last byte of the table contains an 80H which will immediately precede the first line of the BASIC Program (PROG).

Whenever an I/O operation is performed, the appropriate Channel is "selected", i.e. its number is used as an index into STRMS to obtain the offset into the CHANS table. This offset is added to

(CHANS)-1 and the resultant pointer is loaded into the System Variable CURCHL for use by the next character I/O operation (WRCH/RDCH). The device specification from CHANS is used to find and execute the initialization routine in SELTAB.

- C. SELTAB The Select Table is located in the EDIT module of the Home ROM and contains offsets to device dependent initialization routines for the standard devices "K", "S" and "P".
- D. SPEC_T The Specification Table is located in the CHANS module of the Home ROM and contains offsets to device dependent OPEN routines for the standard devices "K", "S" and "P". It is accessed whenever an OPEN # is executed.
- E. CL_TAB The Close Table is located in the CHANS module of the Home ROM and contains offsets to device dependent CLOSE routines for the standard system devices "K", "S" and "P". It is accessed whenever a CLOSE # is executed.

The following sections describe the standard system I/O devices supported via Channel I/O.

4.1.1 Keyboard

The low-level routines supporting keyboard input are executed every 1/60 of a second out of the Interruption Handler (Location 56 (38H)). The controlling routine is labelled UPD K. This routine calls K_SCAN to determine if any key(s) are currently being depressed, controls the debouncing and repeat algorithms, calls K_BASE to determine the Base Code, calls CHCODE to translate the Base Code based on Mode (e.g. "K", "G" or "E" Mode), and finally, stores the resultant keystroke code in LAST_K and sets the flag KEYHIT. Figure 4.1.1-1 illustrates the mode control variable and associated flags and Figure 4.1.1-2 contains flowcharts of the keyboard support routines.

The character input routine associated with Device Spec. "K" is labeled IN K. The entry address is obtained using the pointer in CURCHL when Channel 1 has been Selected and the Character I/O Input routines RDCH/INCH are executed. The IN K routine tests the KEYHIT flag to detect the presence of input from the keyboard. When the KEYHIT flag=1, the contents of LAST_K are returned to the requestor.

FIGURE 4.1.1-1
TS 2068 MODE CONTROLS

<u>System Variable</u>	<u>Location</u>	<u>Description</u>
MODE	23617(5C41H)	Value 0 = "K" or "L" Mode 1 = "E" Mode 2 = "G" Mode
FLAGS	23611(5C3BH)	If MODE = 0 then: Bit 3 = 0 for "K" Mode = 1 for "L" Mode
FLAGS2	23658(5C6AH)	If in "L" Mode then: Bit 3 = 0 CAPS Lock Off = 1 CAPS Lock On

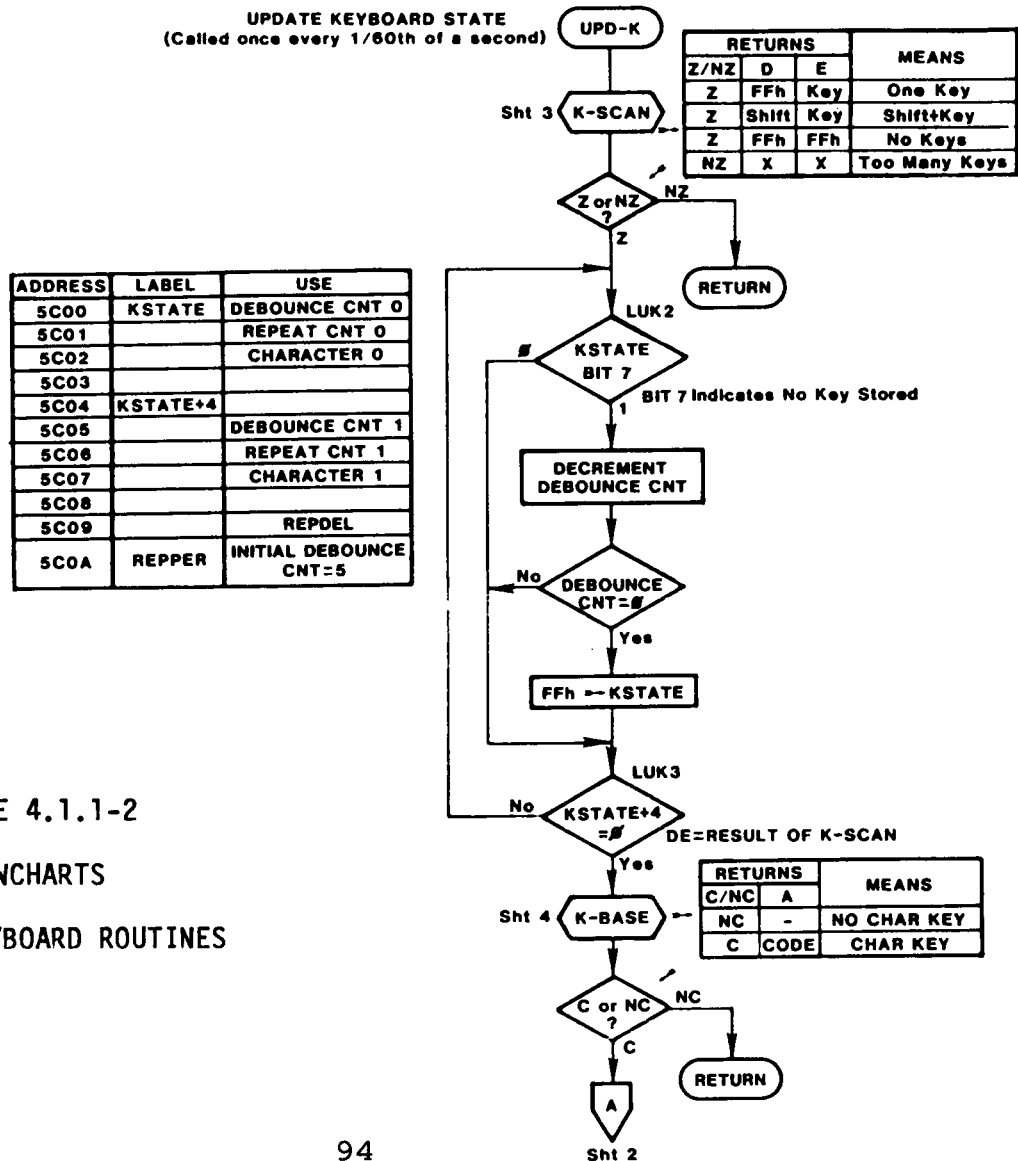
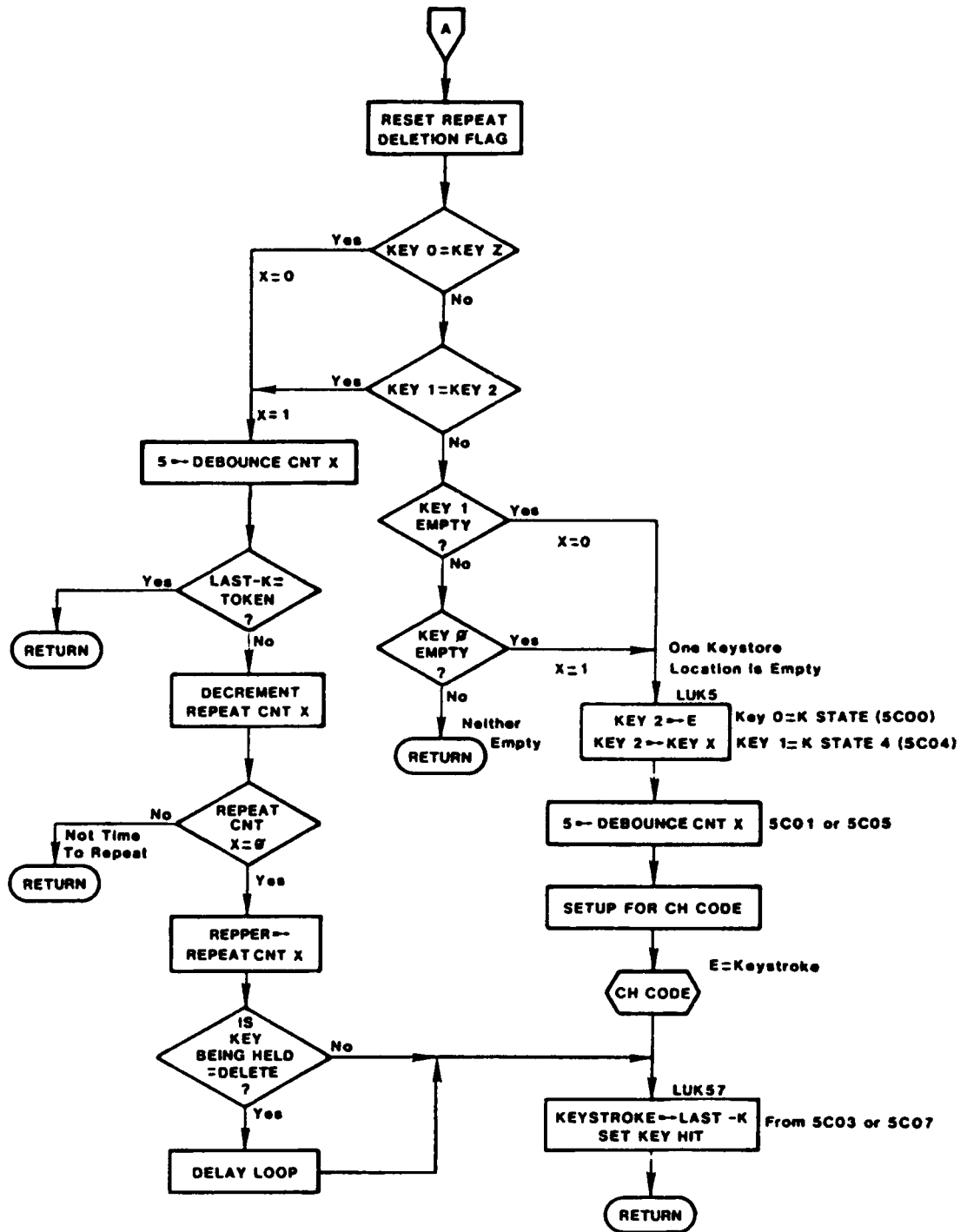
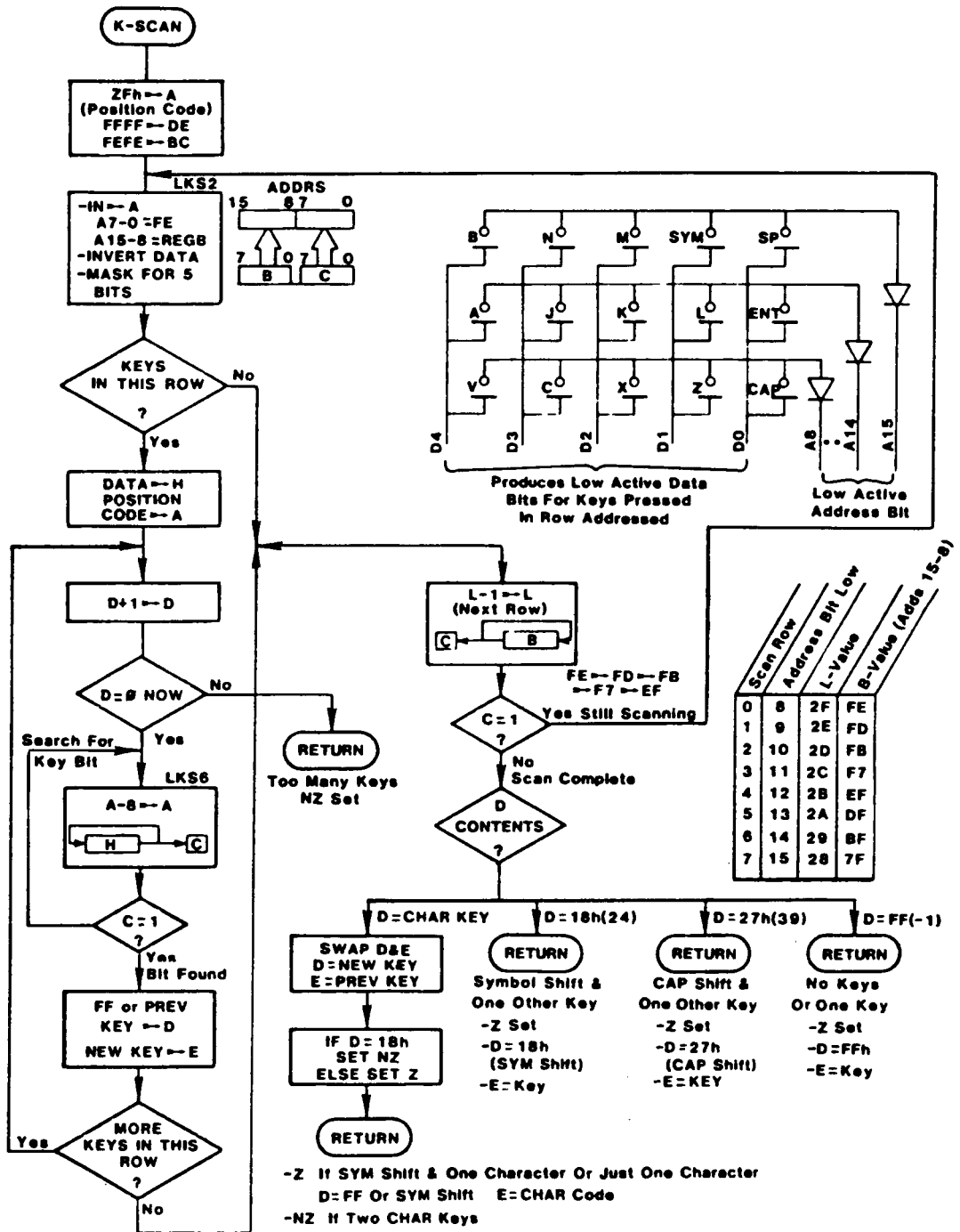
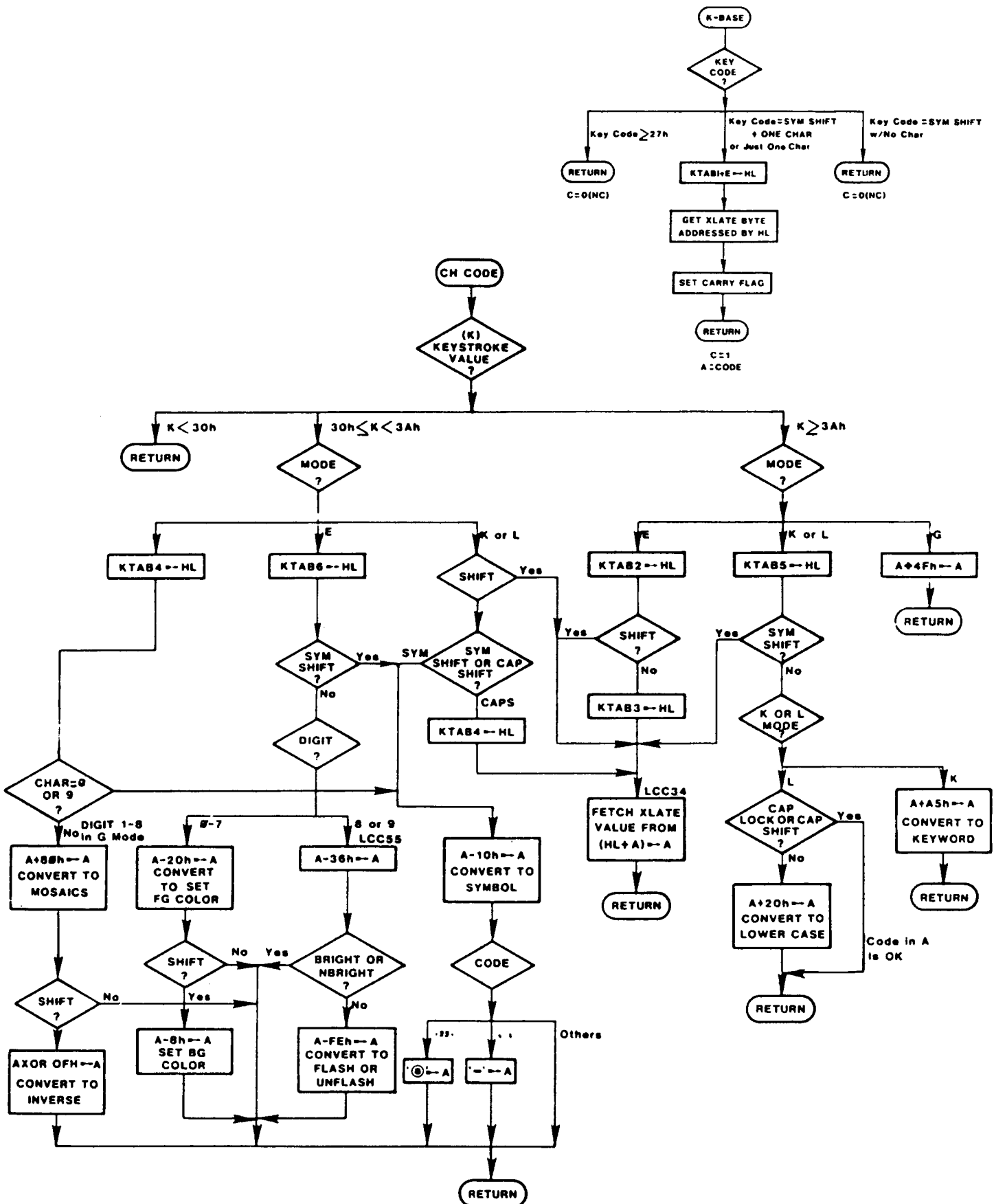


FIGURE 4.1.1-2
FLOWCHARTS
TS 2068 KEYBOARD ROUTINES





K-BASE: Find BASE Code For Key



4.1.2 Video Screen

The TS 2068 system software supports I/O in the primary display file only. See Section 2.1.10 for the display file organization. The screen, which is 32 columns X 24 lines, is partitioned into two parts, the main or upper screen (22 lines) and the lower screen (2 lines). The lower portion of the screen is used for output of system messages and to echo input from the keyboard of BASIC commands, BASIC program lines, or data. The lower screen expands as needed for multi-line input, scrolling the entire screen upwards. The variable DF SZ reflects the number of lines in the lower screen (default=2).

Character output to the screen is done using the Channel I/O described in Section 4.1 using device specification "K" for the lower screen and "S" for the upper screen. Each character is defined by an 8 X 8 group of pixels. The 8 bytes needed for each of the 133 characters supported by the TS 2068 are located as shown in Figure 4.1.2-1. Note that by constructing your own pixel data and placing (base address-100H) into CHARS, you can define your own character set.

Associated with each character position is an Attribute Byte controlling the background (PAPER) color, the foreground (INK) color, the intensity (BRIGHT), and whether the position is constant or alternates between true and inverse video (FLASH). Two other "attributes", OVER and INVERSE, are implemented by software at the time the character(s) are placed into the display file.

FIGURE 4.1.2-1

TS 2068 STANDARD CHARACTER TABLES

<u>Character Set</u>	<u>No.of Chars.</u>	<u>Char.Codes</u>	<u>Location</u>
Standard	96	32-127 (20-7FH)	Home ROM (3D00-3FFFH) (Address-100H in CHARS)
Std.Graphics	16	128-143 (80-8FH)	Dynamically Generated by Software
User Defined Graphics	21	144-164 (90-A4H)	Home RAM (Address in UDG)

The screen output routine, SENDTV, is in Module IO 1 of the Home ROM. This routine is used for output to both the screen (upper and lower) and the dot matrix printer. The following sequence illustrates the major operations involved in executing a PRINT "A" statement:

1. Channel 2 is Selected (normal assignment assumed)
 - loads CURCHL with pointer into CHANS area for Channel 2 (first 2 bytes are address of Output Routine - SENDTV).
 - clears printer and lower screen flags
 - sets ATTR_T to values based on ATTR_P (current "permanent" attribute values are transferred to the system variable used by the screen output routine). If the PRINT statement contained temporary attribute controls, they would override the settings established via Select.
2. The character code for "A" (65/41H) is placed in Register A and a RESTART 16 (10H) is executed (WRCH). This jumps to SENDCH in module EDIT of the Home ROM which passes control to the SENDTV routine based on (CURCHL).
3. The registers are loaded from the System Variables with the current Row/Column position (S_POSN) and Display File address (DF_CC) for the main screen.
4. The character code is determined to be from the standard character set so the registers are loaded with the address from CHARS and the offset to the pixel pattern for "A" is calculated using the character code X 8 (shift left 3 places).
5. The first pixel row (8X1) from the character table is copied to the display file. The character table address is incremented by 1 and the display file address is incremented by 256 (100H). The next pixel row (8X1) is copied to the display file. This process is repeated until the 8 pixel rows have been copied. Masking of the data going into the display file is done based on the flags from P_FLAG thus controlling the OVER and INVERSE attributes.
6. The attribute byte controlling the character position just written is updated based on the value in ATTR_T and other flags.

7. The variables S POSN and DF CC are updated to reflect the next screen position and return is made from the WRCH operation.

In the above sequence, if the print position for the "A" had started a new line following the 22 lines of the main screen, the SCROLL? prompt would have been outputted to the lower screen and, assuming a positive response, the upper screen would be scrolled up 1 line, a blank line inserted at the bottom of the upper screen, and the "A" printed at the start of the new line.

Graphics I/O using pixel coordinates is supported in the primary display file by the PLOT, DRAW and CIRCLE commands. The Home ROM module GRAPHS contains the major routines which implement these commands. They are limited to the 22 lines of the upper screen (256 X 176 pixels).

Figure 4.1.2-2 shows the internal representation used to designate row (line) and column positions. See Section 2.1.10 for details on the organization of the Display Pixel and Attribute Files. See Section 5.2 for details on software support necessary for the advanced video modes.

FIGURE 4.1.2-2

DISPLAY FILE ROW/COLUMN NOTATION

BASIC Parameters	Internal Representation
Line/Row 0	24 (18H)
1	23 (17H)
.	.
.	.
21	3
-----	-----
22	2
23	1
	UPPER SCREEN
	LOWER SCREEN
Column 0	33 (21H)
1	32 (20H)
.	.
.	.
31	2

4.1.3 2040 Dot Matrix Printer

Character output to the 2040 Printer is handled by the same routine used for the screen, SENDTV. When the Printer Flag=1, set by initialization for device "P", the pixel data is written into the Print Buffer instead of into the Display File. There is no Attribute Byte. The "attributes" OVER and INVERSE which are software controlled can be active. Since the Print Buffer is always precleared to zeros, OVER has no effect. INVERSE works exactly as it does for the screen, i.e. INK pixels are zero and PAPER pixels are 1.

The Print Buffer is located at 23296 (5B00H) and is 256 (100H) bytes long, the data needed to print one line of 32 characters, each character comprised of 8 bytes (8 X 8 pixels/character). The buffer is cleared to zeros and the flag PRLEFT set to zero at power-on time (or execution of a NEW command). The PRLEFT flag is set to 1 whenever pixel data is written to the buffer. This flag is used when exit is made from a program to print any unprinted data prior to program termination. As the pixel data for a particular character is entered into the buffer, the buffer address is incremented by 32 (20H); the sequential data in the buffer therefore represents 8 complete scan lines of 32 characters. When the Print Buffer is full, or upon processing an End-of-Line (ODH), or at program termination, the contents of the buffer are written to the Printer, the buffer is cleared and the PRLEFT Flag is set to zero.

Printer I/O is done via Port OFBH, but the Printer responds to any I/O Read/Write with Address Bit 7=1 and Address Bit 2=0. Therefore, any Port providing this combination, e.g. Ports OFA through OF8 and Ports OF3 through OF0 as well as others, will interface to the Printer. See Section 2.1.13.3 for the bit definitions for Printer I/O. The pixel data is written to the device by the routine PRSCAN in module IO_2 of the Home ROM which outputs 1 scan line (32 bytes), one bit at a time on each call to the routine.

There are two controlling routines for output to the printer. DUMPPR is called from SENDTV based on buffer full or End-of-Line control. This routine will call PRSCAN 8 times to output the 256 bytes of the Print Buffer (8 scan lines). The other routine is K DUMP which implements the COPY command. This routine calls PRSCAN 176 times to write the contents of the primary display file for the main screen to the printer (8 X 22). All of the low level print routines are in module IO_2 of the Home ROM.

4.2 Cassette Tape

Tape I/O is done via Port OFEH. An I/O read of Port OFEH pulls in the cassette input on Bit 6. An I/O write of Port OFEH Bit 3 controls the tape output with Bit 3 = 1 generating a high output and Bit 3 = 0 generating a low output.

Data is written to the tape under software control creating the following frequencies and format:

- Sync Pattern of 4032 cycles at 806.5 Hz. (5 sec.)
- Header: 17 bytes of data identifying the following data block as either Program, Number Array, Character Array, or Binary Code and containing other control information.

The header is written as Data, i.e. the Most Significant Bit first in each byte, 1 cycle at 2040 Hz. for a Zero and 1 cycle at 1020 Hz. for a One. The first byte is zero identifying the header. The final byte is a Checksum calculated by XOR of all preceding data bytes.

- Software delay of approximately 835 milliseconds.
- Sync Pattern of 1612 cycles at 806.5 Hz. (2 secs.)
- Transition Pattern of 1 cycle at 2400 Hz.
- Data Block: Written as Data (see above) with first byte = -1 (FFH) and a final Checksum byte.

Figure 4.2-1 shows the header formats for the various types of data.

The routines used to actually write and read the tape (W_TAPE and R_TAPE) are in the TAPE Module of the Extension ROM (see map in Appendix A). They are accessible via the Extension ROM Interface Routine listed in Figure 3.2.2-2. The general flow required to write a header and data block is:

1. Call W_TAPE with A=0. IX contains the address of the header and DE contains the length.
2. Delay loop approximately 1 second.
3. Call W_TAPE with A=FFH. IX contains the address of the data block and DE contains the length.

The R TAPE routine performs either a LOAD (transfers data from tape to memory) or VERIFY (compare data from tape against data in memory) operation, based on the status at entry: Carry Set for Load and No Carry if Verify. As for the Write, A=Block Type (0 for Header and -1 (FFH) for Data Block). IX contains the memory address.

The tape routines return Carry=1 for successful completion and No Carry for error or Break Key detected. Both W TAPE and R TAPE exit via the routine W BORD which restores the Border color based on bits 3-5 of the system variable BORDCR. If the Break Key is detected during this exit routine, a RESTART 8 (ERROR) is executed.

NOTE: The write to Port OFEH in the exit routine restoring the Border Color has bit 3 = 0. This creates a final transition on the tape following a write operation. This transition is necessary in order to successfully read back the final data bit from some tape recording devices. If you are calling the W TAPE routine so as to bypass the normal exit path, you must perform this final write to Port OFEH with Bit 3 = 0 within a similar timeframe.

Addendum to R TAPE routine: Register DE must contain the length of the block to be read (DE=17 for the Header, and DE=HDLEN for Data). See Fig. 4.2-1 for a definition of HDLEN.

FIGURE 4.2-1

TAPE HEADER FORMATS

	HDDTYPE(1)	HDNAME(10)	HDLEN (LSB/MSB)	HDADD (LSB/MSB)	HDVARS (LSB/MSB)
PROGRAM	0	Up to 10 ASCII Chars.	Length of Program + Variables (E LINE - PROG)	Starting Line No. or 8000H E.G.: 0500=Line 5 or 0080H if no Line No.	Length of Pro- gram = Offset to Variables) (VARS) - (PROG)
NO.ARRAY	1	"	Length Field from Data Structure	LSB=00 MSB=Array ID 7.....0 ----- 100 (ASCII - 60H)	N/A(=0)
CHAR.ARRAY	2	"	Length Field from Data Structure	LSB=00 MSB=Array ID 7.....0 ----- 110 (ASCII - 60H)	N/A(=0)
CODE (BINARY)	3	"	Length Specified in SAVE	Address Specified in SAVE	N/A (=0)

4.3 Joysticks

The two joysticks are controlled via Register 14 (I/O Port A) of the Programmable Sound Generator Chip (see Sections 2.1.6 and 2.1.7). Address and data are passed via Ports 0F5H and 0F6H respectively. The joysticks are read by first addressing Register 14 in the PSG by writing a 14 (0EH) to Port 0F5H. The data is then read by executing an IN from Port 0F6H, having the port address in Z80 Register C and the joystick (player) number in Register B (number = 1 or 2). Note that PSG Register 7, Bit 6 is assumed to be zero, enabling I/O Port A for input. If you ever use I/O Port A for output (R7,B6=1), you will want to clear Bit 6 prior to any input operation.

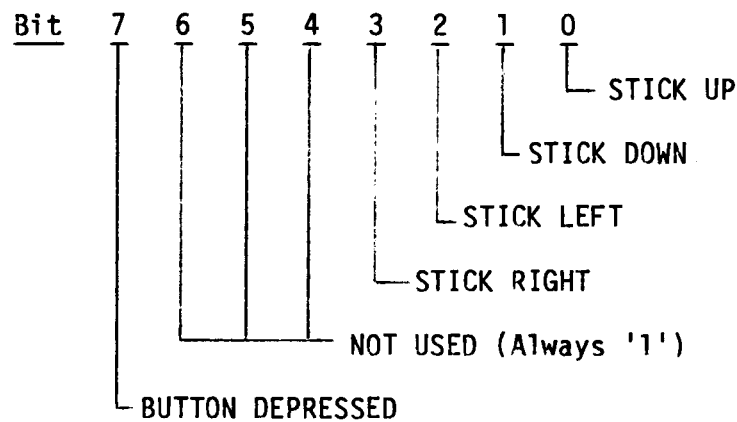
Sample routine:

GETJOY	LD	A,0EH	Load A = 14
	OUT	A,(OF5H)	Address the joystick port
	LD	B,playerno	
	LD	C,OF6H	Data Port address to C
	IN	A,(C)	Joystick data to A
	CPL		Complement to High Active
	AND	8FH	Get significant bits

The data read is LOW ACTIVE, i.e. all bits = 1 (byte=FFH) when the stick is at center and the button is not depressed. Figure 4.3-1 shows the interpretation of the data byte.

FIGURE 4.3-1

JOYSTICK DATA



4.4 S/W Generated Sound (BEEP)

The BEEP command produces sound using the speaker by toggling Bit 4 of I/O Port OFEH to generate a signal of a calculated frequency and duration based on the command parameters. It uses the routine PARP which takes as input two parameters, one defining the period of the signal (HL) and the other defining the number of cycles to be generated (DE) and outputs DE+1 cycles of a tone having the period $8N+235$ to $8N+246$ T-States where $(HL) = N$. Both the BEEP and PARP routines are in the K_SCAN module of the Home ROM. The PARP routine is also used to generate the keyboard "click" and the "raspberry" which can be varied by modifying the values in the system variables PIP (23609/5C39H) and RASP (23608 5C38H).

4.5 Sound Chip (SOUND)

The SOUND command writes the first parameter (register number) to Port OF5H (address to Programmable Sound Generator) and the second parameter (load data) to Port OF6H (data to PSG). The program line is scanned for multiple parameter pairs and continues writing address/data pairs to the PSG until the end of the statement is reached. See Section 2.1.6 for details on the hardware of the PSG.

5.0 Advanced Concepts

5.1 Cartridge Software/Hardware

5.1.1 LROS

An LROS is identified by the following overhead bytes:

<u>Location</u>	<u>Description</u>
0000	Not Used
0001	Cartridge Type 01=LROS
0002/0003	Starting Address (LSB/MSB) Address to be jumped to after Operating System initialization is complete. Order of bytes is as for a JP instruction.
0004	Memory Chunk Specification. Bits 0-7 represent Chunks 0-7 respectively in the Dock Bank in <u>low active</u> format: 0 if in use 1 if not in use

NOTE: When writing to the Horizontal Select Register (Port F4H), the Chunk Specification is High Active

The Memory Chunk Specification is used to enable the specified chunks in the Dock Bank prior to jumping to the address specified in Location 2 and 3. Control is transferred from the Initialization code in the Extension ROM via the GOTO BANK routine in Home Bank RAM Chunk 3, therefore Bit 3 of the Memory Chunk Specification must be set to 1 in order for the transfer to be accomplished as designed (Chunk 3 also contains the Machine Stack).

CAUTION: If Chunk 3 is marked for use in the Dock Bank, then when the Memory Chunk Spec. is written to Port F4H by the Bank Enable code, execution will continue from that point in Chunk 3 in the Dock Bank with the Stack Pointer addressing ROM.

An LROS is Z80 machine code and is in complete control of the TS 2068 hardware after transfer to the starting address has been made. It can directly implement an application, or it

can support multiple applications by implementing a language other than BASIC. An AROS dependent on such an LROS would have to be part of the same cartridge since there is only one cartridge connector.

Interruption Mode 1 has been set by the TS 2068 and interruptions are enabled prior to passing control to the LROS starting address, therefore the LROS must contain appropriate code at location 56 (38H) to cover the case where the interruption occurs after Chunk 0 in the Dock Bank has been enabled, but before any action by the software cartridge to disable the interruption has been taken. Once control is transferred, the LROS may then disable the standard TS 2068 interruption by setting bit 6 of Port FFH, mask the interruption by executing a DI instruction, or set a different Interruption Mode. It may change the location of the Machine Stack. It may also change the memory selection by writing to Port OF4H with each bit set to 1 for the corresponding chunk to be enabled in the Dock Bank (high active format) or 0 to be enabled in the Home Bank. Thus, an LROS may contain code in Chunk 3, but it should be enabled after the OS RAM code has finished execution.

Now that your LROS is in the driver's seat, you are on your own! Some important points to remember when mapping your Dock Bank memory and doing bank switching are:

1. The Display RAM is in Home Bank Chunk 2 for the primary display file and Chunk 3 for the second display file. This memory is accessed independently by the video hardware. The software only needs to enable it when actually reading or writing it.
2. The Dock Bank and Extension ROM Bank are mutually exclusive since they share the Horizontal Select Register in Port F4H. You will need a routine in the Home Bank RAM to do any switching between the two. You must also be careful to have the appropriate Home Bank Chunks enabled which are referenced by the Extension ROM code, e.g. the System Variables in Chunk 2 or possibly the bank switching code in Chunk 3.
3. Some interesting switching routines can be constructed by having parallel code in shadowing chunks of memory to take advantage of the "instant" switch in execution from one bank to another when the memory selection is made. E.g., a routine in the Dock Bank ROM in Chunk 6 could push a Home Bank address on the stack, write to Port F4H enabling Chunk 6 and any other desired chunks in the Home Bank (by deselecting them in the Dock), and have code at the next sequential instruction address in Home Bank RAM Chunk 6 to continue the path. A Return

instruction, for example, would pass control to the address on the stack. Code to switch memory back to the Dock Bank could be mapped in a similar way.

4. If you plan to use any of the System software routines, unless you know otherwise it is probably necessary to maintain the contents of Home Bank Chunks 2 and 3 intact (and Chunk 7 if the OS RAM routines have been relocated). The system routines rely heavily on the System Variables and assume that any pointers in them are pointing to the Home Bank. See Section 3.3.4.1 for details on using the RAM Interruption Handler and Section 6.0 for known corrections when using System S/W.
5. If you design an LROS implementing a higher-level language and want to support an AROS application, you must design your own initialization code to detect the presence of such an AROS. The TS 2068 will not look for the presence of an AROS if an LROS is present, therefore there will be no entry for the AROS in the System Configuration Table. Note that since there is only one cartridge connector, such an AROS would also have to be integrated with the supporting LROS in a single cartridge or cartridge board.

5.1.2 AROS

An AROS is identified by the following overhead bytes:

<u>Location</u>	<u>Description</u>
32768 (8000H)	Language Type 1 = BASIC [and machine code] 2 = Machine code only (Any other value will result in Error S, Missing LROS)
32769 (8001H)	Cartridge Type 2 = AROS
32770/32771 (8002/8003H)	Starting Address(LSB/MSB) BASIC AROS = Addr. of First Program Line Machine Code AROS = Addr. of First Z80 Instruction
32772 (8004H)	Memory Chunk Specification Bits 0-7 represent Chunks 0-7 respectively in the Dock Bank in <u>low active</u> format as follows: 0 if in use 1 if not in use NOTE: Bits 0-3 must be set to 1 for proper execution.
32773 (8005H)	Autostart Specification 0 = No Autostart 1 = Autostart
32774/32775 (8006/8007H)	Number of bytes of RAM to be Reserved for Machine Code Variables (LSB/MSB - 0100H=1 byte Reserved; 0002H=512 bytes Reserved.

5.1.2.1 BASIC AROS

A BASIC AROS is supported by special code in the System ROM (Section 3.2.1.2). The portion of the cartridge containing BASIC program lines is restricted to the upper half of the memory space beginning at location 32776 (8008H) in the Dock Bank. Support for User-Defined Functions, which requires searching for

the definition parameters within the program, is not implemented. Also, because the support code interfaces directly to the bank switching code in Home RAM Chunk 3 (does not allow for it to be relocated to Chunk 7), a BASIC AROS cannot utilize the advanced video modes and also execute BASIC program statements. If the cartridge contained machine code supporting advanced video modes, the TS 2068 would have to be returned to "Normal" video mode with the RAM mapped accordingly (see Figure 1.1-3) if control were to be returned to the BASIC Interpreter USR code.

Since execution of the cartridge BASIC program is done by copying program lines to a buffer in the Home Bank RAM (ARSBUF), the most efficient cartridge execution is obtained by making program lines as large as possible, i.e. making use of the multi-statement feature of the TS 2068. The reverse is true concerning execution of READ commands. An entire DATA statement is copied to the Home Bank RAM, but only the current item is accessed. It therefore will be more efficient to not make DATA statements excessively long. The BASIC program lines appear in the cartridge in exactly the same format used in the RAM, i.e. Line Number (2 bytes), Length (2 bytes), Command Token, etc. terminated by an Enter (ODH). Numerical constants appearing in a program line are followed by the CHR\$ (OEH) byte and 5-byte floating point format described in the User Manual (see Appendix C of the TS 2068 User Manual). The Variables area is built in the RAM (address in VARS) exactly as though the program were in the RAM. All variables, including arrays, are built at the time of program execution - there is no provision for copying or accessing pre-defined variables from the cartridge, however, see Section 5.3.2. The last program line must be followed by a terminator byte having the Most Significant Bit set (e.g. 80H), otherwise the Interpreter cannot detect the end of the program.

A BASIC AROS may contain machine code accessed via the USR function. If the machine code address is within the memory designated by the AROS Memory Select Specification as "in use", the Dock Bank will be enabled, otherwise the machine code address is assumed to be in the Home Bank. (See Section 6.0 for details on known problems in this area of the code.) Obviously, once control is transferred to the machine code in the AROS, the ball is now in your court. You could have additional machine code residing in the lower half of the Dock Bank memory space which you can now switch in. You only have to know what you're about. If and when you are ready to go back to

executing your BASIC program, you must enable Chunks 0-3 in the Home Bank and have the stack and other Home Bank RAM in the proper state for return to the USR function code in the BASIC Interpreter, i.e. what it was when the USR function passed control to you.

The Autostart feature begins execution out of the BASIC AROS immediately after system initialization. If the Autostart parameter is zero, control will go to the BASIC Interpreter as if there were no cartridge installed, although internal flags have been set noting that a BASIC AROS is present. The cartridge will be started when you execute a RUN or GOTO Line Number command.

The final parameter in the overhead bytes allows you to reserve RAM beginning in Chunk 3 at Location 26688 (6840H) for machine code and/or machine code variables. The designated number of bytes are reserved by the AROS support code prior to beginning program execution. The AROS buffer (ARSBUF) begins immediately following this reserved area (see Fig. 1.1-3). Note that this area is part of the RAM that gets relocated if the second display file is opened. Therefore access to your machine code and/or variables should be conditional on the video mode rather than direct if you are going to be using the advanced video modes. This reserved area begins at 31488 (7B00H) when the second display file is open. Remember -- use of the second display file and execution of BASIC program from the cartridge are mutually exclusive.

The standard technique of reserving space for machine code by modifying RAMTOP could also be used to place machine code/variables at the top of the Home Bank RAM. If you place code above (RAMTOP) which is to be accessed via the BASIC USR function, the affected memory chunk(s) cannot be marked as "in use" in the cartridge in the AROS Memory Selection Specification.

5.1.2.2 Machine Code AROS

A machine code AROS is similar to an LROS with the exception that it is dependent on the System ROM for interruption handling if the interruption is enabled. This implies that Chunks 0-3 are enabled in the Home Bank.

The Autostart parameter should be set to 1 since if it is zero, control will be passed to the BASIC Interpreter as if the cartridge were not present. There is no BASIC command to directly start execution of a Machine Code AROS.

Because of a "bug" in the Initialization code handling a Machine Code AROS, the parameter specifying the number of bytes to be reserved for machine code variables must be adjusted by adding 21 (15H) to the actual number of bytes needed. This preserves the 21 byte CHANS area starting at 26688 (6840H). The reserved area then starts at 26709 (6855H) (or 31488 (7B15H) when the second display file is open). Access to the variables should be conditional based on the video mode rather than direct if you plan to use the advanced video modes. If you do not plan to utilize any of the system software, you can disregard the above and "do your own thing" with the RAM.

See Section 6.0 for known corrections when using System S/W.

5.1.3 EPROM Cartridge Board Application

Figure 5.1-1 provides the logic diagram for a pluggable EPROM cartridge board capable of configuring up to four 16K-byte (128K-bit) EPROM's of the 27128 type. The artwork for the PC board implementing that logic diagram is provided in Figures 5.1-2, 5.1-3 and 5.1-4 for the Component Side art, the Solder Side art, and the Solder Mask (one common mask for both sides), respectively.

See Section 2.4.2 for mechanical details of the connector portion of the PCB.

FIGURE 5.1-1
 PLUGGABLE EPROM CARTRIDGE BOARD
 LOGIC DIAGRAM

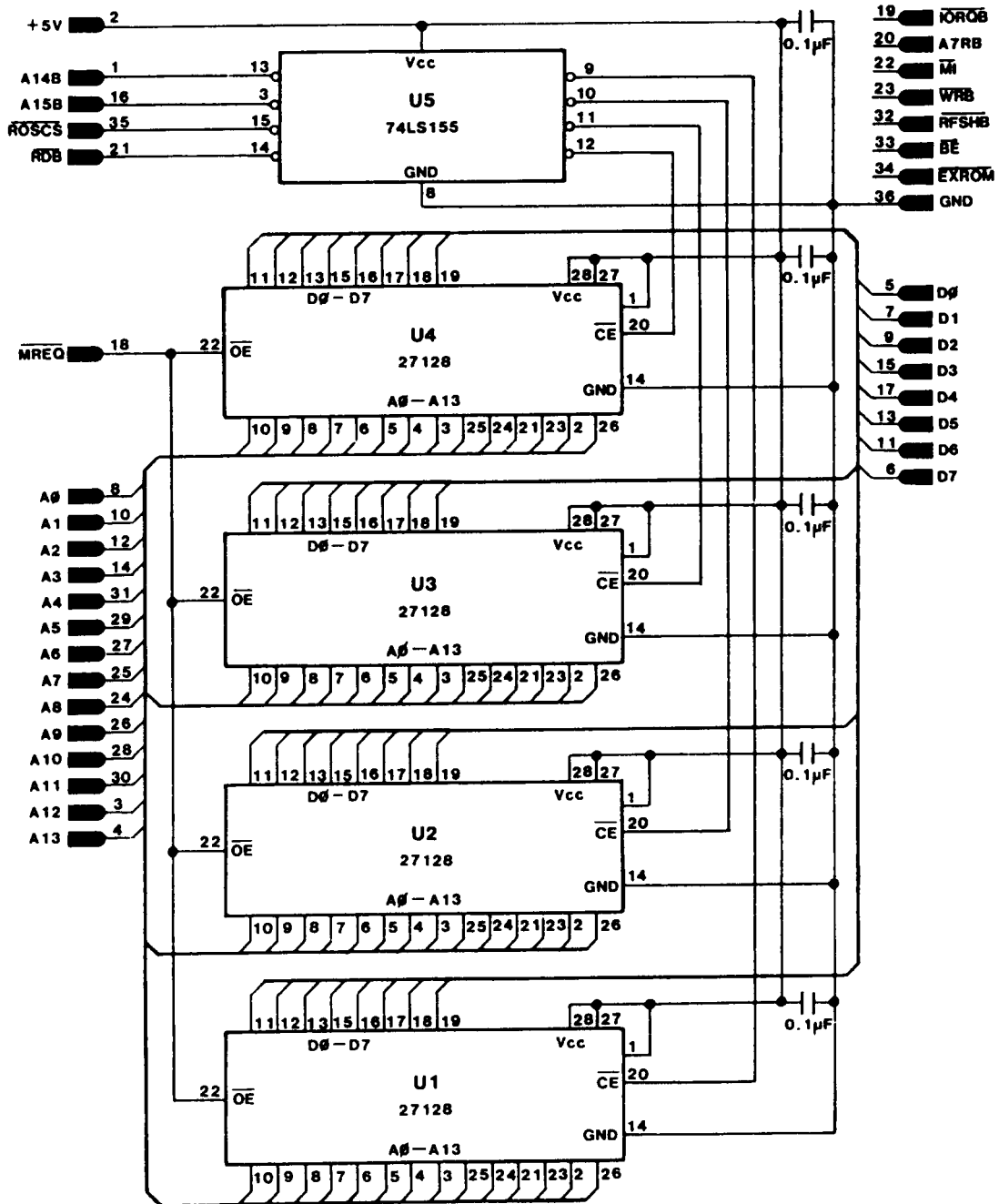
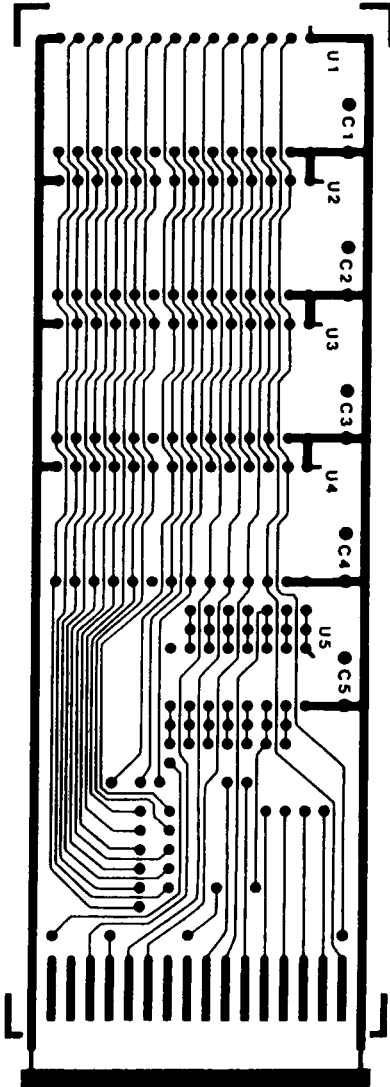


FIGURE 5.1-2
EPROM CARTRIDGE BOARD
COMPONENT SIDE ARTWORK



COMPONENT SIDE
SK2000-81

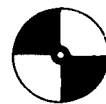
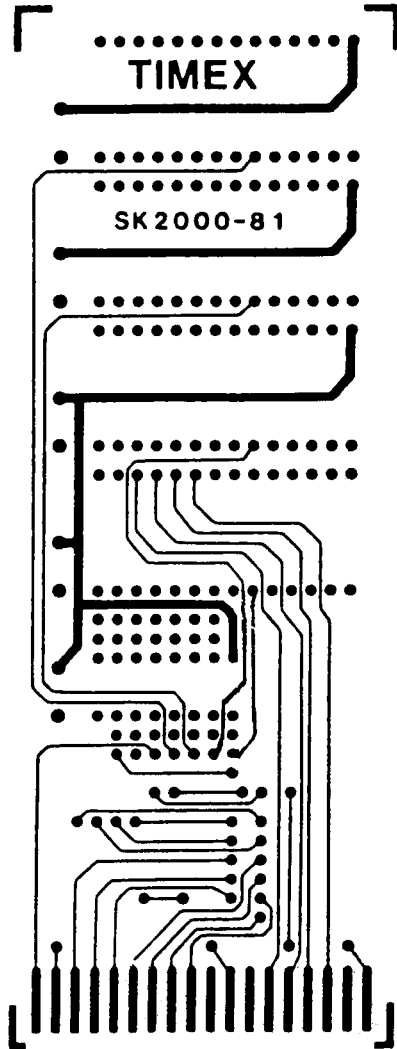
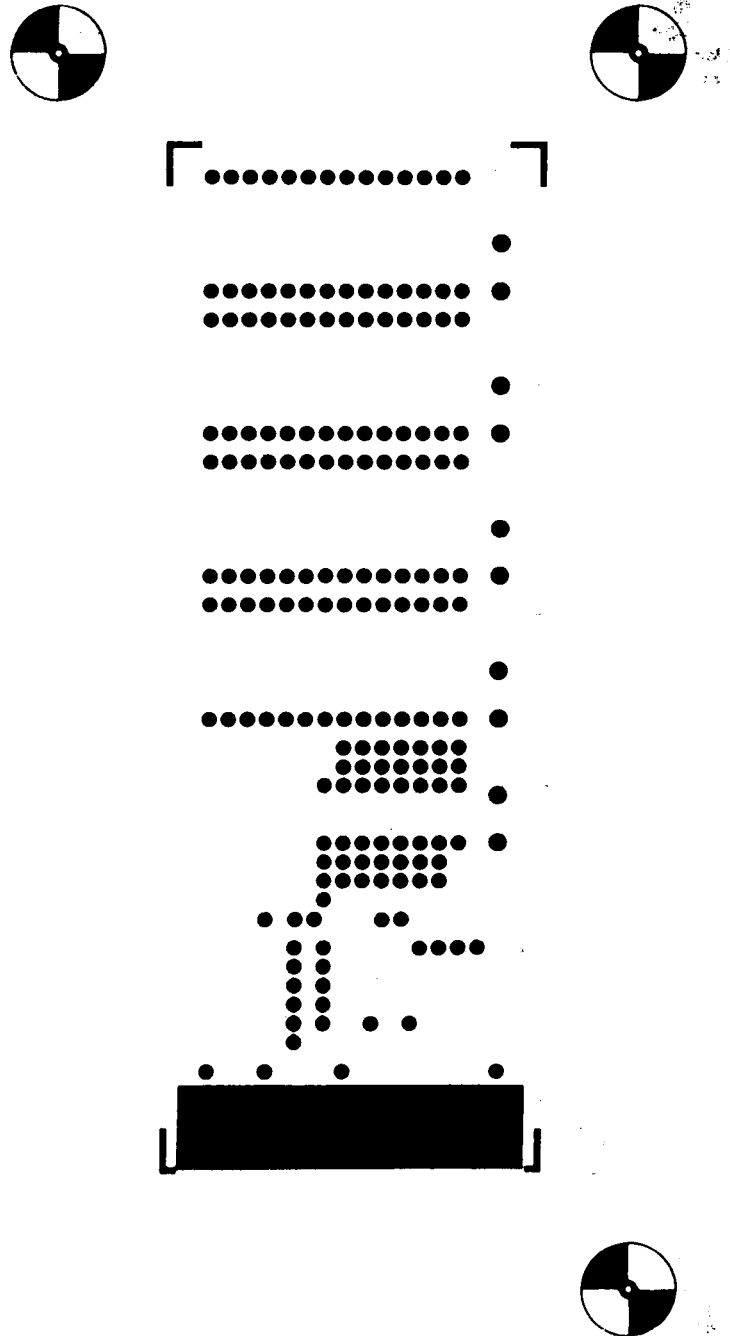


FIGURE 5.1-3
EPROM CARTRIDGE BOARD
SOLDER SIDE ARTWORK



SOLDER SIDE
SK2000-81

FIGURE 5.1-4
EPROM CARTRIDGE BOARD
SOLDER MASK



SOLDER MASK
SK2000-81

5.2 Advanced Video Modes

The following sections describe the various video modes available on the TS 2068 and the major software support functions necessary. See Sections 3.2.2.3 and 3.2.2.4 for details on using the Video Mode Change Service. Appendix C contains descriptions and code listings for a number of software packages developed by Timex that support various screen modes and applications. Reference to these packages should aid in gaining an understanding of the software techniques needed to support the video mode hardware.

The TS 2068 video mode hardware works out of two areas of RAM, the primary display file at 4000H and the second display file at 6000H. Each area consists of 6912 (1B00H) bytes used for pixel and/or attribute data based on the mode selected via bits 0-5 of Port FFH. The pixel data area divides into three blocks, each supporting 8 contiguous lines on the screen. See Section 2.1.10 for details on organization of the display RAM. Because the two display files occupy the same relative positions within their respective 8K Chunks, by setting/clearing Address Bit 13 a software routine can address the corresponding location in each file:

Address Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00	
	0	1	0	X	X	X	X	X	X	X	X	X	X	X	X	X	DF1
	4000 - 5AFFH (Bit 13 = 0)																

Address Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00	
	0	1	1	X	X	X	X	X	X	X	X	X	X	X	X	X	DF2
	6000 - 7AFFH (Bit 13 = 1)																

In order to display a character on the screen, 8 bytes of pixel data must be entered into the display file, one for each scan row. For a particular character position, the scan rows are 100H bytes apart. E.g, the 8 bytes of pixel data for position Line 0/Column 0 are located at 4000H, 4100H, 4200H,.....,4700H. Since this is the first character position on the screen, its Attribute byte, in Normal Mode, is the first byte in the Attribute File which starts at 5800H. The 768 (300H) Attribute Bytes are in sequential order starting at position 0/0 through 0/31,1/0 through 1/31, and so forth, ending with 23/0 through 23/31.

One method of determining the starting display file address for a particular line/column position is to build a table containing the starting address of each of the 24 lines (2 bytes per entry). Then construct an algorithm that takes the

line number and forms an index by multiplying it by 2 (shift left 1), add the index to the base address of the table, and read out the display file address. The column position is then simply an offset added to this address. By testing VIDMOD (23746 - 5CC2H) you can determine whether to set Bit 13 for the second display file, e.g. because you are in an odd column in 64-column mode, or simply because you are using the second display file in dual screen mode.

The following example illustrates this method. The table entries are in Hex:

LINE #	INDEX	TABLE		
		LSB	MSB	
0	0	00 40	4000H =	Line 0 (Top of Screen)
1	2	20 40		Line 1
2	4	40 40		Line 2
.	.	(+20H)		
.	.	(+20H)		
7	14(0EH)	E0 40		Line 7 (End of Upper Block)
8	15(10H)	00 48	4800H =	Line 8 (Top of Middle Block)
9	18(12H)	20 48		Line 9
.	.	(+20H)		
.	.	(+20H)		
15	30(1EH)	E0 48		Line 15(End of Middle Block)
16	32(20H)	00 50	5000H =	Line 16(Top of Bottom Block)
17	34(22H)	20 50		Line 17
.	.	(+20H)		
.	.	(+20H)		
23	46(2EH).....	E0 50		Line 23(End of Bottom Block)

Line 17, Column 23 (11H/17H) would yield a display file address of 5020H + 17H = 5037H. If VIDMOD indicated the second display file was to be used, setting Bit 13 of the address would yield 7037H. If we were using 64-column mode, because the column is odd (Bit 0=1) we would set Bit 13 of the starting line address getting 7020H, then divide the column address by 2 (shift right 1) since there are only 32 columns in each display file. This would give us an offset of 11 (0BH) which added to the starting address results in a display file address of 702BH. Having the display file address, we now insert the 8 bytes of pixel data for the character desired, incrementing the display file address by 100H between each write (this is easily done by simply incrementing the upper register of the register pair containing the address). The following routine is a simplified version illustrating this process. It assumes that Reg. Pair DE contains the address of the desired character in the character table and that HL contains the address of the desired position in the display file.


```

      .
      .
      .
      LD   B,8           Set Scan Count
LOOP  LD   A,(DE)       Get pixel pattern
      LD   (HL),A       Write to Display File
      INC  DE           Next pixel pattern byte
      INC  H            Next DF Position (+100H)
      DJNZ LOOP        Continue for 8 Scan Rows
      .
      .
      .

```

Finally, we must update the Attribute Byte controlling the updated character position. The following sample algorithm will formulate the Attribute File address given the address of any of the scan rows of the character position. We will assume we have saved off the starting display file address and now have it in Register Pair HL.

```

GETATT  LD   A,H           MSB of DF Address
        RRCA           Shift right circular
        RRCA           to get Bits 3&4 (Block #)
        RRCA           to positions 0&1
        AND  3          Clear other bits
        OR   58H        OR in Attr.File Base Adrs.
        LD   H,A        Update MSB

```

NOTE: The LSB is the same as for the pixel data.

Using our first example, with a Display File address of 5037H, the Attribute File address would be 5A37H. The second example was using 64-Column Mode which does not require attribute file update (attributes determined by video mode setting).

See Section 5.2.2 for a sample algorithm to formulate the display file address for X,Y pixel coordinates. The above routine for calculating Attribute File address would be substituted for the method used in the example if not working in High Resolution Graphics mode.

In addition to data insertion, two major screen support functions are scrolling and clearing the screen. Scrolling is done in the System ROM by copying the entire display file data and attribute controls up one line position (Line 1 to Line 0, Line 2 to Line 1, etc.) and inserting a blank line at the bottom. Numerous more elaborate scrolling techniques can be implemented using various directions (up, down, left,

right) and smaller areas or "windows" of the screen. Similarly, clearing the screen, which consists of writing zeros to the data file and updating the attribute bytes to a uniform value, can be implemented on smaller sections of the screen. The software packages in Appendix C contain examples of such implementations.

5.2.1 Dual Screen Mode

In this mode the second display file is used to provide a second independent screen having the same data and attribute organization as the primary display file. By writing to Port FFH with Bits 0-5 = 1 (Bit 0 set), the second display file is activated at the video screen. Appendix C contains a software package supporting Dual Screen Mode. The software package uses the system variable VIDMOD to determine which display file is the target of the current operation. Special values for VIDMOD have been defined to permit building of one display file while the other is active at the screen so that a complete screen image is ready when the hardware mode is changed. Copy and Exchange routines have been provided to move data within and between the two display files. This enables the BASIC graphics commands like PLOT, CIRCLE and DRAW, which work only in the primary display file, to be used to create screens which are then moved into the second display file.

Because the System ROM works only in the primary display file, you can come up with some unusual situations when you have the second display file active at the screen and you are executing BASIC or using the System ROM routines. If an error occurs, for example, the error message will be placed into the primary display file and the ROM will be waiting for input from the keyboard to direct the next action, but all of this is invisible since you have the other display file active. The machine will appear to be "hung", but it is only doing its normal thing. Be prepared to enter a OUT 255,0 to an invisible command line in order to switch the display back to the standard file!!! Don't forget to also set VIDMOD (POKE 23746,128) to keep things consistent inside the dual screen support code.

5.2.2 High Resolution Graphics Mode

This mode is set by writing to Port OFFH with Bits 0-5=2 (Bit 1 set). In this mode, also called Extended Color Mode, the second display file is used to expand the number of Attribute bytes from one for each 8 X 8 pixel group to one for each 8 X 1 pixel group thus giving 32 X 192 positions within each of which two colors plus Bright and Flash can be defined. Each byte of pixel data entered into the primary display file has

its own Attribute byte in the corresponding location in the second display file, e.g. the byte written to Location 4000H has its Attribute byte at Location 6000H, the byte at 47FFH (last byte of last scan row in Line 7) has its Attribute byte at Location 67FFH, the byte at 57FFH (last byte of last scan row in Line 23) has its Attribute byte at Location 77FFH. The routine writing data to the screen would therefore enter the pixel data to the desired location and then set Address Bit 13 of the Primary Display File address and write the desired attribute control byte to the resultant location. If normal characters are being written to the screen in this mode, eight Attribute bytes must also be written, one for each of the bytes defining the character. The same technique would be used for writing to both display files, i.e. for each of the seven bytes entered after the first, the display file address would be incremented by 256 (100H).

The System ROM graphics commands (PLOT, DRAW and CIRCLE) place data into the Primary Display File and update the Attribute File associated with the standard video mode (5800H-5AFFH). In High Resolution Graphics Mode, the hardware does not access this area for attribute control, therefore its contents have no visible effect. If before or immediately following execution of the BASIC graphics operation, you update the attribute control information in the second display file, you could possibly take advantage of the System ROM graphics capability. Admittedly, this is not a simple operation in the case of circles or drawing diagonal lines and it will be more efficient to develop code specifically to support this video mode.

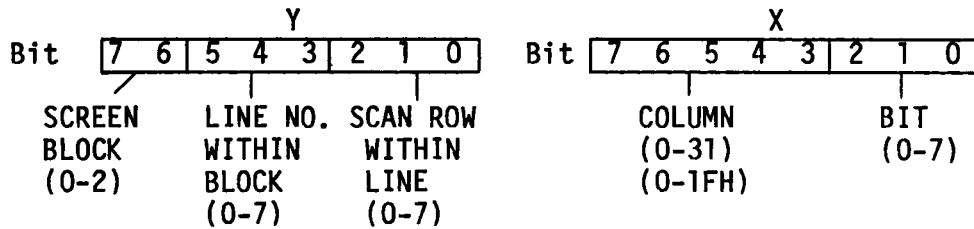
The following sample routine takes as input two single byte binary digits representing the X and Y coordinates of a pixel position on the screen. It formulates the display file address of the byte containing the pixel, creates a pattern or mask byte for the specified bit position, sets the bit in the display file, and updates the attribute byte (High Resolution Graphics Mode assumed). This represents a simplified version of the approach used in the System ROM graphics support routines PLOTBC and SCRMBL.

The two inputs are assumed to be as follows:

- Reg. C = X Coordinate 0-255 (0-FFH) going left to right across the screen.
- Reg. B = Y Coordinate 0-191 (0-BFH) going from bottom to top of the screen.

NOTE: This covers the full vertical range of 192 positions.

The Y Coordinate is checked for valid range and reversed directionally so that 0 represents the top of the screen and 191 represents the bottom. After this reversal, the two coordinates represent the following values:



We first formulate the MSB of the display file address using the Block and Scan Line information in the Y Coordinate:

PLOTXY	PUSH (SAVECO),BC	Save coordinates
	LD A,191	Test Y within range
	SUB B	
	JP C,ERROR	Y coordinate beyond range
	LD B,A	Y Coordinate now 0=Top
	AND 0COH	Get Block No. (0-2)
	RRA	Shift Bits to Pos. 3&4
	RRA	
	RRA	
	LD H,A	Save Block Bits
	LD A,B	Y Coordinate
	AND 07	Get Scan Row Bits
	OR H	Combine Block and Scan Row
	OR 40H	Base Address of DF (4000H)
	LD H,A	H = MSB of DF Address

Next we formulate the LSB of the display file address using the Line information from the Y Coordinate and the Column information from the X Coordinate:

	LD A,C	Get X Coordinate
	RLCA	Align to Pick Up Line
	RLCA	Bits from Y
	RLCA	A=2 LS Bits Column/XXX/3 MS
;		Bits Column
	AND 0C7H	Clear Bits 3-5
	LD L,A	Save A in L
	LD A,B	Get Y Coordinate
	AND 38H	Get Line Bits
	OR L	Combine with Col.Bits
	RLCA	Shift to Final Position
	RLCA	A=Line #/Column
	LD L,A	L = LSB Display File Addr.

Next we get the pixel position within the byte by taking the last 3 bits of the X Coordinate and create a mask byte having all bits zero except the addressed pixel. This mask is then used to set the bit in the Display File. The address is set to Display File 2 to update the Attribute File (High Res. Graphics Mode is assumed to be active), and the routine is finished. The memory locations defined as ATTR and SAVECO are for illustration purposes only:

```

                LD    A,C           Get Pixel Position
                AND   7           0=Leftmost (MSB);7=
;                                     Rightmost (LSB)
                LD    B,A           Use as Control Count
                INC   B           B=1-8
                LD    A,00000001B  Bit Mask
LOOP RRCA           Rotate Mask Bit
                DJNZ  LOOP         to Proper Position
                OR    (HL)         OR Bit into DF
                LD    A,20H
                OR    H           Set Bit 13 for DF2
                LD    H,A           HL = Attribute File
                LD    A,(ATTR)     Get Attribute Byte
                LD    (HL),A       Update Attribute File
                POP   BC           Original X/Y to BC Regs.
                RET

```

Repetitive calls to this routine with the appropriate X/Y Coordinate values will "draw" on the screen. The System ROM routines for drawing lines and circles calculate the successive X/Y Coordinate values and use common low-level routines similar to the above to place each pixel in the display file.

5.2.3 64-Column Mode

In this mode, set by writing to Port OFFH with Bits 0-2=6 (Bits 1 and 2 set) and Bits 3-5 selecting ink color (0-7), the pixel data portions of the two display files are merged by the hardware on an alternating column basis to produce 64-columns across the screen. All even columns (0,2,4....62) are derived from the primary display file and all odd columns (1,3,5.....63) are derived from the second display file. There are still 24 lines vertically from top to bottom. The attributes are controlled by bits 3-5 written to Port FFH selecting one of eight ink/paper combinations. The Bright and Flash attributes are fixed at 0 and the Border is fixed to match the paper color. The Attribute Files in RAM at 5800H-5AFFH (primary display file) and 7800H-7AFFH (second display file) are not utilized in this mode.

Software supporting this mode must set up the display file address for character insertion based on the column position (even=DF1; odd=DF2). When scrolling the screen (or a portion of it), any line of text on the screen requires the same operation to be done at the corresponding locations in each display file. This is also true to clear the screen (or a portion of it). To save a Screen on tape you must save two Code files, one for each display file. The SAVE filename SCREEN\$ will work for the Primary Display File only. You will have to specifically SAVE the second display file via a SAVE filename CODE 24576,6144. Note also that because the Border color is fixed by the video mode, you will not see the usual "stripes" during a tape operation.

Code to support an 80-column mode screen was developed utilizing the 64-column hardware mode and redefining the character size to a 6 X 8 pixel group (there is really room for 84 characters if the full 256 pixel width is used). Since individual characters now can span the two display files (e.g. 2 pixels in DF1 and 4 in DF2) insertion of data into the display files involves masking the 6-bit character (or portion thereof) with the 8 bits of data read/written from/to the display file.

Appendix C contains descriptions and code listings of software packages supporting 64 and 80-Column modes.

5.2.4 Other

Appendix C also contains software packages supporting the following video screen features:

- A. 40-Column Mode - utilizes the 6 X 8 character set defined for 80-Column Mode in "normal" mode. May be combined with the Dual Screen package.
- B. Sprites - supports movement of software-defined objects and multi-directional screen scrolling services in the Primary Display File. You must create the actual bit map defining the shape of your sprite(s), but this package does the rest.

5.3 Other Advanced Concepts

5.3.1 Interruption Fielding

For a machine code program executing in the Home RAM, you can intercept the 17 ms. interruption for your own purposes by permanently enabling Chunk 0 in the Extension ROM Bank (write a 1 to Port OF4H and always have Bit 7 of Port OFFH = 1) and inserting at Location 25262 (62AE Hex) a branch to your own interruption handler. (Or if VIDMOD is not zero, insert your branch instruction at Location 64110 (FA6EH).) By doing this you are forcing the interruption to branch to the RAM and then bypassing the OS RAM Interruption Handler - see Sections 3.2.2.1 and 3.3.3.1. Because the Video Mode Change Service automatically updates internal branch addresses in the OS RAM code when it is relocated between Chunk 3 and Chunk 7, you probably do not want to directly overlay the OS RAM Interruption Handler with your own code if you will be using the Video Mode service. Your branch instruction at 62AEH, however, will be copied unmodified to location FA6EH in Chunk 7 and vice versa.

Note that this technique cannot be used if you are using BASIC since then you must have Chunk 0 enabled in the Home Bank. It also cannot be used from a cartridge because the memory selection hardware (Port OF4H) is common to the Dock and Extension ROM Banks and can only enable one of them at a given time as selected by Bit 7 of Port OFFH.

5.3.2 BASIC AROS Variables

In order to use pre-defined arrays and/or other BASIC variables, store them in the cartridge (possibly in the lower half of the addressable space which is not usable for BASIC program) and branch to a machine code routine via the USR function at the beginning of your BASIC AROS program. Use this routine to do the necessary memory selection and copy your data from the cartridge to the RAM (address in VARS). Adjust the System Variables E LINE, WORKSP, STKBOT and STKEND to all point to the first free memory following your BASIC variables. Of course, all BASIC variables must conform to the format expected by the BASIC Interpreter. In addition to BASIC structures, you can also store screen images and machine code/variables in the cartridge for transfer to the RAM under your control. Consider using the XFER_BYTES service in the OS RAM.

6.0 Known "BUGS" and Corrections

This section describes the known problems in the TS 2068 System Software and gives corrections or work-arounds where these have been defined.

6.1 LROS and Autostart Machine Code AROS

6.1.1 If you will be using the System ROM Keyboard routines and accessing the input character code from system variable LAST_K (5C08H), you must initialize the TS 2068 to "L" mode by setting the system variable MODE at 23617(5C41H) to zero and setting Bit 3 of FLAGS (23611-5C3BH) to 1. (The TS 2068 is in "K" mode when control is passed from System Initialization to the Cartridge; Keyword Token codes will be placed in LAST_K instead of character codes.)

6.1.2 If you will be using the System ROM Calculator routines (RESTART 40 (28H)) or any ROM routines that invoke them, you must initialize the System Variable MEM by doing the following:

```
LD    HL,5C92H          Set HL=MEMBOT
LD    (5C68H),HL       Initialize MEM
```

6.1.3 Chunk 3 must not be designated as "in use" by the Cartridge Memory Selection Specification byte. This will cause deselection of the bank switching code prior to completion of the transfer of control to the cartridge starting address. Once control has been transferred, the cartridge code may then enable Chunk 3 in the Dock Bank if desired. (See Section 5.1.)

6.1.4 No entry is made in the System Configuration Table for an AROS if an LROS is present. This means that an LROS designed to support either RAM based or cartridge based applications must include code for detection of an AROS.

6.2 Machine Code AROS

When setting the AROS Overhead parameter requesting RAM space for machine code variables, $21 + n$ bytes ($15H + n$) must be requested where n is the number of bytes needed. The machine language variables area then starts at 6855H immediately following the 21-byte CHANS area. (See Section 5.1.2.3.)
NOTE: This does not apply to an AROS that contains both BASIC and machine code.

6.3 BASIC AROS

- 6.3.1 USR Function - When testing the USR address against the Cartridge Memory Selection byte to determine if the address is in the Home Bank or the Dock Bank, the wrong nibble is tested in the register thus a valid cartridge address could be erroneously processed as a Home Bank address. Since the ROM code cannot be corrected, the machine code in the cartridge would have to be moved to an address that does not cause a problem.
- 6.3.2 FOR/NEXT - If the limit of the FOR statement has already been passed on its initial execution, (e.g. FOR A=1 TO 10 and A has been set to 12), control is passed to the statement following the corresponding NEXT. In the AROS support code, the address of this statement is lost giving unpredictable results. Since the ROM code cannot be corrected, care must be taken not to use this technique in an AROS Cartridge. Normal usage of FOR/NEXT loops is not affected.
- 6.3.3 Advanced Video Modes - Because the BASIC AROS support code interfaces directly to the Bank Switching code in Chunk 3 (does not access based on its relocatability), the second display file cannot be open when executing BASIC program from an AROS.

6.4 Video Mode Change Service

- 6.4.1 Available Memory Test - When the size of memory needed is calculated by adding the size of the second display file (6912 bytes or 1B00H) to the memory now in use (address in System Variable STKEND), the code fails to check for overflow. Thus if the address in STKEND is greater than 58623 (E4FFH), the fact that there is not enough free memory to open the second display file will not be detected and the system will "crash". If your BASIC program and/or variables area are large, you may want to make this test yourself prior to invoking the Video Mode Change Service in order to avoid this problem. The size of memory needed is subsequently tested against the contents of RAMTOP and if there is not sufficient space (value in RAMTOP is less than size needed), you will get Error 4, Out of Memory.

6.4.2 RAMTOP - When the machine stack and OS RAM code is moved to Chunk 7, the User Defined Graphics area is moved down in RAM by 2112 bytes (840H) to make room for the stack and OS RAM routines at the top of memory. The pointer in UDG is updated, however, the value in RAMTOP is not modified to insure that the relocated UDG area as well as the OS code and stack are protected from expansion of the BASIC program. You can avoid problems by setting RAMTOP via a CLEAR command specifying an address no greater than 63255 (F717H) prior to invoking the Video Mode Change Service. This reserves space between RAMTOP and the end of memory of 2280 bytes (8E8H) utilized as:

168 bytes (A8H)	User Defined Graphics (21 X 8)
2112 bytes (840H)	Machine Stack and OS Routines
<u>2280</u>	<u>(8E8H)</u>

Example:	RAMTOP = 63255	(F717H)
	+ Reserved Area	2280 (08E8H)
		<u>65535 (FFFFH)</u>

The software packages in Appendix C are written assuming that RAMTOP is set to 57343 (DFFFH) or lower to protect the machine code which is loaded beginning at 57344 (E000H).

6.4.3 NEW Command - If you have used the Video Mode Change Service to open the second display file and now wish to execute the NEW command, you should first return the computer to "normal" mode by calling the video mode service with A=zero. This returns the User Defined Graphics and other RAM structures to their normal locations. If you don't do this, the UDG area will remain in the alternate location and, if you have not corrected RAMTOP as explained above, part or all of your UDG area could be cleared to zeros by the NEW command.

6.4.4 VIDMOD - When Mode 128 (80H) is designated for activating the Primary Display File in Dual Screen Mode the System Variable VIDMOD at 23746 (5CC2H) is set to zero instead of to 128. This creates a potential problem if the 17 ms. interruption occurs before VIDMOD can be corrected since the interruption fielder will branch to Chunk 3 instead of to Chunk 7 and Chunk 3 is now in use for the second display file. This problem is corrected by disabling the interruption prior to calling the Video Mode Change Service and setting VIDMOD to the correct value prior to re-enabling it. These corrections are included in the Extension ROM Interface Routine in Figure 3.2.2-2.

NOTE: On an initial access changing video mode from normal to Mode 128, the interruption is re-enabled within the Video Mode Change Service itself after copying the stack and other Chunk 3 data to Chunk 7. This cannot be corrected, but has not proven to present a problem in actual use. At the point where the interruption is first enabled, the Chunk 3 code is still intact allowing for correct processing of one interruption, and the path length from there to the point of correcting VIDMOD is apparently less than 17 ms. The interruption is also re-enabled within the Video Mode Change Service if you have applied the patches for the BANK_ENABLE and RESTORE_STATUS routines (Section 6.5.4) which are executed in connection with inserting space into the RAM to open the second display file. Again, this has not proven to be a problem in actual use.

6.4.4 Interruption Inhibit - By setting Bit 6 of Port OFFH to a 1, the normal 17 ms. interruption generated from the SCLD to the Z80A CPU will be inhibited. When Port OFFH is written to by the Video Mode Change Service, Bit 6 is forced to zero. If you wish to inhibit the normal interruption via this mechanism, and also plan to use the Video Mode Change Service, it is recommended that you first invoke the service to remap the RAM and open the second display file, then set Bit 6 of Port OFFH to inhibit the normal interruption and write your own routine(s) for subsequent changing of the video mode setting that do not involve remapping the RAM. In this way you can maintain the value in Bit 6.

6.5 OS RAM Routines

In patching the OS RAM routines, care must be taken not to relocate CALL and JP instructions since this affects the modification of the code when it is moved between Chunks 3 and 7. All of the code containing actual addresses must be modified to reflect the relocation and this is done using a table in the Extension ROM. Since the table cannot be changed, none of these instructions can be moved. Also, any CALL or JP instructions added must be modified by you when the code is relocated.

6.5.1 Function Dispatcher -For a variety of reasons such as conflict with use of the IX Register, incorrect entries in the ROM Function Dispatcher Jump Table, etc. some Service Codes have been deleted from the Function Dispatcher table (Table 3.3.4-2). In addition, the following correction to the GET_STATUS routine is required in order to successfully utilize the Function Dispatcher from a cartridge.

6.5.2 GET_STATUS- Returns invalid memory selection status for the Home Bank, ROM Extension and Dock. This results in switching out of either the Home Bank or the Dock when status is "restored". This affects use of the Function Dispatcher and GET WORD routines, and any other code using GET_STATUS. Figure 6.5-1 shows the patches and additions necessary to correct this routine.

6.5.3 PUT_WORD- Write data passed in Reg. Pair DE is overwritten prior to use. Figure 6.5-2 shows corrections.

6.5.4 BANK_ENABLE and RESTORE_STATUS-

If the 17 ms. interruption occurs during update of the memory selection hardware, it can cause the system to hang and RAM to be overwritten. This occurs when the interruption happens in an interval when Port FF Bit 7 is zero (thus selecting the Dock Bank) and Port F4 Bit 0 is one (thus enabling Chunk 0 in the Dock Bank) and there is no memory in Chunk 0 of the Dock Bank. This can be true when there is no cartridge installed, or if the cartridge installed is an AROS. This problem is corrected by disabling or masking the interruption while updating the memory selection hardware. Figure 6.5-3 shows one implementation of this correction.

6.5.5 SAVE_STATUS and RESTORE_STATUS - The value of Port FFH which includes video mode and interruption inhibit as well as Ext. ROM/Dock Select is saved and restored as a full 8-bits. Therefore any modification of this port by code accessed between execution of SAVE_STATUS and subsequent execution of RESTORE_STATUS (e.g. via CALL_BANK or use of the Function Dispatcher) is "undone". This is one reason the Video Mode Change Service and some of the bank switching routines such as BANK_ENABLE cannot be meaningfully accessed via the Function Dispatcher.

6.5.6 CALL_BANK- Does not correctly retrieve the stack entry designating the count of parameters being passed. Memory is overwritten in the case where this count is not zero. This is corrected by setting Location 6610H = 9 (POKE 26128,9). You only need to apply the correction once; it will be duplicated in Chunk 7 if the code is relocated.

FIGURE 6.5-1
GET_STATUS CORRECTIONS

LOCATION (HEX)	OBJ.CODE (HEX)	SOURCE STATEMENT	COMMENTS
		Input: Bank # in B	
		Output: Bank # in B (Bank Status if Exp.Bank) Memory Selection in C (Low Active Format)	
6405	F5	GET_STATUS PUSH AF	Save Regs.
6406	D5	PUSH DE	
6407	78	LD A,B	Get Bank #
6408	FEFE	CP OFEH	Test if Ext.(254)
* 640A	2824	JR Z,GS_EXT	
640C	FEFF	CP OFFH	Test if Home(255)
* 640E	2837	JR Z,GS_HOME	
6410	A7	AND A	Test if Dock (0)
* 6411	2827	JR Z,GS_DOCK	
6413	.	.	
.	.	.	(Code for Expansion Banks not applicable)
.	.	.	
.	.	.	
* 6430	OEFF	GS_EXT LD C,OFFH	Assume none
* 6432	DBFF	IN A,(OFFH)	Test if selected
* 6434	E680	AND 80H	
* 6436	2812	JR Z,GS_XT1	Not active
* 6438	1808	JR GETHS	Get Hor.Select
* 643A	OEFF	GS_DOCK LD C,OFFH	Assume none
* 643C	DBFF	IN A,(OFFH)	Test if selected
* 643E	E680	AND 80H	
* 6440	2008	JR NZ,GS_XT1	Not active
* 6442	DBF4	GETHS IN A,(OFFH)	Get Hor.Select Reg.
* 6444	2F	CPL	Invert to Low Active
* 6445	1802	JR GS_XT0	Exit
* 6447	DBF4	GS_HOME IN A,OFFH	All bits set are not active in Home Bank
* 6449	4F	GS_XT0 LD C,A	Memory Select to C
644A	D1	GS_XT1 POP DE	Restore Regs.
644B	F1	POP AF	
644C	C9	RET	Return

The asterisks mark the locations modified. See next page for list of corresponding POKE's for BASIC.

FIGURE 6.5-1
GET_STATUS CORRECTIONS
(continued)

From BASIC:

POKE 25610,40	(Location 640AH)
POKE 25611,36	
POKE 25614,40	(Location 640EH)
POKE 25615,55	
POKE 25617,40	(Location 6411H)
POKE 25618,39	
POKE 25648,14	(Location 6430H)
POKE 25649,255	
POKE 25650,219	
POKE 25651,255	
POKE 25652,230	
POKE 25653,128	
POKE 25654,40	
POKE 25655,18	
POKE 25656,24	
POKE 25657,8	
POKE 25658,14	
POKE 25659,255	
POKE 25660,219	
POKE 25661,255	
POKE 25662,230	
POKE 25663,128	
POKE 25664,32	
POKE 25665,8	
POKE 25666,219	
POKE 25667,244	
POKE 25668,47	
POKE 25669,24	
POKE 25670,2	
POKE 25671,219	
POKE 25672,244	
POKE 25673,79	

FIGURE 6.5-2

PUT_WORD CORRECTIONS

LOCATION (HEX)	OBJ.CODE (HEX)	SOURCE STATEMENT	COMMENTS
Input: Data in DE, Address in HL, Bank # in B			
633B	F5	PUT_WORD PUSH AF	Save Regs.
633C	C5	PUSH BC	
633D	CD5E64	CALL GET_NUMBER	Bank # of Owner
* 6340	D5	PUSH DE	Save Data
6341	50	LD D,B	Save Target Bank #
6342	47	LD B,A	Bank # of Owner
6343	CD0564	CALL GET_STATUS	Get Bank Status
6346	C5	PUSH BC	Save It
6347	CD4D64	CALL GET_CHUNK	Get Bit Map
634A	2F	CPL	Set High Active
634B	42	LD B,D	Target Bank # to B
634C	4F	LD C,A	Memory Select Byte
634D	CD9964	CALL BANK_ENABLE	Enbl.Target Mem.
* 6350	C1	POP BC	Saved Bank Status
* 6351	D1	POP DE	Saved Data
* 6352	73	LD (HL),E	Write LSB
* 6353	23	INC HL	Increment Adrs.
* 6354	72	LD (HL),D	Write MSB
* 6355	2B	DEC HL	Restore HL
6356	CD9964	CALL BANK_ENABLE	Restore Bank St.
6359	C1	POP BC	Restore Regs.
635A	F1	POP AF	
635B	C9	RET	Return

The asterisks mark the locations modified.

From BASIC:

```
POKE 25408,213
POKE 25424,193
POKE 25425,209
POKE 25426,115
POKE 25427,35
POKE 25428,114
POKE 25429,43
```

NOTE: The corrections to GET_STATUS and BANK_ENABLE are also required.

FIGURE 6.5-3

BANK_ENABLE AND RESTORE_STATUS CORRECTIONS

BANK_ENABLE:	Location	Object Code	From BASIC	
			POKE Address	Value
	6499H	00 NOP	25753	0
	649DH	F3 DI	25757	243
	651CH	FB EI	25884	251

RESTORE_STATUS:

654AH	F3	DI	25930	243
6570H	FB	EI	25968	251

In both cases, the Disable Interrupt and Enable Interrupt are being done by deleting the preservation of the AF Registers (PUSH AF/POP AF). If your code requires AF to be saved, you must do it prior to calling either of these routines or any other system routines that use them. Note also that if you already have the interruption masked when these routines are entered, it will be enabled when they are exited. If this proves to be a problem, replace the Enable Interruption (EI) instruction with a NOP and do the enable at a more appropriate place in your own code.

6.5.6 GET_NUMBER- Always returns the Dock Bank # for any memory enabled in the ROM Extension. Unlikely to be a problem because of limited use of the ROM Extension.

6.5.7 XFR_BYTES- Improperly passes memory select byte for the case where source and destination are in the same bank. This is corrected by setting Location 676AH = 5FH (POKE 26474,95).

6.6 GENERAL

6.6.1 Pressing ENTER multiple times with an invalid tape command on the edit line (syntax error) causes the system to reset. This is due to overflowing the Bank Status Stack in RAM Chunk 3/7 due to the multiple calls to and from the Extension ROM via the Call Bank code without normal termination (the error causes a RESTART 8 to be executed out of Home ROM code called from the ROM Extension). It shouldn't take anybody that many tries to get a tape command right, so this is not a real problem, but you may want to keep it in mind. For any call made through the OS RAM services, you should have a corresponding return to keep the structures clean.

6.6.2 ON ERR GOTO - If a non-existent line number is specified, followed by an error, the system will hang. The ROM code is in an endless loop trying to report the absence of a valid error handler to the non-existent error handler!!! On some errors, you will get an unexpected 0 OK termination showing the line number of your Error Handler. This is because some ROM routines temporarily clear the INTPT Flag (Bit 7 of FLAGS). This flag is set to 0 when checking syntax and set to 1 when executing; if an error is detected while the Flag=0, the error handler code is branched to but is not executed.

6.6.3 Parameters to the SOUND command are not fully validated, therefore you can specify a number beyond the valid range for a given operation and not get an error, for example, you can write a value greater than 63 to the Enable Register (Reg.7), possibly changing the I/O Port used for reading the joysticks from input to output. If you specify a number larger than 255 (FFH), only the least significant byte will be actually written to the Programmable Sound Generator. Access to PSG Reg. 14 (IO-A) used for the Joysticks is also not precluded via the SOUND command.

If you experience difficulty in reading the joystick(s), do a write to PSG Reg. 7 clearing Bit 6 to 0 to guarantee that the joystick path is enabled for input (see Section 4.3). This write can be done by executing a SOUND 7,63 (or any value less than 63).

The INTEGER function for (-65536) gives an incorrect result of -1, and for other cases where the result should be -65536, it gives -1E-38. Since the ROM code cannot be changed, there is no correction.

6.6.4 If you respond to the SCROLL? message using multiple keys such as Cap Shift/2 or Cap Shift/Symbol Shift, you will get strange results like dumping of the Edit Line with the "C" or "E" cursor, display of ROM data, or multiple scrolls. Stick to single key responses and you won't have any problems!

6.6.5 When DELETE (Cap Shift/0) is held down to do deletion of characters in the Edit Line, sometimes it outputs the DELETE Keyword instead (it should not do this in auto-repeat mode). This is especially noticeable when the input line is long. Since the ROM code cannot be corrected, you must try releasing and pressing the DELETE key at differing frequencies and you will be able to get past this "Bug".

APPENDIX A

HOME ROM MAP

LINK 1.7			DATA	1E82	SYNTAX
LOAD MAP			DEF	201D	SYNTAX
MODULE	ORIGIN	LENGTH	DELREC	1750	LIST
BLOCK	0000	0000	DELSYM	0B7E	IO_2
BASIC	0000	0227	DEL_DE	174D	LIST
KSCAN	0227	02D9	DEL_K	0BFD	IO_2
IO_1	0500	0502	DESLUG	0D0D	IO_2
IO_2	0A02	031B	DE_HL	1668	LIST
EDIT	0D1D	0682	DIGIT?	30D9	INOUT
CHANS	139F	0142	DIM	2FC0	IDENT
LIST	14E1	02D4	DIVIDE	356E	SUMS
AROS	17B5	0190	DRAW	26DB	GRAPHS
SYNTAX	1945	080A	DRAWLN	2813	GRAPHS
SYNTWO	214F	04B4	DRAW_L	2810	GRAPHS
GRAPHS	2603	0251	DUMPPR	0A23	IO_2
EXPRN	2854	041C	DYADIC	1BDC	SYNTAX
IDENT	2C70	03E9	ECHO	0C83	IO_2
INOUT	3059	0301	EDIT_K	0A82	IO_2
SUMS	325A	032A	END?	1B44	SYNTAX
CALC	3684	0437	ENDSTT	1AB9	SYNTAX
FUNCTS	3ABB	01CE	ENDTEM	1B4A	SYNTAX
TAPEMSG	3C89	0053	ERASE	25D4	SYNTWO
CH_SET	3D00	0300	ERR2	1B91	SYNTAX
			ERR4	1FCF	SYNTAX
			ERR5	07C1	IO_1
GLOBAL	ADDRESS	MODULE	ERR6	356C	SUMS
ACS	3C5E	FUNCTS	ERRB	1F29	SYNTAX
ADD	33D3	SUMS	ERRH	237E	SYNTWO
ALNUM?	3046	IDENT	ERRO	123D	EDIT
ALPHA?	304B	IDENT	EXECUTE	1AD8	SYNTAX
ANGLE	3B9E	FUNCTS	EXP	3ADF	FUNCTS
AROS	18C6	AROS	EXPRN	2854	EXPRN
ARRAY	37C5	CALC	FIND_L	16D6	LIST
AR_LN	17EA	AROS	FIND_LN	2C70	IDENT
AR_NXT	17FF	AROS	FIX_U	1F23	SYNTAX
ASN	3C4E	FUNCTS	FIX_U1	1F1E	SYNTAX
ATN	3BFD	FUNCTS	FLASHA	160D	LIST
ATTBYT	0710	IO_1	FLOAT	3656	SUMS
BEEP	0436	KSCAN	FOR	1C78	SYNTAX
BORDER	243E	SYNTWO	FORMAT	25CC	SYNTWO
BREAK?	2009	SYNTAX	FP2A	3193	INOUT
CAT	25C8	SYNTWO	FP2BC	3160	INOUT
CHCODE	0371	KSCAN	F_ATTR	28D7	EXPRN
CHINIT	11AA	EDIT	F_INKY	29F2	EXPRN
CHK_SZ	1FBB	SYNTAX	F_PI	29E5	EXPRN
CIRCLE	2679	GRAPHS	F_PNT	2624	GRAPHS
CLCHAN	13BE	CHANS	F_SCRN	288E	EXPRN
CLEAR	1F36	SYNTAX	GETAL	17CF	AROS
CLEL	133F	EDIT	GET_LEL	2D54	IDENT
CLLHS	08A9	IO_1	GET_LN	1324	EDIT
CLOSE	139F	CHANS	GET_LY	2660	GRAPHS
CLPR	0A35	IO_2	GO_SUB,	1F99	SYNTAX
CLR_BC	1F39	SYNTAX	GR_COL	239C	SYNTWO
CLS	08EA	IO_1	HIFLSH	241D	SYNTWO
CLS_B	097F	IO_1	INCH	11E1	EDIT
COLITM	23A6	SYNTWO	ININT	30F9	INOUT
COLOUR	23DE	SYNTWO	INIT	0D31	EDIT
CONT	1EE4	SYNTAX	INPUT	222B	SYNTWO
COS	3BC5	FUNCTS	INSI	12B8	EDIT
CP_BC	16E8	LIST	INSA	0AE7	IO_2
CTRO	371A	CALC	INSERT	12BB	EDIT
			INT	3ACA	FUNCTS
			INTDIV	3ABB	FUNCTS

INTPT?	2889	EXPRN	REMGSZ	12CA	EDIT
IN_LK	0C0E	IO_2	RESET	1354	EDIT
I_SEQ	226B	SYNTWO	RESTBC	1ECA	SYNTAX
JUMP	1EF1	SYNTAX	RETURN	1FD4	SYNTAX
K_BASE	035C	KSCAN	RND	29B6	EXPRN
K_CLS	08A6	IO_1	ROOM?	3768	CALC
K_DUMP	0A02	IO_2	ROOT	3C65	FUNCTS
K_LIST	1545	LIST	RSET	2454	SYNTWO
K_LLLST	1541	LIST	RSTSTR	13A8	CHANS
K_LPR	2155	SYNTWO	R_ATT5	0888	IO_1
K_NEW	0D1D	EDIT	SCRL	0939	IO_1
K_PRIN	2159	SYNTWO	SCRMBL	2603	GRAPHS
K_SCAN	02B0	KSCAN	SEARCH	136B	EDIT
LCU2	132D	EDIT	SELECT	1230	EDIT
LDDE	313D	INOUT	SEL_HL	1248	EDIT
LDMES	3CA8	TAPEMSG	SENDCH	11ED	EDIT
LDTVCU	061A	IO_1	SENDTV	0500	IO_1
LE3	0055	BASIC	SEPRMT	3C89	TAPEMSG
LED18	0E2F	EDIT	SETCUR	0914	IO_1
LED4	0E8D	EDIT	SETTVC	0914	IO_1
LET	2EBD	IDENT	SET_AT	05B2	IO_1
LINENO	1768	LIST	SHIFT	339C	SUMS
LIST	14E1	LIST	SIN	3B0D	FUNCTS
LN	3B2E	FUNCTS	SKIP	1D28	SYNTAX
LPO	15AC	LIST	SKIPIT	2569	SYNTWO
LS4	1A44	SYNTAX	SLICER	2E10	IDENT
LT22	1BBC	SYNTAX	SMINIT	11C1	EDIT
MOVE	25D0	SYNTWO	SOUND	2128	SYNTAX
MULT	3468	SUMS	SRCHSC	1374	EDIT
NC_HL	0077	BASIC	STBOOL	3926	CALC
NEGATE	382D	CALC	STDE_S	314C	INOUT
NEW	0D82	EDIT	STDE_U	314A	INOUT
NEWDEV	24D2	SYNTWO	STKUSN	3059	INOUT
NEXT	1D55	SYNTAX	STK_0	1C51	SYNTAX
NEXTCH	0074	BASIC	STK_A	30E6	INOUT
NEXT_L	165B	LIST	STK_BC	30E9	INOUT
NOTKB?	2380	SYNTWO	STK_M	3773	CALC
NXT_HL	2C69	EXPRN	STOP	1C59	SYNTAX
OPCHAN	1465	CHANS	STRITO	220F	SYNTWO
OPEN	142A	CHANS	STTVCU	05F3	IO_1
OPTNO	1C49	SYNTAX	SUB	33CE	SUMS
OUTPUT	31A1	INOUT	SUBLIN	16F0	LIST
PAEDCB	2E74	IDENT	SUBLN1	16F3	LIST
PARP	03F3	KSCAN	SUMSLD	3379	SUMS
PASSEM	25B9	SYNTWO	SYNERR	1BED	SYNTAX
PAUSE	1FEB	SYNTAX	SYNTAX	1A27	SYNTAX
PHLAF	004F	BASIC	TAN	3BF5	FUNCTS
PLOT	2635	GRAPHS	TC_HL	0078	BASIC
PLOTBC	263E	GRAPHS	TEM1	1B82	SYNTAX
PLUGIN	0000	BASIC	TEM10	1BEF	SYNTAX
POPSTR	2FAF	IDENT	TEM6	1BE5	SYNTAX
PRSCAN	0A4A	IO_2	TEMP38	19E0	SYNTAX
PR_CUR	162D	LIST	TEMP39	19E1	SYNTAX
PR_TV2	0776	IO_1	TERM?	21E7	SYNTWO
PSHSTR	2E70	IDENT	TEST0	3904	CALC
PUT	15C9	LIST	TIMES	3489	SUMS
PUTDIG	11EA	EDIT	TOKENS	0098	BASIC
PUTMES	073F	IO_1	TO_THE	3C6C	FUNCTS
PUT_BC	1788	LIST	TRUNC	35D3	SUMS
PUT_LN	1795	LIST	TVFUL?	0790	IO_1
PUT_SR	15A1	LIST	TV_COL	23BB	SYNTWO
P_LFT	053A	IO_1	UPD_K	02E1	KSCAN
P_NL	0566	IO_1	USRRET	3882	CALC
P_RT	0554	IO_1	WRCH	0010	BASIC
P_SEQ	217E	SYNTWO	XEY	310D	INOUT
RAMNO	377F	CALC	X_CALC	134E	EDIT
RAND	1ED4	SYNTAX	X_T_HL	1363	EDIT
RDCH	11CF	EDIT			
READ	1D97	SYNTAX			
RECLN	1720	LIST			

PROGRAM BLOCK -- 4000 BYTES
ENTRY: 0000

EXTENSION ROM MAP

LINK 1.7

LOAD MAP MODULE	ORIGIN	LENGTH
XBASIC	0000	0068
TAPE	0068	087F
INIT	08E7	04C9
CHNG_VID	0DB0	0193
PASSING	0F43	0047
BS	0F8A	001E

GLOBAL	ADDRESS	MODULE
AKEY	08AA	TAPE
BLDSCT	09F4	INIT
CALL_B	0F99	BS
CHNG_V	0E8E	CHNG_VID
CLDFIL	0E27	CHNG_VID
EXINIT	08E7	INIT
GOTO_B	0F8A	BS
LOAD	05CC	TAPE
MERGE	06E5	TAPE
OPDFIL	0DB0	CHNG_VID
PASSIN	0F43	PASSING
RD_BIT	0189	TAPE
RESSCT	0C4C	INIT
R_EDGE	018D	TAPE
R_TAPE	00FC	TAPE
SAVE	0851	TAPE
SLVM	01AB	TAPE
W_BORD	00E5	TAPE
W_TAPE	0068	TAPE

PROGRAM XBASIC -- 1000 BYTES
ENTRY: 0000

DISPATCH 1000 0624 THIS MODULE IS COPIED TO RAM 6200 (space reserved 6200-683FH).
GLOBAL ADDRESS MODULE Relocated to RAM F9C0-FFFFH when second Display File is used.

			ORIGIN	LENGTH
BANK_E	6499	DISPATCH		
BS_MAX	6315	DISPATCH		
BS_SP	65CE	DISPATCH		
CALL_B	65D0	DISPATCH		
CREATE	66E8	DISPATCH	TABLES: FIXTBL	1D00 007C
DISPAT	6200	DISPATCH		
GET_CH	644D	DISPATCH		
GET_NU	645E	DISPATCH		
GET_ST	6405	DISPATCH		
GET_WO	6316	DISPATCH		
GOTO_B	6572	DISPATCH		
GOTO_E	6815	DISPATCH		
INT	62AE	DISPATCH		
			UNUSED:	1624 06DC
				107C 0160

```

420 *LIST ON
421 *LIST ON
422 *INCLUDE NEW_SYSVAR.S
423 ! HERE ARE SOME NEW SYSTEM VARIABLE DEFINITIONS
424 !
425 !
426 STKSZ          EQU    200H
427 SADDPT         EQU    0F5H    !SOUND CHIP ADDR PORT
428 SDATPT         EQU    0F6H    !SOUND CHIP DATA PORT
429 HS             EQU    40H
430 HS_LSN        EQU    HS
431 BNA           EQU    80H
432 HS_MSN        EQU    BNA
433 ABN           EQU    0A0H
434 HSP           EQU    ABN    !HS' REG ADDR
435 STALL         EQU    ABN
436 CMD           EQU    0C0H
437 STA_0        EQU    CMD
438 LOWNYB        EQU    0C00H    !RESET NYBBLE STEERING LOGIC CMD
439 FREE_BYTES    EQU    32
440 PRM_OUT       EQU    8
441 HOR_SEL       EQU    10
442 BANK         EQU    11
443 UPD_K         EQU    02E1H
444 !
445 !
446 !
447 GLOBAL DISPATCH, INT, NMI, PUT_WORD, GET_WORD
448 GLOBAL WRITE_BS_REG, READ_BS_REG, GET_STATUS, GET_NUMBER
449 GLOBAL GET_CHUNK, BANK_ENABLE, GOTO_BANK, CALL_BANK
450 GLOBAL XFER_BYTES, BS_MAX_BANK, BS_SP
451 GLOBAL CREATE_BITMAP, MOVE_BYTES
452 !
453 ! DISPATCH (SVC_CODE: PASSED ON STACK)
454 !
455 ! SVC_CODE IS A 16 BIT QUANTITY. BIT 15 IS USED AS A JUMP FLAG! IF
456 ! SET, THE DISPATCHER WILL DO A GOTO_BANK TO THE SPECIFIED ROUTINE.
457 ! OTHERWISE IT WILL DO A CALL_BANK.
458 !
459 !
460 !
461 JMPTBL         EQU    1FFFH
462 LAST_EXT_SVC  EQU    13
463 LAST_RAM_SVC  EQU    24
464 !
465 !
466 !
6200                ORG    6200H
467 !
468 !
6200 DISPATCH      LD     IX, 0
6204 DD39           470 ADD  IX, SP    !IX = SP
6206 C5            471 PUSH B    !RESERVE A WORD ON THE STACK
6207 F5            472 PUSH AF   !SAVE REGS
6208 C5            473 PUSH HC
6209 D5            474 PUSH DE
620A E5            475 PUSH HL
620B DD5E02       476 LD     E, (IX+2)
620E DD5603       477 LD     D, (IX+3)    !DE = SVC_CODE
6211 AF           478 XOR  A
6212 CB23        479 SLA  E
6214 CB12        480 RL   D    !DE = 2*DE
6216 17          481 RLA  !A = JUMP FLAG
6217 210D00      482 LD     HL, LAST_EXT_SVC
621A CB25        483 SLA  L
621C CB14        484 RL   H    !HL = 2*HL
621E A7          485 AND  A
621F ED52       486 SBC  HL, DE !COMPARE HL AND DE
6221 3013       487 JR   NC, D_EXT !IF DE <= HL
6223 211800     488 LD     HL, LAST_RAM_SVC
6226 CB23       489 SLA  L
6228 CB14       490 RL   H
622A A7         491 AND  A
622B ED52       492 SBC  HL, DE
622D 380F       493 JR   C, D_HOME
622F 06FF       494 LD     B, 255 !HERE FOR RAM-BASED SERVICES
6231 CD0564     495 CALL  GET_STATUS !GET STATUS OF HOME BANK
6234 06FF       496 LD     B, 255 !BC = HOME BANK / HORIZ-SELECT
6236 180A       497 JR   D_SAVE
6238 06FE       498 LD     B, 254 !HERE FOR EXT. ROM BASED SERVICES
623A 0EFE       499 LD     C, 0FEH
623C 1804       500 JR   D_SAVE
623E 06FF       501 LD     B, 255 !SET BANK_ENABLE PARMS FOR HOME
6240 0E00       502 LD     C, 0
6242 F5         503 D_SAVE PUSH AF
6243 C5         504 D_SAVE PUSH BC
6244 21FF1F     505 LD     HL, JMPTBL !SAVE JUMP FLAG AND BANK_ENABLE PARMS
6247 37         506 SCF  !CALC. ADDR OF TABLE ENTRY
6248 ED52       507 SBC  HL, DE
624A 06FE       508 LD     B, 254
624C CD1663     509 CALL  GET_WORD !READ TABLE ENTRY
624F EB         510 EX   DE, HL
6250 C1         511 POP  BC
6251 F1         512 POP  AF
6252 A7         513 AND  A
6253 281F       514 JR   Z, D_CALL
6255 DD71FE     515 LD     (IX-2), C !PUT BANK# AND HOR-SEL ON STACK
6258 DD70FF     516 LD     (IX-1), B
625B DD6E00     517 LD     L, (IX) !SAVE RET ADDR
625E DD6601     518 LD     H, (IX+1)
6261 DD7403     519 LD     (IX+3), H !PUT RET ADDR BACK ON STACK

```

6264	DD7502	520	LD	(IX+2), L	
6267	DD7201	521	LD	(IX+1), D	!SET UP STACK FOR GOTO_BANK
626A	DD7300	522	LD	(IX), E	!PUT ADDR ON STACK
626D	E1	523	POP	HL	!RESTORE REGS
626F	D1	524	POP	DE	
6271	C1	525	POP	BC	
6270	F1	526	POP	AF	
6271	CD7265	527	CALL	GOTO_BANK	!HERE IF JUMP FLAG NOT SET
6274	DD6E00	528	LD	L, (IX)	!SET UP STACK FOR CALL_BANK
6277	DD6601	529	LD	H, (IX+1)	!PUT RET_ADDR IN PROPER LOC
627A	E5	530	PUSH	HL	
627H	DD6E04	531	LD	L, (IX+4)	
627E	DD6605	532	LD	H, (IX+5)	
6281	DD75FE	533	LD	(IX-2), L	
6284	DD74FF	534	LD	(IX-1), H	
6287	DD6E06	535	LD	L, (IX+6)	!PUT PRM_OUT IN PROPER LOC
628A	DD6607	536	LD	H, (IX+7)	
628D	DD7500	537	LD	(IX), L	
6290	DD7401	538	LD	(IX+1), H	
6293	DD7102	539	LD	(IX+2), C	!PUT BANK #, HS ON STACK
6296	DD7003	540	LD	(IX+3), B	
6299	DD7304	541	LD	(IX+4), E	!PUT ADDR ON STACK
629C	DD7205	542	LD	(IX+5), D	
629F	E1	543	POP	HL	
62A0	DD7506	544	LD	(IX+6), L	
62A3	DD7407	545	LD	(IX+7), H	
62A6	E1	546	POP	HL	!RESTORE REGS
62A7	D1	547	POP	DE	
62A8	C1	548	POP	BC	
62A9	F1	549	POP	AF	
62AA	CDD065	550	CALL	CALL_BANK	!HERE IF JUMP FLAG NOT SET
62AD	C9	551	RET		
		552			
		553			
		554			
		555			
					!RST 56: HERE TO SERVICE INTERRUPT BY READING KEYBOARD
62AE	F5	556	INT	PUSH AF	
62AF	E5	557		PUSH HL	
62B0	DDE5	558		PUSH IX	
62B2	210000	559		LD HL, 0	
62B5	39	560		ADD HL, SP	
62B6	D5	561		PUSH DE	
62B7	3A1563	562		LD A, (RS_MAX_BANK)	
62BA	5F	563		LD E, A	
62BB	1600	564		LD D, 0	
62BD	13	565		INC DE	
62BE	13	566		INC DE	
62BF	A7	567		AND A	
62C0	ED52	568		SBC HL, DE	
62C2	EB	569		EX DE, HL	
62C3	DD210000	570		LD IX, 0	
62C7	DD19	571		ADD IX, DE	
62C9	D1	572		POP DE	
62CA	DDF0	573		LD SP, IX	
62CC	CD1E65	574		CALL SAVE_STATUS	
62CF	C5	575		PUSH BC	
62D0	06FF	576		LD B, 0FFH	
62D2	CD0564	577		CALL GET_STATUS	
62D5	06FF	578		LD B, 0FFH	
62D7	79	579		LD A, C	
62D8	E6F8	580		AND 0FFH	
62DA	4F	581		LD C, A	
62DB	CD9964	582		CALL BANK_ENABLE	
62DE	C1	583		POP BC	
62DF	2A785C	584		LD HL, (FRAMES) !NOW INCREMENT FRAME COUNTER	
62E2	23	585		INC HL	
62E3	22785C	586		LD (FRAMES), HL	
62E6	7C	587		LD A, H	
62E7	B5	588		OR L	
62E8	2003	589		JR NZ, LIT3	
62EA	FD3440	590		INC (Y-Y+FRAME2)	
62ED	C5	591	LIT3:	PUSH BC	
62EE	D5	592		PUSH DE	
62EF	CDE102	593		CALL UPD_K	
62F2	D1	594		POP DE	
62F3	C1	595		POP BC	
		596	PHLAF:	!JUMP HERE TO POP HL, POP AF, ENABLE INTERRUPTS & RETURN	
62F4	DD210000	597		LD IX, 0	
62F8	DD39	598		ADD IX, SP	
62FA	CD4A65	599		CALL RESTORE_STATUS	
62FD	DD23	600		INC IX	
62FF	DDF9	601		LD SP, IX	
6301	DDE1	602		POP IX	
6303	E1	603		POP HL	
6304	F1	604		POP AF	
6305	FB	605		EI	
6306	C9	606		RET	
		607			
		608			!HERE TO SERVICE NON-MASKABLE INTERRUPT
		609			!IF (NMIADD) = 0 THEN RETURNS STRAIGHT AWAY!
		610			!ELSE, JUMPS TO (NMIADD) WITH HL (ON TOP), AF & RETN ADDR ON
		611			! THE STACK.
		612			
6307	F5	612	NMI	PUSH AF	
6309	E5	613		PUSH HL	
6309	2AB05C	614		LD HL, (NMIADD)	
630C	7C	615		LD A, H	
630D	B5	616		OR L	
630E	2001	617		JR NZ, LNI3	!IF NO USER-SUPPLIED SERVICE ROUTINE
6310	E9	618		JP (HL)	

```

6311 E1 619
6312 F1 620 LN13: POP HL
6313 ED45 621 POP AF
622 RETN
623
624
6315 625 PS_MAX_BANK DEFS 1 (THIS IS A COPY OF MAX_BANK)
626
627 GET_WORD (ADDR: HL, BANK: B)
628
629
6316 F5 630 GET_WORD PUSH AF (SAVE REGS)
6317 C5 631 PUSH BC
6318 D5 632 PUSH DE
6319 CD5E64 633 CALL GET_NUMBER (GET BANK # OF OWNER OF ADDR)
6310 F5 634 PUSH AF
6311 50 635 LD D, H
631E 47 636 LD B, A
631F CD0564 637 CALL GET_STATUS (GET STATUS OF OWNER)
6322 C5 638 PUSH BC
6323 CD4D64 639 CALL GET_CHUNK (SET HS FOR GETTING AT ADDR)
6326 2F 640 CPL (PUT IN ACTIVE LOW FORMAT)
6327 42 641 LD B, D
6328 4F 642 LD C, A
6329 CD9964 643 CALL BANK_ENABLE (ENABLE ADDR)
632C 5E 644 LD E, (HL) (READ THE WORD)
632D 23 645 INC HL
632E 56 646 LD D, (HL)
632F 2B 647 DEC HL
6330 EB 648 EX DE, HL
6331 C1 649 POP BC
6332 F1 650 POP AF
6333 47 651 LD B, A
6334 CD9964 652 CALL BANK_ENABLE (REENABLE OWNER OF ADDR)
6337 D1 653 POP DE (RESTORE REGS)
6338 C1 654 POP BC
6339 F1 655 POP AF
633A C9 656 RET
657
658
659 PUT_WORD (WORD: DE, ADDR: HL, BANK: B)
660
661
633B F5 662 PUT_WORD PUSH AF (SAVE REGS)
633C C5 663 PUSH BC
633D CD5E64 664 CALL GET_NUMBER (GET BANK # OF OWNER OF ADDR)
6340 F5 665 PUSH AF
6341 50 666 LD D, B
6342 47 667 LD B, A
6343 CD0564 668 CALL GET_STATUS (GET STATUS OF OWNER)
6346 C5 669 PUSH BC
6347 CD4D64 670 CALL GET_CHUNK (SET HS FOR GETTING AT ADDR)
634A 2F 671 CPL (PUT IN ACTIVE LOW FORMAT)
634B 42 672 LD B, D
634C 4F 673 LD C, A
634D CD9964 674 CALL BANK_ENABLE (ENABLE ADDR)
6350 73 675 LD E, (HL), E (WRITE THE WORD)
6351 23 676 INC HL
6352 72 677 LD (HL), D
6353 2B 678 DEC HL
6354 C1 679 POP BC
6355 F1 680 POP AF
6356 CD9964 681 CALL BANK_ENABLE (REENABLE OWNER OF ADDR)
6359 C1 682 POP BC
635A F1 683 POP AF
635B C9 684 RET
685
686
687 WRITE_BS_REG (REG_ADDR: D, REG_DATA: E)
688
689
635C F5 690 WRITE_BS_REG PUSH AF (SAVE REGISTERS)
635D C5 691 PUSH BC
635E E5 692 PUSH HL
635F 62 693 LD H, D
6360 2E00 694 LD L, 0 (HL = MEMORY MAPPED ADDR)
6362 3A00C0 695 LD A, (LOWNYB) (SAVE (0E000H))
6365 F5 696 PUSH AF
6366 7E 697 LD A, (HL) (SAVE (HL))
6367 F5 698 PUSH AF
6368 3E07 699 LD A, 7
636A D3F5 700 OUT (SADDPT), A (SAVE VALUES OF SOUND REGS 7 AND E)
636C DBF6 701 IN A, (SDATPT)
636E 47 702 LD B, A
636F 3E0E 703 LD A, 0EH
6371 D3F5 704 OUT (SADDPT), A
6373 DBF6 705 IN A, (SDATPT)
6375 4F 706 LD C, A
6376 3E07 707 LD A, 7 (SET IOA CHANNEL TO OUTPUT)
637B D3F5 708 OUT (SADDPT), A
637A 3E40 709 LD A, 40H
637C D3F6 710 OUT (SDATPT), A
637E 3E0E 711 LD A, 0EH
6380 D3F5 712 OUT (SADDPT), A
6382 AF 713 XOR A
6383 D3F6 714 OUT (SDATPT), A
6385 3E02 715 LD A, 2
6387 3200C0 716 LD (LOWNYB), A (RESET NYBBLE STEERING LOGIC)
638A 7B 717 LD A, E
638B 77 718 LD (HL), A (WRITE LSN OF DATA)
638C CB2F 719 SRA A

```

```

638E CB2F 720 SRA A
6390 CB2F 721 SRA A
6392 CB2F 722 SRA A
6394 77 723 LD (HL), A ;WRITE MSN OF DATA
6395 3E07 724 LD A, 7 ;RESTORE SOUND REGS
6397 D3F5 725 OUT (SADDPT), A
6399 78 726 LD A, B
639A D3F6 727 OUT (SDATPT), A
639C 3E0E 728 LD A, 0EH
639E D3F5 729 OUT (SADDPT), A
63A0 79 730 LD A, C
63A1 D3F6 731 OUT (SDATPT), A
63A2 F1 732 POP AF
63A4 77 733 LD (HL), A ;RESTORE (HL)
63A5 F1 734 POP AF
63A6 3200C0 735 LD (LOWNYB), A ;RESTORE (0E000H)
63A9 E1 736 POP HL ;RESTORE REGISTERS
63AA C1 737 POP BC
63AB F1 738 POP AF
63AC C9 739 RET
740 ;
741 ;
742 ; READ_BS_REG (LEN:ADR: D: NON-ADDR: E: BYTE:DATA: C)
743 ;
744 ;
63AD F5 745 READ_BS_REG PUSH AF ;SAVE REGISTER
63AE C5 746 PUSH BC
63AF E5 747 PUSH HL
63B0 62 748 LD H, D
63B1 2E00 749 LD L, 0 ;HL = MEMORY MAPPED ADDR
63B3 3A00C0 750 LD A, (LOWNYB) ;SAVE (0E000H)
63B4 F5 751 PUSH AF
63B7 7E 752 LD A, (HL) ;SAVE (HL)
63B8 F5 753 PUSH AF
63B9 3E07 754 LD A, 7
63BA D3F5 755 OUT (SADDPT), A ;SAVE VALUES OF SOUND REGS 7 AND E
63BB D3F6 756 N A, (SDATPT)
63BD 47 757 LD B, A
63BE 3E0E 758 LD A, 0EH
63C0 D3F5 759 OUT (SADDPT), A
63C2 D3F6 760 IN A, (SDATPT)
63C4 4F 761 LD C, A
63C5 C5 762 PUSH BC
63C8 3E07 763 LD A, 7 ;SET IOA CHANNEL TO OUTPUT
63CA D3F5 764 OUT (SADDPT), A
63CC 3E40 765 LD A, 40H
63CE D3F6 766 OUT (SDATPT), A
63D0 3E0E 767 LD A, 0EH
63D2 D3F5 768 OUT (SADDPT), A
63D4 AF 769 XOR A
63D5 D3F6 770 OUT (SDATPT), A
63D7 3E02 771 LD A, 2
63D9 3200C0 772 LD (LOWNYB), A ;RESET NYBBLE STEERING LOGIC
63DA 7E 773 LD A, (HL) ;READ LSN OF DATA
63DD E60F 774 AND 0FH
63DF 4F 775 LD C, A
63E0 63 776 LD H, E
63E1 7E 777 LD A, (HL) ;READ MSN OF DATA
63E2 CB27 778 SLA A
63E4 CB27 779 SLA A
63E6 CB27 780 SLA A
63E8 CB27 781 SLA A
63EA B1 782 OR C
63EB 5F 783 LD E, A ;RETURN BYTE DATA IN E
63EC C1 784 POP BC
63ED 3E07 785 LD A, 7 ;RESTORE SOUND REGS
63EF D3F5 786 OUT (SADDPT), A
63F1 78 787 LD A, B
63F2 D3F6 788 OUT (SDATPT), A
63F4 3E0E 789 LD A, 0EH
63F6 D3F5 790 OUT (SADDPT), A
63F8 79 791 LD A, C
63F9 D3F6 792 OUT (SDATPT), A
63FB F1 793 POP AF
63FC 77 794 LD (HL), A ;RESTORE (HL)
63FD F1 795 POP AF
63FE 3200C0 796 LH (LOWNYB), A ;RESTORE (0E000H)
6401 E1 797 POP HL ;RESTORE REGISTERS
6402 C1 798 POP BC
6403 F1 799 POP AF
6404 C9 800 RET
801 ;
802 ;
803 ; GET_BANK_STATUS (BANK: B: STATUS: B: HORIZONTAL_SELECT: C)
804 ;
805 ;
6405 F5 806 GET_STATUS PUSH AF ;SAVE SAVE REGS
6406 D5 807 PUSH DE
6407 78 808 LD A, B
6408 FEFE 809 CP 0FEH
640A 252E 810 JR Z, GS_EXT ;IF BANK = 254
640C FEFF 811 CP 0FFH
640E 251D 812 JR Z, GS_HOME ;IF BANK = 255
6410 A7 813 AND A
6411 251F 814 JR Z, GS_DOC ;IF BANK = 0
6413 1680 815 LD D, BNA ;HERE IF EXP. BANK
6415 58 816 LD E, B
6416 CD5C63 817 CALL WRITE_BS_REG
6418 1640 818 LD D, HS_LSN
6419 1E80 819 LD E, HS_MSN

```



```

6410 CDAD63      820          CALL  READ_BS_REG      IREAD HS
6420 7B         821          LD      A, E
6421 2F         822          CPL
6422 4F         823          LD      C, A
6423 14A0       824          LD      D, STA_L
6425 1E00       825          LD      E, STA_0
6427 CDAD63      826          CALL  READ_BS_REG      IREAD STATUS NYBBLES
642A 43         827          LD      B, E
642B 1B1D       828          JR      GS_EXIT
642D 010000     829          LD      BC, 0          IRETURN 0 FOR HOME BANK STATUS
6430 1B1B       830          JR      GS_EXIT
6432 DBF4       831          IN      A, (DKHSPT)   IRETURN DOCK BANK STATUS
6434 2F         832          CPL
6435 47         833          LD      B, A
6436 0E00       834          LD      C, 0
6438 1B10       835          JR      GS_EXIT
643A DBFF       836          IN      A, (HREXPT)
643C E680       837          AND    80H          ICLEAR ALL BITS EXCEPT BIT 7
643E 2F         838          CPL
643F 07         839          RLCA
6440 47         840          LD      B, A
6441 DBF4       841          IN      A, (DKHSPT)
6443 2F         842          CPL
6444 E601       843          AND    1
6446 B0         844          OR     B
6447 47         845          LD      B, A
6448 0E00       846          LD      C, 0
644A D1         847          MOV    DE, A          IRESTORE D, C
644B F1         848          MOV    AF, A
644C 07         849          RFT
850 ;
851 ;
852 ; GET_CHUNK (ADDR: HL; MASK: A)
853 ;
854 ;
644D 05         855          GET_CHUNK  PUSH  BC          ISAVE B
644E 7C         856          LD      A, H          ICHUNK NUMBER = HIGH 3 BITS OF
644F 0605       857          LD      R, 5          ICHUNK NUMBER
6451 0B3F       858          GC_SHIFT SRL      A          ICHUNK NUMBER = HIGH 3 BITS OF
6453 10FC       859          DUNZ   GC_SHIFT
6455 3C         860          INC    A
6456 47         861          LD      B, A          ICREATE MASK BY ROLLING A 1
6457 AF         862          XOR    A             ILEFT CHUNK NUMBER+1 TIMES.
6458 37         863          SCF    A             ITHE 1 COMES FROM THE CARRY
6459 17         864          GC_ROLL GC_ROLL    IFLAG
645A 14FD       865          DUNZ   GC_ROLL
645C 01         866          POP    BC          IRESTORE B
645D 09         867          RET
868 ;
869 ;
870 ; GET_BANK_NUMBER (ADDR: HL; BANK_NUMBER: A)
871 ;
872 ;
645E 05         873          GET_NUMBER  PUSH  BC          ISAVE REGS
645F 05         874          PUSH  DE
6460 CDAD64      875          CALL  GET_CHUNK
6463 4F         876          LD      C, A
6464 3A1563      877          LD      A, (BS_MAX_BANK) IGET LARGEST BANK NUMBER
6467 A7         878          AND    A
6468 2B0A       879          JR      Z, ON_RD_DOCK IIF NO EXP. BANKS
646A 47         880          LD      B, A
646B 59         881          LD      E, B          ISEARCH ALL EXP. BANKS
646C CD0564      882          CALL  GET_STATUS
646F A1         883          AND    C
6470 2923      884          JR      Z, ON_LEXP    IFOUND THE CHUNK, SO EXIT LOOP
6472 10F7       885          DUNZ   ON_CHECK
6474 DBF4       886          IN      A, (DKHSPT)   INOT IN EXP. BANKS, SO CHECK DOCK
6476 2F         887          CPL
6477 A1         888          AND    C
6478 2B1B       889          JR      Z, ON_DOCK
647A 0D         890          DEC    C
647B 2011      891          JR      NZ, GN_HOME   IIF CHUNK > 1, THEN CAN'T BE IN EXT.
647D DBFF       892          IN      A, (HREXPT)   ICHECK IF IN EXT. BANK
647F E680       893          AND    80H
6481 57         894          LD      D, A
6482 DBF4       895          IN      A, (DKHSPT)
6484 E601       896          AND    1
6486 0F         897          RRCA
6487 A2         898          AND    D
6488 2B04       899          JR      Z, GN_HOME    INOT IN EXT. BANK
648A 3EFE       900          LD      A, 0FEH       IIN EXT. BANK, SO RETURN 254
648C 1508       901          JR      GN_EXIT
648E 3EFF       902          GN_HOME LD  A, 0FFH    IIN HOME BANK, SO RETURN 255
6490 1B04       903          JR      GN_EXIT
6492 AF         904          GN_DOCK  XOR    A             IFOUND CHUNK IN DOCK, SO RETURN 0
6493 1B01       905          JR      GN_EXIT
6495 78         906          GN_LEXP  LD      A, B          IRETURN EXP. BANK NUMBER
6496 D1         907          GN_EXIT  POP    DE          IRESTORE REGS
6497 C1         908          POP    BC
6498 09         909          RET
910 ;
911 ;
912 ; BANK_ENABLE (BANK: B, HORIZONTAL_SELECT: C)
913 ;
914 ;
6499 F5         915          BANK_ENABLE  PUSH  AF          ISAVE REGISTERS
649A 05         916          PUSH  BC
649B 05         917          PUSH  DE
649C 03         918          PUSH  HL
649D 50         919          LD      H, B

```

649E	3A1563	920		LD	A, (BS_MAX_BANK) ! GET LARGEST BANK NUMBER
64A1	A7	921		AND	A
64A2	2911	922		JR	Z, BE_SKIP
64A4	1680	923		LD	D, BNA
64A6	1E00	924		LD	E, 0
64A8	CD5C63	925		CALL	WRITE_BS_REG
64AB	16A0	926		LD	D, HSP
64AD	F5	927		PUSH	AF
64AE	79	928		LD	A, C
64AF	2F	929		CPL	
64B0	5F	930		LD	E, A
64B1	F1	931		POP	AF
64B2	CD5C63	932		CALL	WRITE_BS_REG
		933			! TURN OFF APPROPRIATE BITS OF
					! ALL EXP. BANKS
64B5	79	934	BE_SKIP	LD	A, B
64B6	A7	935		AND	A
64B7	2011	936		JR	NZ, BE_NTDCK
64B9	79	937		LD	A, C
64BA	FEFF	938		CP	OFFH
64BC	2806	939		JR	Z, BE_EXT_OK
64BE	DBFF	940		IN	A, (HREXT)
64C0	CBFF	941		RES	7, A
64C2	D3FF	942		OUT	(HREXT), A
64C4	79	943	BE_EXT_OK	LD	A, C
64C5	2F	944		CPL	
64C6	D3F4	945		OUT	(DNHSPT), A
64C8	184F	946		JR	BE_EXIT
64CA	79	947	BE_NTDCK	LD	A, B
64CB	FEFE	948		CP	OFFH
64CD	201D	949		JR	NZ, BE_NTEXT
64CF	DBFF	950		IN	A, (HREXT)
64D1	17	951		RLA	
64D2	CB19	952		RR	C
64D4	2F	953		CCF	
64D5	1F	954		PRA	
64D6	D3FF	955		OUT	(HREXT), A
64D8	CB7F	956		BIT	7, A
64DA	2003	957		JR	NZ, BE_SET
64DC	DBF4	958		IN	A, (DNHSPT)
64DE	CEB7	959		RES	0, A
64E0	DBF4	960		OUT	(DNHSPT), A
64E2	103F	961		JR	BE_EXIT
64E4	DBF4	962	BE_SET	IN	A, (DNHSPT)
64E6	CB07	963		SET	0, A
64E8	D3F4	964		OUT	(DNHSPT), A
64EA	102D	965		JR	BE_EXIT
64EC	DBF4	966	BE_NTEXT	IN	A, (DNHSPT)
64EE	2F	967		CPL	
64EF	5F	968		LD	E, A
64F0	79	969		LD	A, C
64F1	2F	970		CPL	
64F2	F3	971		OR	E
64F3	2F	972		CPL	
64F4	D3F4	973		OUT	(DNHSPT), A
64F6	CB41	974		BIT	0, C
64F8	200C	975		JR	NZ, BE_CHL_HOME
64FA	DBFF	976		IN	A, (HREXT)
64FC	CBFF	977		RES	7, A
64FE	D3FF	978		OUT	(HREXT), A
6500	DBF4	979		IN	A, (DNHSPT)
6502	CB07	980		RES	0, A
6504	13F4	981		OUT	(DNHSPT), A
6506	78	982	BE_CHL_HOME	LD	A, B
6507	FEFF	983		CP	OFFH
6509	280E	984		JR	Z, BE_EXIT
650B	1680	985		LD	D, BNA
650D	58	986		LD	E, B
650E	CD5C63	987		CALL	WRITE_BS_REG
6511	16A0	988		LD	D, HS
6513	79	989		LD	A, C
6514	2F	990		CPL	
6515	5F	991		LD	E, A
6516	CD5C63	992		CALL	WRITE_BS_REG
6519	E1	993	BE_EXIT	POP	HL
651A	D1	994		POP	DE
651B	C1	995		POP	BC
651C	F1	996		POP	AF
651D	C9	997		RET	
		998			!
		999			!
		1000			! SAVE_BANK_STATUSES (STATUS_ADDR: IX)
		1001			!
		1002			! PUSHES THE STATUS OF ALL BANKS ON THE STACK
		1003			!
		1004			!
651E	F5	1005	SAVE_STATUS	PUSH	AF
651F	C5	1006		PUSH	BC
6520	15	1007		PUSH	DE
6521	DBFF	1008		IN	A, (HREXT)
6523	00	1009		NOP	
6524	00	1010		NOP	
6525	DD7700	1011		LI	(IX), A
6526	DD23	1012		INC	IX
652A	DBF4	1013		IN	A, (DNHSPT)
652C	DD7700	1014		LD	(IX), A
652F	DD23	1015		INC	IX
6531	3A1563	1016		LD	A, (BS_MAX_BANK) ! GET NUMBER OF BANKS
6534	A7	1017		AND	A
6535	280D	1018		JR	Z, SS_EXIT
6537	47	1019		LD	B, A
6538	58	1020	SS_LOOP	LD	E, B
6539	CD0564	1021		CALL	GET_STATUS
					! GET BANK STATUS OF BANK: 0B

```

653C D07100 1022 LD (IX), C
653F D023 1023 INC IX
6541 43 1024 LD B, E
6542 10F4 1025 DJNZ SS_LOOP 1DO FOR ALL
6544 D02B 1026 SS_EXIT DEC IX
6546 D1 1027 POP DE 1RESTORE REGS
6547 C1 1028 POP BC
6548 F1 1029 POP AF
6549 C9 1030 RET
1031 |
1032 |
1033 | RESTORE_BANK_STATUSES (STATUS_ADDR: IX)
1034 |
1035 | RESTORES BANK STATUS TO ALL BANKS
1036 |
1037 |
654A F5 1038 RESTORE_STATUS PUSH AF 1SAVE REGS
654B C5 1039 PUSH BC
654C 05 1040 PUSH DE
654D D07E00 1041 LD A, (IX) 1GET EXT. ROM STATUS
6550 03FF 1042 OUT (HREPT), A
6552 D023 1043 INC IX
6554 D07E00 1044 LD A, (IX) 1GET DOCK BANK STATUS
6557 03F4 1045 OUT (DKHSPT), A
6559 D023 1046 INC IX
655B 3A1563 1047 LD A, (BS_MAX_BANK) 1GET NUMBER OF BANKS
655E A7 1048 AND A
655F 260B 1049 JR Z, RS_EXIT
6561 47 1050 LD B, A 1SET UP COUNTER
6562 D04E00 1051 RS_LOOP LD C, (IX)
6565 CD9964 1052 CALL BANK_ENABLE 1WRITE BANK STATUS OF BANK #B
6566 D023 1053 INC IX
656A 10F6 1054 DJNZ RS_LOOP 1DO FOR ALL
656C D02B 1055 RS_EXIT DEC IX
656E D1 1056 POP DE 1RESTORE REGS
656F C1 1057 POP BC
6570 F1 1058 POP AF
6571 C9 1059 RET
1060 |
1061 |
1062 | GOTO_BANK (BANK, HORIZONTAL_SELECT, ADDR PASSED ON STACK)
1063 |
1064 |
1065 | SETS UP THE DESTINATION BANK AND JUMPS WITHOUT RETURN TO ADDRESS
1066 | IN BANK.
1067 |
1068 |
6572 D0210000 1069 GOTO_BANK LD IX, 0 1SET IX TO 0
6574 D023 1070 ADD IX, SP
657B D07100 1071 LD (IX), C 1SAVE BC AND TRASH RET ADDR
657D D07001 1072 LD (IX+1), B
657E D04E02 1073 LD C, (IX+2) 1GET FARMS FOR BANK_ENABLE
6581 D04603 1074 LD B, (IX+3)
6584 CD9964 1075 CALL BANK_ENABLE
6587 C1 1076 POP BC 1RESTORE BC
6588 DDE1 1077 POP IX 1TRASH FARMS TO GOTO_BANK
658A DDE1 1078 POP IX 1GET ADDR
658C DDE9 1079 JMP IX (IX)
1080 |
1081 |
1082 | CALL_BANK (ADDR, BANK, HORIZONTAL_SELECT, PRM_OUT, PRM_IN)
1083 | ALL INPUT PARAMETERS ARE PUSHED ON THE STACK
1084 |
1085 | CLOBBERS IX
1086 |
1087 |
1088 | SETS UP THE BANK AND MAKES A JUMP WITH RETURN ADDRESS TO ADDRESS
1089 | IN BANK.
1090 |
1091 |
658E 1092 BS_STACK DEFS 64
659E 1093 BS_SP DEFS 2
1094 |
1095 |
65D0 E3 1096 CALL_BANK EX (SP), HL 1GET RET ADDR
65D1 D02ACE65 1097 LD IX, (BS_SP)
65D5 D02B 1098 DEC IX
65D7 D07400 1099 LD (IX), H
65DA D02B 1100 DEC IX
65DC D07500 1101 LD (IX), L 1PUSH HL ON BS_STACK
65DF E1 1102 POP HL
65E0 E3 1103 EX (SP), HL 1GET PRM_IN
65E1 D02B 1104 DEC IX
65E3 D07400 1105 LD (IX), H
65E6 D02B 1106 DEC IX
65E9 D07500 1107 LD (IX), L 1PUSH PRM_IN ON BS_STACK
65EB D022CE65 1108 LD (BS_SP), IX 1UPDATE BS_SP
65EF D5 1109 PUSH DE 1SAVE REGS
65F0 C5 1110 PUSH BC
65F1 F5 1111 PUSH AF
65F2 210000 1112 LD HL, 0
65F3 39 1113 ADD HL, SP 1HL = SP
65F6 54 1114 LD D, H
65F7 5D 1115 LD E, L
65F8 3A1563 1116 LD A, (BS_MAX_BANK)
65FB 4F 1117 LD C, A
65FC 0600 1118 LD B, 0
65FE 03 1119 INC BC
65FF 03 1120 INC BC 1BC = MAX_BANK + 2
6600 A7 1121 AND A

```

```

6601 ED42      1122      SBC      HL, BC
6603 F9        1123      LD       SP, HL
6604 DD210000  1124      LD       IX, 0
6608 DD19      1125      ADD     IX, DE
660A EB        1126      EX      DE, HL
                                     !DE, HL NOW CONTAIN DEST. SRC
                                     ! POINTERS FOR A BLOCK MOVE
660B DD4E08  1128      LD       C, (IX+PRM_OUT)
660E DD4608  1129      LD       B, (IX+PRM_OUT)
6611 3E0E      1130      LD       A, 14
6613 81        1131      ADD     A, C
6614 4F        1132      LD       C, A
6615 3001     1133      JR      NC, CB_LNC1
6617 04        1134      INC     B
                                     !BC = PRM_OUT + 14
6618 EDB0     1135      LDIR   LDIR
661A D5        1136      PUSH   DE
                                     !MAKE ROOM FOR BANK STATUS
661B DDE1     1137      POP    IX
                                     !IX = DE
661D DD1E65  1138      CALL  SAVE_STATUS
6620 DD210000 1139      LD       IX, 0
6624 DD39     1140      ADD     IX, SP
6626 DD4E0A  1141      LD       C, (IX+HOR_SEL)
6629 DD4608  1142      LD       B, (IX+BANK)
662C DD9964  1143      CALL  BANK_ENABLE
662F F1        1144      POP    AF
6630 C1        1145      POP    BC
6631 D1        1146      POP    DE
6632 E1        1147      POP    HL
6633 DDE1     1148      POP    IX
6635 DDE1     1149      POP    IX
6637 DDE1     1150      POP    IX
6639 DD9C65  1151      CALL  JMP_IX
663C F5        1152      PUSH  AF
663D C5        1153      PUSH  BC
663E D5        1154      PUSH  DE
663F E5        1155      PUSH  HL
6640 DD2ACE65 1156      LD       IX, (BS_SP)
6644 DD4E00  1157      LD       C, (IX)
6647 DD23     1158      INC     IX
6649 DD4600  1159      LD       B, (IX)
664C DD23     1160      INC     IX
664E DD22CE65 1161      LD       (BS_SP), IX
6652 DD210000 1162      LD       IX, 0
6656 DD39     1163      ADD     IX, SP
6658 3E08     1164      LD       A, 8
665A 81        1165      ADD     A, C
665B 4F        1166      LD       C, A
665C 3001     1167      JR      NC, CB_LNC2
665E 04        1168      INC     B
665F DD09     1169      ADD     IX, BC
6661 DDE5     1170      PUSH  IX
6663 E1        1171      POP    HL
6664 28        1172      DEC     HL
6665 DD4A65  1173      CALL  RESTORE_STATUS
6668 DDE5     1174      PUSH  IX
666A D1        1175      POP    DE
666B EDB6     1176      LDIR   LDIR
666D EB        1177      EX      DE, HL
666E 23        1178      INC     HL
666F F9        1179      LD       SP, HL
6670 DD2ACE65 1180      LD       IX, (BS_SP)
6674 DD4E00  1181      LD       C, (IX)
6677 DD23     1182      INC     IX
6679 DD4600  1183      LD       B, (IX)
667C DD23     1184      INC     IX
667E DD22CE65 1185      LD       (BS_SP), IX
6682 C5        1186      PUSH  BC
6683 DDE1     1187      POP    IX
6685 E1        1188      POP    HL
6686 D1        1189      POP    DE
6687 C1        1190      POP    BC
6688 F1        1191      POP    AF
6689 DDE5     1192      PUSH  IX
668B C9        1193      RET
                                     !PUT RET ADDR ON STACK
1194 !
1195 !
1196 ! HERE ARE SOME EQUATES WHICH ARE USED BY XFER_BYTES AND THE ROUTINES IT
1197 ! CALLS.
1198 !
1199 ! DIRECTION      EQU    0
1200 ! BUF_PTR        EQU    0
1201 ! LENGTH         EQU    2
1202 ! DEST_ADDR     EQU    4
1203 ! SRC_ADDR      EQU    6
1204 ! DEST_BANK     EQU    8
1205 ! SRC_BANK      EQU    9
1206 !
1207 !
1208 ! MOVE_BYTES (BYTES_TO_MOVE: DE, DIRECTION: A)
1209 !
1210 !
1211 ! MOVE_BYTES:    PUSH   HL
1212 !                PUSH   DE
1213 !                PUSH   BC
1214 !                LD     C, B
1215 !                LD     B, (IX+SRC_BANK)
1216 !                CALL  BANK_ENABLE
1217 !                LD     B, D
1218 !                LD     C, E
1219 !                LD     E, (IX+BUF_PTR)
1220 !                LD     D, (IX+BUF_PTR+1)
1221 !                LD     L, (IX+SRC_ADDR)
1222 !                LD     H, (IX+SRC_ADDR+1)
                                     !SELECT SRC BANK
                                     !MOVE FROM SRC TO STACK

```

```

66A4 07      1223      RLCA
66A5 0F      1224      RRCA
66A6 3805    1225      JR
66A8 ED80    1226      LDIR
66AA 09      1227      ADD
66AB 1805    1228      JR
66AD ED88    1229      MB_RV1  LDIR
66AF A7      1230      AND
66B0 ED42    1231      SBC
66B2 DD7506   1232      MB_UP1  LD
66B5 DD7407   1233      LD
66B8 C1      1234      PCF
66B9 E1      1235      PCF
66BA E5      1236      PUSH
66BB C5      1237      PUSH
66BC DD4608   1238      LD
66BF CD9964   1239      CALL
66C2 44      1240      LD
66C3 4D      1241      LD
66C4 DD5E04   1242      LD
66C7 DD5A05   1243      LD
66CA DD6E00   1244      LD
66CD DD6601   1245      LD
66D0 07      1246      RLCA
66D1 0F      1247      RRCA
66D2 3805    1248      JR
66D4 ED80    1249      LDIR
66D6 09      1250      ADD
66D7 1805    1251      JR
66D9 ED88    1252      MB_RV2  LDIR
66DB A7      1253      AND
66DC ED42    1254      SBC
66DE DD7504   1255      MB_UP2  LD
66E1 DD7405   1256      LD
66E4 C1      1257      POP
66E5 D1      1258      POP
66E6 E1      1259      POP
66E7 C9      1260      RET
1261 |
1262 |
1263 | CREATE_BITMAP (ADDR: HL; BITMAP: A)
1264 |
1265 |
66E8 54      1266      CREATE_BITMAP LD
66E9 5D      1267      LD
66EA DD4E02   1268      LD
66ED DD4E03   1269      LD
66F0 DD7E00   1270      LD
66F3 07      1271      RLCA
66F4 0F      1272      RRCA
66F5 3803    1273      JR
66F7 09      1274      ADD
66F8 1802    1275      JR
66FA ED42    1276      CE_SUB  SBC
66FC CD4D64   1277      CE_CONT CALL
66FF 2F      1278      CPL
6700 47      1279      LD
6701 EB      1280      EX
6702 CD4D64   1281      CALL
6705 2F      1282      CPL
6706 4F      1283      LD
6707 A8      1284      XOR
6708 2816    1285      JR
670A 79      1286      LD
670B A0      1287      AND
670C 47      1288      LD
670D 0E00    1289      LD
670F 37      1290      SCF
6710 78      1291      MB_NB1  LD
6711 CB11    1292      RL
6713 A1      1293      AND
6714 20FA    1294      JR
6716 78      1295      MB_NB2  LD
6717 CB11    1296      RL
6719 A1      1297      AND
671A 2804    1298      JR
671C A8      1299      XOR
671D 47      1300      LD
671E 18F6    1301      JR
6720 78      1302      MB_EXIT LD
6721 C9      1303      RET
1304 |
1305 |
1306 | XFER_BYTES (DIRECTION, LENGTH, DEST_ADDR, SRC_ADDR, DEST_BANK,
1307 | SRC_BANK) PASSED ON STACK IN ORDER SHOWN; STATUS_CODE: A)
1308 |
1309 | ALL PARAMETERS ON STACK HAVE OFFSETS DEFINED ABOVE.
1310 |
1311 |
6722 F5      1312      XFER_BYTES PUSH
6723 C5      1313      PUSH
6724 D5      1314      PUSH
6725 E5      1315      PUSH
6726 210000   1316      LD
6729 39      1317      ADD
672A 110A00   1318      LD
672D 19      1319      ADD
672E EB      1320      EX
672F 3A1563   1321      LD

```

6732	4F	1322	LD	C, A
6733	0600	1323	LD	B, 0
6735	210000	1324	LD	HL, 0
6738	39	1325	ADD	HL, SP
6739	A7	1326	AND	A
673A	ED42	1327	SBC	HL, BC
673C	2B	1328	DEC	HL
673D	2B	1329	DEC	HL
673E	E5	1330	PUSH	HL
673F	DDE1	1331	POP	IX
6741	D0F9	1332	LD	SP, IX
6743	DD1E65	1333	CALL	SAVE_STATUS
6746	D5	1334	PUSH	IE
6747	DDE1	1335	POP	IX
6749	DD6E06	1336	LD	L, (IX+SRC_ADDR)
674C	DD6E07	1337	LD	H, (IX+SRC_ADDR+1)
674F	CDE866	1338	CALL	CREATE_BITMAP
6752	F5	1339	PUSH	AF
6753	DD6E04	1340	LD	L, (IX+DEST_ADDR)
6756	DD6605	1341	LD	H, (IX+DEST_ADDR+1)
6759	CDE866	1342	CALL	CREATE_BITMAP
675C	4F	1343	LD	C, A
675D	F1	1344	POP	AF
675E	47	1345	LD	B, A
675F	DD7E09	1346	LD	A, (IX+SRC_BANK)
6762	DD5608	1347	LD	D, (IX+DEST_BANK)
6765	BA	1348	CP	D
6766	2005	1349	JR	NZ, XB_DIFF_BANKS
6768	78	1350	LD	A, B
6769	A1	1351	AND	C
676A	47	1352	LD	B, A
676B	180B	1353	JR	XB_DO_MOVE
676D	78	1354	LD	A, B
676E	B1	1355	OR	C
676F	FEFF	1356	CP	OFFH
6771	202D	1357	JR	NZ, XB_OVERLAP
6773	58	1358	LD	E, B
6774	42	1359	LD	B, D
6775	CD9964	1360	CALL	BANK_ENABLE
6778	DD4609	1361	LD	B, (IX+SRC_BANK)
677B	4B	1362	LD	C, E
677C	CD9964	1363	CALL	BANK_ENABLE
677F	DD6E06	1364	LD	L, (IX+SRC_ADDR)
6782	DD6607	1365	LD	H, (IX+SRC_ADDR+1)
6785	DD5E04	1366	LD	E, (IX+DEST_ADDR)
6788	DD5605	1367	LD	D, (IX+DEST_ADDR+1)
678B	DD4E02	1368	LD	C, (IX+LENGTH)
678E	DD4603	1369	LD	B, (IX+LENGTH+1)
6791	DD7E00	1370	LD	A, (IX+DIRECTION)
6794	07	1371	RLCA	
6795	0F	1372	RRCA	
6796	3804	1373	JR	C, XB_REVERSE
6798	EDB0	1374	LDIR	
679A	1852	1375	JR	XB_EXIT
679C	EDB8	1376	LDUR	
679E	184E	1377	JR	XB_EXIT
67A0	21C05C	1378	LD	HL, HSTBOT
67A3	C5	1379	PUSH	BC
67A4	06FF	1380	LD	B, 255
67A6	CD1663	1381	CALL	GET_WORD
67A9	C1	1382	POP	BC
67AA	110002	1383	LD	DE, STKSZ
67AD	A7	1384	AND	A
67AE	ED52	1385	SBC	HL, DE
67B0	112000	1386	LD	DE, FREE_BYTES
67B3	19	1387	ADD	HL, DE
67B4	EB	1388	EX	DE, HL
67B5	210000	1389	LD	HL, 0
67B8	39	1390	ADD	HL, SP
67B9	13	1391	INC	DE
67BA	A7	1392	AND	A
67BB	ED52	1393	SBC	HL, DE
67BD	3004	1394	JR	NZ, XB_SPACE
67BF	2E01	1395	LD	A, 1
67C1	182B	1396	JR	XB_EXIT
67C3	18	1397	DEC	DE
67C4	EB	1398	EX	DE, HL
67C5	F9	1399	LD	SP, HL
67C6	12	1400	INC	DE
67C7	DD7E00	1401	LD	A, (IX+DIRECTION)
67CA	DD7500	1402	LD	(IX+BUF_PTR), L
67CD	DD7401	1403	LD	(IX+BUF_PTR+1), H
67D0	DD6E02	1404	LD	L, (IX+LENGTH)
67D3	DD6603	1405	LD	H, (IX+LENGTH+1)
67D6	A7	1406	AND	A
67D7	ED52	1407	SBC	HL, DE
67D9	3905	1408	JR	C, XB_LAST_MOVE
67DB	CD8C66	1409	CALL	MOVE_BYTES
67DE	18F6	1410	JR	XB_MOVE_LOOP
67E0	19	1411	ADD	HL, DE
67E1	EB	1412	EX	DE, HL
67E2	CD8C66	1413	CALL	MOVE_BYTES
67E5	EB	1414	EX	DE, HL
67E6	DD6E00	1415	LD	L, (IX+BUF_PTR)
67E9	DD6601	1416	LD	H, (IX+BUF_PTR+1)
67EC	19	1417	ADD	HL, DE
67ED	F9	1418	LD	SP, HL
67EE	AF	1419	XOR	A
67EF	DD210000	1420	LD	IX, 0
67F3	DD39	1421	ADD	IX, SP
67F5	CD4A65	1422	CALL	RESTORE_STATUS

```

67F8 DD23 1423 INC IX
67FA DDF9 1424 LD SP, IX
67FC E1 1425 POP HL
67FD D1 1426 POP DE
67FE C1 1427 POP BC
67FF F1 1428 POP AF
6800 DDE1 1429 POP IX
6802 DDE3 1430 EX (SP), IX
6804 DDE1 1431 POP IX
6806 DDE3 1432 EX (SP), IX
6808 DDE1 1433 POP IX
680A DDE3 1434 EX (SP), IX
680C DDE1 1435 POP IX
680E DDE3 1436 EX (SP), IX
6810 DDE1 1437 POP IX
6812 DDE3 1438 EX (SP), IX
6814 C9 1439 RET
1440 !
1441 !
1442 ! GOTO_EXT_INIT (ADDR: HL)
1443 !
1444 !
6815 DDE1 1445 GOTO_EXT FUF IY
6817 FS 1446 PUSH AF
6818 DFFF 1447 IN A, (HREXPT)
681A CFFF 1448 SET 7, A
681C D3FF 1449 OUT (HREXPT), A
681E 3E01 1450 LD A, 1
6820 D3F4 1451 OUT (DKHSPT), A
6822 F1 1452 POP AF
6823 E9 1453 JP (HL)
1454 END

```

```

FIXTBL
LOC OBJ CODE M STMT SOURCE STATEMENT ASH 5.9
1 DISPATCH EQU 6200H
2 INT EQU 62AEH
3 GET_WORD EQU 6316H
4 PUT_WORD EQU 633BH
5 GET_STATUS EQU 6405H
6 GET_NUMBER EQU 645EH
7 BANK_ENABLE EQU 6499H
8 SAVE_STATUS EQU 651EH
9 RESTORE_STATUS EQU 654AH
10 BS_STACK EQU 658EH
11 BS_SP EQU 65CEH
12 GOTO_BANK EQU 6572H
13 CALL_BANK EQU 65D0H
14 MOVE_BYTES EQU 668CH
15 CREATE_BITMAP EQU 66ERH
16 XFER_BYTES EQU 6722H
17
18 ! HERE IS THE FIXUP TABLE FOR THE VIDEO MODE CHANGER. IT DEFINES THE
19 ! LOCATIONS IN RAM WHICH MUST BE UPDATED WHEN MOVED FROM CHUN: 3 TO CHUN: 7
20 ! OR VICE-VERSA. THE ADDRESSES IN THE TABLE ARE DEFINED AS CHUN: 3 ADDRESSES
21
22
23
24
25
26 FIXTBL
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55

```

LOC	OBJ	CODE	M	STMT	SOURCE STATEMENT	ASH 5.9
1D00				23	ORG 1D00H	
1D00	3262			26	DISPATCH+32H	
1D02	4062			27	DISPATCH+40H	
1D04	7262			28	DISPATCH+72H	
1D06	AB62			29	DISPATCH+0ABH	
1D08	B862			31	INT+0AH	
1D0A	CD62			32	INT+1FH	
1D0C	D362			33	INT+25H	
1D0E	DC62			34	INT+2EH	
1D10	FB62			35	INT+4DH	
1D12	1A63			37	GET_WORD+4H	
1D14	2063			38	GET_WORD+0AH	
1D16	2463			39	GET_WORD+0EH	
1D18	2A63			40	GET_WORD+14H	
1D1A	3563			41	GET_WORD+1FH	
1D1C	3E63			43	PUT_WORD+3H	
1D1E	4463			44	PUT_WORD+9H	
1D20	4863			45	PUT_WORD+0DH	
1D22	4E63			46	PUT_WORD+13H	
1D24	5763			47	PUT_WORD+1CH	
1D26	1764			49	GET_STATUS+12H	
1D28	1E64			50	GET_STATUS+19H	
1D2A	2864			51	GET_STATUS+23H	
1D2C	6164			53	GET_NUMBER+3H	
1D2E	6564			54	GET_NUMBER+7H	
1D30	6D64			55	GET_NUMBER+0FH	

1D36	B364	59	DEFW	BANK_ENABLE+1AH
1D38	0E65	60	DEFW	BANK_ENABLE+75H
1D3A	1665	61	DEFW	BANK_ENABLE+7DH
		62		
1D3C	3265	63	DEFW	SAVE_STATUS+14H
1D3E	3A65	64	DEFW	SAVE_STATUS+1CH
		65		
1D40	5C65	66	DEFW	RESTORE_STATUS+12H
1D42	6665	67	DEFW	RESTORE_STATUS+1CH
		68		
1D44	CE65	69	DEFW	BS_SP
		70		
1D46	8565	71	DEFW	GOTO_BANK+13H
		72		
1D48	D365	73	DEFW	CALL_BANK+3H
1D4A	ED65	74	DEFW	CALL_BANK+1DH
1D4C	F965	75	DEFW	CALL_BANK+29H
1D4E	1E66	76	DEFW	CALL_BANK+4EH
1D50	2D66	77	DEFW	CALL_BANK+5DH
1D52	3A66	78	DEFW	CALL_BANK+6AH
1D54	4266	79	DEFW	CALL_BANK+72H
1D56	5066	80	DEFW	CALL_BANK+80H
1D58	6666	81	DEFW	CALL_BANK+96H
1D5A	7266	82	DEFW	CALL_BANK+0A2H
1D5C	8066	83	DEFW	CALL_BANK+0B0H
		84		
1D5E	9466	85	DEFW	MOVE_BYTES+8H
1D60	C066	86	DEFW	MOVE_BYTES+34H
		87		
1D62	FD66	88	DEFW	CREATE_BITMAP+15H
1D64	0367	89	DEFW	CREATE_BITMAP+1BH
		90		
1D66	3067	91	DEFW	XFER_BYTES+0EH
1D68	4F67	92	DEFW	XFER_BYTES+2DH
1D6A	5067	93	DEFW	XFER_BYTES+2EH
1D6C	5A67	94	DEFW	XFER_BYTES+38H
1D6E	7667	95	DEFW	XFER_BYTES+54H
1D70	7D67	96	DEFW	XFER_BYTES+58H
1D72	A767	97	DEFW	XFER_BYTES+85H
1D74	DC67	98	DEFW	XFER_BYTES+0BAH
1D76	E367	99	DEFW	XFER_BYTES+0C1H
1D78	F667	100	DEFW	XFER_BYTES+0D4H
		101		
1D7A	0000	102	DEFW	0 ; THIS IS THE TABLE TERMINATOR

APPENDIX B

System Variables Definition File

2068 HOME ROM

TS2000 HOME ROM	BASIC	
LOC	OBJ CODE M	STMT SOURCE STATEMENT
		ASH 5.9
13		*EJECT
14		*INCL SYSVAR
15		*PAGESIZE 54
16		
17	RST:	MACRO #ROUT
18		RST #ROUT
19		ENDM
20		
21	ASSERT:	MACRO #COND
22		COND .NOT.(#COND)
23		ERROR IN ASSERTION #COND
24		ENDC
25		ENDM
26		
27		; SYSTEM VARIABLES
28		
29	L_LEN	EQU 32 ; # CHARS PER LINE ON THE DISPLAY
30	TV_LNS:	EQU 24 ; NO. OF LINES ON TV SCREEN
31	D_FILE:	EQU 4000H ; ADDRESS OF DISPLAY FILE
32	ATTRS:	EQU D_FILE+L_LEN*TV_LNS*8 ; SCREEN ATTRIBUTES
33	PRBUFF:	EQU ATTRS+TV_LNS*L_LEN ; PRINTER BUFFER
34		ASSERT PRBUFF.AND.OFFH=0
		COND .NOT.(PRBUFF.AND.OFFH=0)
		ERROR IN ASSERTION PRBUFF.AND.OFFH=0
		ENDC


```

35 KSTATE: EQU PRBUFF+L_LEN*8      ; SEE KB DOCUMENTATION
36 KS_A: EQU 0                    ; 1ST BYTE IS A CHAR KEY PRESSED
37 KS_C: EQU 1                    ; 2ND IS TIME TILL COUNTS AS RELEASED
38 KS_B: EQU 2                    ; 3RD IS TIME (IN FRAMES) TILL REPEAT
39 KS_D: EQU 3                    ; 4TH IS CODE WHEN REPEATS
40                                ; 5TH - 8TH ARE A SECOND SET OF 1ST FOUR
41 LAST_K: EQU KSTATE+8           ; NEWLY PRESSED KEY
42 REPDEL: EQU LAST_K+1           ; DELAY BEFORE 1ST REPEAT (INITIALIZED TO 35)
43 REPPER: EQU REPDEL+1           ; DELAY BEFORE SUBSEQUENT REPEATS (INITIALIZED TO 5)
44 DEFADD: EQU REPPER+1           ; -> CHAR AFTER '(' IN FORMAL PARAMETER LIST; MUST BE
45                                ; 0 WHEN NO USER-DEFINED FN BEING EVALUATED
46 K_DATA: EQU DEFADD+2           ; DATA BYTE IN COMPOSITE CHAR FROM KEYBOARD
47 TVDATA: EQU K_DATA+1           ; USED FOR STORING BYTES IN COMPOSITE CHARACTERS:
48                                ; (TVDATA) = KEY BYTE,
49                                ; (TVDATA+1) = 1ST DATA BYTE FOR AT OR TAB.
50 STRMS: EQU TVDATA+2            ; STREAM DATA; POINTERS (OFFSETS FROM (CHANS)-1) TO
51                                ; CHANNELS, 0 = STREAM_NOT_OPEN.
52 HIDSTR: EQU 3                  ; NO. STREAMS HIDDEN FROM USER, THESE ARE TIED
53                                ; UNALTERABLY TO SPECIFIC CHANNELS.
54 HID_K: EQU -3                  ; KEYBOARD
55 HID_S: EQU -2                  ; TV, UPPER HALF OF SCREEN
56 HID_R: EQU -1                  ; INSERTION IN RAM
57 COM_ST: EQU 0                  ; STREAM FOR COMMANDS
58 INP_ST: EQU 1                  ; STREAM FOR INPUT DATA
59 PR_ST: EQU 2                   ; STREAM FOR PRINT
60 LPR_ST: EQU 3                  ; STREAM FOR LPRINT
61 CHARS: EQU STRMS+(HIDSTR+16)*2 ; -> 8*20H BYTES BEFORE CHARACTER SET
62 FART: EQU CHARS+2              ; NO. CYCLES OF ERROR NOISE (2 SVES BELOW MIDDLE C)
63 PIP: EQU FART+1                ; NO. CYCLES OF KEYBOARD NOISE (3 SVES ABOVE MIDDLE C)
64 Y: EQU PIP+1                   ; VALUE ALWAYS HELD IN IY
65 ERR_NNR EQU Y                  ; [RUN TIME ERROR #] - 1
66 FLAGS: EQU ERR_NNR+1           ; VARIOUS FLAGS
67 SPC: EQU 0                     ; SUPPRESS SPACE BEFORE TOKENS
68 PR: EQU 1                      ; PRINTING TO PRINTER, NOT TV
69 LMODE1: EQU 2                  ; L MODE, NOT K, AT CURRENT CHARACTER
70 LMODE: EQU 3                   ; L MODE, NOT K, AT CURSOR
71 KEYHIT: EQU 5                  ; KEYHIT FOUND
72 NO: EQU 6                      ; EXPRESSION IS NUMERICAL, NOT STRING
73 INTPT: EQU 7                   ; REQ INTERPRET RATHER THAN CHECK SYNTAX
74 TVFLAG: EQU FLAGS+1            ; FLAGS ASSOCIATED WITH THE TV
75 LHS: EQU 0                     ; PRINTING TO LOWER HALF OF SCREEN
76 EDIT: EQU 1                    ; OUTPUTTING LINE FOR EDIT OR NO. FOR STRING
77 ECHREQ: EQU 3                  ; ECHO REQUESTED IF INPUTTING FROM KEYBOARD
78 LIST: EQU 4                    ; OUTPUTTING AN AUTOMATIC LISTING
79 CLHS: EQU 5                    ; CLEAR LOWER HALF WHEN KEY PRESSED
80 ERR_SP: EQU TVFLAG+1            ; -> BOTTOM ITEM ON MACHINE STACK.
81 LISTSP: EQU ERR_SP+2            ; -> RETURN ADDRESS FROM AUTOMATIC LISTING
82 MODE: EQU LISTSP+2              ; 0 = K OR L, 1 = F, 2 = G.
83 NEWPPC: EQU MODE+1              ; LINE TO BE JUMPED TO
84 NSPPC: EQU NEWPPC+2             ; SUBLINE TO BE JUMPED TO (BIT 7 OFF FORCES JUMP)
85 PPC: EQU NSPPC+1                ; LINE # OF INSTR BEING INTERPRETED
86 SUBPPC: EQU PPC+2               ; NO. WITHIN LINE OF INSTR BEING INTERPRETED
87 BORDCR: EQU SUBPPC+1            ; BORDER COLOUR (SHIFTED LEFT BACKG BITS WITH OS IN
88                                ; BITS 0-2 & 6-7)
89 E_PPC EQU BORDCR+1              ; LINE # OF "CURRENT" LINE IN LISTING
90                                ; THE VARIABLES FROM (VARS) UP TO & INCLUDING (STKEND)
91                                ; ARE 'MOVABLE' IN THE SENSE THAT THEY ARE ADJUSTED
92                                ; (BY REMGSZ IN MODULE EDIT) WHENEVER STUFF IS
93                                ; INSERTED IN OR DELETED FROM RAM.
94 VARS EQU E_PPC+2                ; -> 1ST RECORD FOR A VARIABLE (LAST IS 1 BYTE 80H)
95 DEST EQU VARS+2                 ; -> VAR MATCHED BY TEMPL CODE 1 OR 4 (TEXT OR RECORD)
96 CHANS_: EQU DEST+2              ; -> CHANNEL DATA (INCLUDING FLOPPY BUFFERS).
97                                ; EACH ITEM COMPRISES:
98                                ; THE ADDRESS OF AN OUTPUT ROUTINE FOR WRCH.
99                                ; THE ADDRESS OF AN INPUT ROUTINE FOR INCH.
100                               ; A 1-BYTE CODE FOR THE DEVICE TYPE,
101                               ; &, WHERE APPROPRIATE, A FILE NAME, ADDITIONAL
102                               ; DATA & A BUFFER.
103 CURCHL: EQU CHANS_+2            ; -> DATA FOR CURRENT CHANNEL
104 PROG: EQU CURCHL+2              ; -> BASIC PROGRAM
105 NXTLIN: EQU PROG+2              ; -> NEXT LINE OF SOURCE CODE

```

```

106 DATADD: EQU NXTLIN+2 ; -> TERMINATOR OF LAST DATA ITEM
107 E_LINE EQU DATADD+2 ; -> LINE BEING EDITED
108 K_CUR: EQU E_LINE+2 ; -> CURRENT CHAR IN INPUT BUFFER
109 CH_ADD EQU K_CUR+2 ; -> CURRENT CHAR WHEN SYNTAX CHECKING ETC
110 X_PTR EQU CH_ADD+2 ; -> 1ST CHAR NOT SYNTACTICALLY OK (0 IF ALL OK)
111 ; ALSO STORES (CH_ADD) DURING READ & INPUT
112 WORKSP: EQU X_PTR+2 ; -> TEMPORARY WORK SPACE
113 STKBOT: EQU WORKSP+2 ; -> BOTTOM OF CALCULATOR STACK
114 STKNXT: EQU STKBOT+2 ; -> NEXT FREE PLACE ON CALCULATOR STACK
115 STKEND: EQU STKNXT ; ALTERNATIVE NAME
116
117 BREG: EQU STKEND+2 ; KEEPS VALUE OF CALCULATOR B REGISTER
118 MEM: EQU BREG+1 ; -> AREA USED BY CALCTR INSTRS MEMORY & COPY
119 FLAGS2: EQU MEM+2 ; MORE FLAGS
120 ALOS: EQU 0 ; AUTOMATIC LISTING ON SCREEN
121 PRLEFT: EQU 1 ; PRINTER BUFFER NOT EMPTY
122 L_STR: EQU 2 ; INSIDE STRING WHEN DOING KB MODE IN LISTCH
123 CAPS_L: EQU 3 ; CAPITALS SHIFT LOCK ON
124 RETPOS: EQU 4 ; RETYPE POSSIBLE AFTER SYNTAX ERROR
125 DELREP: EQU 5 ; DELETE KEY REPEAT (KEY HELD DOWN)
126 DF_SZ EQU FLAGS2+1 ; # LINES IN 2ND HALF OF SCREEN INC SEP'G BLANK LINE
127 S_TOP EQU DF_SZ+1 ; LINE # (IN PROGRAM) OF TOP LINE ON SCREEN
128 OLDPPC EQU S_TOP+2 ; LINE # OF E.G. INTERRUPTED STMT
129 OSPPC EQU OLDPPC+2 ; (OLD SUB PPC) STATEMENT NO. WITHIN LINE FOR OLDPPC
130 FLAGX: EQU OSPPC+1 ; FLAGS ASSOCIATED WITH ASSIGNMENT
131 FLEX: EQU 0 ; FLEXIBLE LENGTH ASSIGNMENT REQUIRED
132 UNFND: EQU 1 ; DESTINATION OF ASSIGNMENT NOT FOUND
133 INPLN: EQU 5 ; REQ INPUT VALUE RATHER THAN LINE OF PROGRAM
134 ;NO: EQU 6 ; REQD TYPE IS NUMERIC
135 LINPLN: EQU 7 ; LINPUT (INPUT LINE) RATHER THAN STRAIGHT INPUT
136 STRLEN: EQU FLAGX+1 ; LENGTH OF DESTINATION WHEN STRING TYPE
137 T_ADDR EQU STRLEN+2 ; -> NEXT BYTE IN TEMPLATE
138 SEED EQU T_ADDR+2 ; LAST RANDOM # BEFORE SCALING
139 FRAMES: EQU SEED+2 ; LS 2 BYTES OF 3-BYTE FRAME COUNTER
140 FRAME2: EQU FRAMES+2 ; MS BYTE OF 3-BYTE FRAME COUNTER
141 UDG: EQU FRAME2+1 ; -> 1ST USER DEFINED GRAPHIC
142 COORDS: EQU UDG+2 ; COORDINATES OF LAST PLOT ETC.: (COORDS) = X-COORD.,
143 ; (COORDS+1) = Y-COORD.
144 P_POSN: EQU COORDS+2 ; COLUMN NO. OF PRINTER POSN
145 PR_CC: EQU P_POSN+1 ; LS BYTE OF ADDRESS OF NEXT CHAR FOR PRINTER
146 ECHO_E: EQU PR_CC+2 ; COORDS IN LOWER HALF OF END OF KEYBOARD INPUT BUFFER
147 DF_CC EQU ECHO_E+2 ; -> SCREEN CHAR UNDER PRINT CURSOR
148 DFCCL: EQU DF_CC+2 ; LIKE DF_CC FOR LOWER HALF
149 S_POSN EQU DFCCL+2 ; SCREEN POSN (COL & LINE) OF NEXT CHAR TO BE OUTPUT
150 S_POSNL: EQU S_POSN+2 ; LIKE S_POSN FOR LOWER HALF
151 SCR_CT: EQU S_POSNL+2 ; (SCROLL COUNT) DECREMENTED FOR EACH SCROLL
152 ATTR_P: EQU SCR_CT+1 ; CURRENT PERMANENT PRINTING ATTRIBUTES
153 FOREG: EQU 0 ; LS BIT OF FOREGROUND COLOUR
154 BLUE: EQU 0 ;
155 RED: EQU 1 ; (INK)
156 GREEN: EQU 2 ;
157 BACKG: EQU 3 ; LS BIT OF BACKGROUND COLOUR
158 BLUEB: EQU 3 ; (PAPER)
159 REDB: EQU 4 ;
160 GREENB: EQU 5 ;
161 HILITE: EQU 6 ; BRIGHT
162 FLASH: EQU 7 ; FLASH
163 MASK_P: EQU ATTR_P+1 ; CURRENT PERMANENT PRINTING ATTRIBUTES MASK:
164 ; 0 FOR NEW, 1 FOR OLD
165 ATTR_T: EQU MASK_P+1 ; CURRENT TEMP. PRINTING ATTRIBUTES (BITS AS ATTR_P)
166 MASK_T: EQU ATTR_T+1 ; CURRENT TEMPORARY PRINTING ATTRIBUTES MASK
167 P_FLAG: EQU MASK_T+1 ; ADDITIONAL FLAGS FOR PRINTING: TEMPORARY FLAGS IN
168 ; EVEN BITS, PERMANENT FLAGS IN ODD BITS
169 XOR_CH: EQU 0 ; NEW CHARS XOR'D INTO OLD RATHER THAN BEING LOADED
170 INV_CH: EQU 2 ; NEW CHARS INVERTED
171 F_CB: EQU 4 ; FOREGROUND := COMPLEMENT OF BACKGROUND
172 B_CF: EQU 6 ; BACKGROUND := COMPLEMENT OF FOREGROUND
173 MEMBOT: EQU P_FLAG+1 ; BOTTOM OF CALCULATOR MEMORY (6 NUMBERS)
174 NMIADD: EQU MEMBOT+30 ; -> USER'S NMI SERVICE ROUTINE
175 RAMTOP: EQU NMIADD+2 ; LAST ADDRESS OF BASIC SYSTEM AREA
176 P_RAMT: EQU RAMTOP+2 ; -> LAST BYTE OF PHYSICAL RAM

```

```

177
178          !**** ADDITIONAL
179 ERR_LN: EQU P_RAMT+2      !POINTER TO ON ERROR LINE NUMBER FOR A GO-TO.
180 ERR_C:  EQU ERR_LN+2     !STORE LINE NUMBER IN WHICH ERROR OCCURRED
181 ERR_S:  EQU ERR_C+2     !STORES STATEMENT NUMBER IN WHICH ERROR OCCURRED
182 ERR_T:  EQU ERR_S+1     !STORE FOR 'ERROR TYPE' AFTER A 'ON ERR'
183 SYSCON: EQU ERR_T+1     !SYSTEM CONFIGURATION TABLE.
184 MAX_BANK: EQU SYSCON+2  !LARGEST BANK NUMBER ASSIGNED
185 CURCBN: EQU MAX_BANK+1  !BANK NUMBER OF THE CURRENT CHANNEL
186 MSTBOT: EQU CURCBN+1    !ADDRESS OF LOCATION ABOVE MACHINE STACK
187 VIDMOD: EQU MSTBOT+2
188 ;
189 ;
190 ARSBUF: EQU VIDMOD+2    !POINTER TO AROS BUFFER.
191 ARSFLG: EQU ARSBUF+2    !AROS FLAG - BIT 7 SET INDICATES AROS PRESENT.
192 ;
193 ;
194 ;
195 ;
196 ;
197 ADATLN: EQU ARSFLG+1    !POINTER TO THE START OF THE CURRENT DATA LINE
198 ;
199 DTLNLN: EQU ADATLN+2    !LENGTH OF THE CURRENT DATA LINE (AROS ONLY).
200 STRNMH: EQU DTLNLN+2    !CURRENT STREAM NUMBER, USED FOR BUS EXPANSION
201 ;
202 MSTACK: EQU 6200H       !LOCATION ABOVE MACHINE STACK
203 DRIVES: EQU 6840H       !START OF 'DRIVES' AREA
204 BANK_ENABLE EQU 6499H
205 CALL_BANK EQU 65D0H
206 MOVE_SZ EQU DRIVES-6000H
207 DEST7 EQU OFFFH-MOVE_SZ+1
208 FIX EQU DEST7-6000H
209 CALL_VBANK EQU CALL_BANK+FIX
210 GOTO_BANK EQU 6572H     !ADDRESS OF "GO TO BANK" BANK SWITCHING
211 ;
212 XFER_BYTES EQU 6722H    !INDIRECT DATA TRANSFER BETWEEN BANKS.
213 GOTO_EXT EQU 6815H      !FOR INITIALIZATION CODE IN HOME BANK
214 ;
215 SLVM EQU 01ABH         !ADDRESS OF TAPE ROUTINES FOR SAVE, LOAD
216 ;
217 BLD SCT EQU 09F4H      !ADDRESS OF INITIALIZATION ROUTINE TO
218 ;
219 RESSCI EQU 0C4CH       !ADDRESS OF RESET ROUTINE TO ADD DEVICES.
220 PASSING EQU 0F09H      !ADDRESS OF ROUTINE TO PUSH PARAMETERS TO
221 ;
222 ;
223 ;
224 ;
225 ; OTHER EQUATES
226 ;
227 ; RESTARTS
228 ;
229 ERROR: EQU 8
230 WRCH: EQU 16
231 IGN_SP: EQU 24
232 NXT_IS: EQU 32
233 CALCTR: EQU 40
234 COPYUP: EQU 48
235 ;
236 NOSIZE EQU 5           ! # OF BYTES IN A FLOATING POINT NUMBER
237 DIGIT EQU '0'         ! DIGIT+N IS CODE FOR DIGIT N
238 LETTER EQU 0          ! LETTER+'ALPHA' IS CODE FOR LETTER ALPHA
239 DEBDEL: EQU 5         ! NO. CONSECUTIVE TIMES KB SWITCH FOUND OPEN BEFORE
240 ;
241 ;
242 ; CONTROL CHARACTERS (APPEARING ON STREAM)
243 ;
244 COM_CC: EQU 6         ! PRINT COMMA
245 EDT_CC: EQU 7         ! EDIT
246 BS_CC: EQU 8         ! BACKSPACE (CURSOR LEFT)
247 CRT_CC: EQU 9        ! CURSOR RIGHT
248 CD_CC: EQU 0AH       ! CURSOR DOWN
249 CU_CC: EQU 0BH       ! CURSOR UP

```

```

250 RUB_CC: EQU OCH          ; RUBOUT
251 CR_CC: EQU ODH          ; CARRIAGE RETURN (NEWLINE)
252 NL: EQU CR_CC
253 SLUG: EQU OEH          ; PRECEDES 5 BYTES OF SLUG
254 FORECC: EQU 10H        ; FOREGROUND
255                          ; THE CONTROL CHARS FOR FORE, BACK, FLASH, BRIGHT,
256                          ;   INVERT & OVER ARE CONSECUTIVE IN THAT ORDER.
257 AT_CC: EQU 16H         ; PRINT AT
258 TAB_CC: EQU 17H        ; PRINT TAB
259
260                          ; CONTROL CHARACTERS (RECEIVED FROM KEYBOARD)
261
262 STY_KC: EQU 0           ; STEADY
263 FSH_KC: EQU 1           ; FLASH
264 LOL_KC: EQU 2           ; LOWLIGHT
265 HIL_KC: EQU 3           ; HIGHLIGHT
266 NLV_KC: EQU 4           ; NORMAL VIDEO
267 INV_KC: EQU 5           ; INVERSE VIDEO
268 CSL_KC: EQU 6           ; CAPS SHIFT LOCK TOGGLE
269 TM_KC: EQU OEH         ; TOKEN MODE
270 GRM_KC: EQU OFH        ; GRAPHICS MODE
271 FG_KC: EQU 10H         ; FOREGROUND BLACK
272 BG_KC: EQU 18H        ; BACKGROUND BLACK
273
274 SPACE: EQU ' '
275 QUOTE EQU '"'          ; STRING QUOTE
276 DOLLAR EQU '$'         ; DOLLAR SIGN
277 COLON: EQU ':'
278 COMMA EQU ','
279 KET EQU ')'
226
227                          ; RESTARTS
228
229 ERROR: EQU 8
230 WRCH: EQU 16
231 IGN_SP: EQU 24
232 NXT_IS: EQU 32
233 CALCTR: EQU 40
234 COPYUP: EQU 48
235
236 NOSIZE EQU 5            ; # OF BYTES IN A FLOATING POINT NUMBER
237 DIGIT EQU '0'          ; DIGIT+N IS CODE FOR DIGIT N
238 LETTER EQU 0           ; LETTER+'ALPHA' IS CODE FOR LETTER ALPHA
239 DEBDEL: EQU 5          ; NO. CONSECUTIVE TIMES KB SWITCH FOUND OPEN BEFORE
240                          ;   KEY RECKONED RELEASED.
241
242                          ; CONTROL CHARACTERS (APPEARING ON STREAM)
243
244 COM_CC: EQU 6           ; PRINT COMMA
245 EDT_CC: EQU 7           ; EDIT
246 BS_CC: EQU 8           ; BACKSPACE (CURSOR LEFT)
247 CRT_CC: EQU 9           ; CURSOR RIGHT
248 CD_CC: EQU 0AH         ; CURSOR DOWN
249 CU_CC: EQU 0BH         ; CURSOR UP
250 RUB_CC: EQU OCH          ; RUBOUT
251 CR_CC: EQU ODH          ; CARRIAGE RETURN (NEWLINE)
252 NL: EQU CR_CC
253 SLUG: EQU OEH          ; PRECEDES 5 BYTES OF SLUG
254 FORECC: EQU 10H        ; FOREGROUND
255                          ; THE CONTROL CHARS FOR FORE, BACK, FLASH, BRIGHT,
256                          ;   INVERT & OVER ARE CONSECUTIVE IN THAT ORDER.
257 AT_CC: EQU 16H         ; PRINT AT
258 TAB_CC: EQU 17H        ; PRINT TAB
259
260                          ; CONTROL CHARACTERS (RECEIVED FROM KEYBOARD)
261
262 STY_KC: EQU 0           ; STEADY
263 FSH_KC: EQU 1           ; FLASH
264 LOL_KC: EQU 2           ; LOWLIGHT
265 HIL_KC: EQU 3           ; HIGHLIGHT
266 NLV_KC: EQU 4           ; NORMAL VIDEO
267 INV_KC: EQU 5           ; INVERSE VIDEO

```

```

268 CSL_KC: EQU 6           ; CAPS SHIFT LOCK TOGGLE
269 TM_KC: EQU 0EH         ; TOKEN MODE
270 ORL_KC: EQU 0FH       ; GRAPHICS MODE
271 FO_KC: EQU 10H        ; FOREGROUND BLACK
272 BG_KC: EQU 18H        ; BACKGROUND BLACK
273
274 SPACE: EQU ' '         ;
275 QUOTE EQU '"          ; STRING QUOTE
276 DOLLAR EQU '$         ; DOLLAR SIGN
277 COLON: EQU ':'         ;
278 COMMA EQU ','         ;
279 KET EQU ')'           ;
280 BRA EQU '('           ;
281 GT: EQU '>'           ;
282 MINUS EQU '-'         ;
283 EQUAL EQU '='         ;
284 PLUS: EQU '+'         ;
285 STROKE: EQU '/'       ;
286 POWER: EQU '^'       ;
287 POINT: EQU '.'        ;
288 SHARP: EQU '5FH       ; PRESTEL CODE FOR '#'
289 STD_GR: EQU 80H       ; 1ST STANDARD GRAPHIC
290 UD_GR: EQU 90H       ; 1ST USER-DEFINED GRAPHIC
291
292                                ; TOKENS
293
294 TOKO: EQU 0A5H         ; 1ST TOKEN
295 RNDTOK: EQU 0A5H      ; 'RND'
296 INKEY: EQU 0A6H       ; 'INKEY$'
297 PI: EQU 0A7H          ; 'PI'
298 FN_TK: EQU 0A8H       ; 'FN'
299 PNT_TK: EQU 0A9H      ; 'POINT'
300 SCRNTK: EQU 0AAH      ; 'SCREEN$'
301 ATTRTK: EQU 0ABH      ; 'ATTRT'
302 AT: EQU 0ACH          ; 'AT'
303 TOK_FN: EQU FN_TK     ; 1ST TOKEN TO REQUIRE A SPACE AFTER
304 TAB: EQU 0ADH         ; 'TAB'
305 VALSTK: EQU 0AEH      ; 'VAL$'
306 LO_MON: EQU 0AFH      ; TOKEN FOR 1ST MONADIC OPTR AFTER VAL$ (CODE)
307 BIN_TK: EQU 0C4H      ; 'BIN'
308 OR_TK: EQU 0C5H       ; 'OR' NB THE TOKENS FOR OR, AND, <=, >=, <> ARE
309                                ; CONSECUTIVE IN THAT ORDER.
310 LINETK: EQU 0CAH      ; 'LINE'
311 THEN: EQU 0CBH        ; 'THEN'
312 TO: EQU 0CCH          ; 'TO'
313 STEP: EQU 0CDH        ; 'STEP'
314 DEF_TK: EQU 0CEH      ; 'DEF'
315 MIN_KW: EQU DEF_TK    ; 1ST TOKEN THAT IS A KEYWORD RATHER THAN _ OPERATOR
316 CAT_TK: EQU 0CFH      ; 'CAT'
317 FORMTK: EQU 0D0H      ; 'FORMAT'
318 MOVETK: EQU 0D1H      ; 'MOVE'
319 DEL_TK: EQU 0D2H      ; 'DELETE'
320 OPN_TK: EQU 0D3H      ; 'OPEN'
321 CLO_TK: EQU 0D4H      ; 'CLOSE'
322 MGE_TK: EQU 0D5H      ; 'MERGE'
323 VFY_TK: EQU 0D6H      ; 'VERIFY'
324 BEEPTK: EQU 0D7H      ; 'BEEP'
325 ARC_TK: EQU 0D8H      ; 'ARC'
326 FGTOK: EQU 0D9H       ; 'FOREGROUND' NB THE TOKENS FOR FORE, BACK, FLASH,
327                                ; BRIGHT, INVERT & OVER ARE CONSECUTIVE IN THAT
328                                ; ORDER.
329 INVTOK: EQU FGTOK+5    ; 'INVERT'
330 OUT_TK: EQU 0DFH      ; 'OUT'
331 LPR_TK: EQU 0E0H      ; 'LPRINT'
332 LL_TK: EQU 0E1H       ; 'LLIST'
333 STOPTK: EQU 0E2H      ; 'STOP'
334 READTK: EQU 0E3H      ; 'READ'
335 DATATK: EQU 0E4H      ; 'DATA'
336 RESTTK: EQU 0E5H      ; 'RESTORE'
337 NEXTOK EQU 0F3H       ; 'NEXT'
338 DUMPTK: EQU 0FFH      ; 'COPY'
339
340 BORDPT: EQU 0FEH      ; OUTPUT PORT FOR SETTING BORDER COLOUR

```

```

341 PR_IN: EQU OFBH      ; FOR INPUT FROM PRINTER
342 PR_OUT: EQU OFBH    ; FOR OUTPUT TO PRINTER.
343 KB_PT: EQU OFEH     ; INPUT PORT FOR READING KEYBOARD
344 O_PORT: EQU OFEH    ; OUTPUT PORT FOR TAPE
345 I_PORT: EQU OFEH    ; INPUT PORT FOR TAPE
346 TAPE_I: EQU 6       ; TAPE INPUT BIT IN (I_PORT)
347                                     ;***ADDITIONAL
348 DKHSPT: EQU OF4H    ; DOCK HORIZONTAL SELECT PORT
349 BDATPT: EQU OFCH    ; EXPANSION BANK DATA PORT
350 BCMDPT: EQU OFDH    ; EXPANSION BANK COMMAND PORT
351 HREXPT: EQU OFFH    ; HOME ROM EXPANSION BANK PORT
352                                     ;***
353
354 ;OFFSETS FROM (CHANS) OF PERMANENT CHANNELS
355
356 CHAN_K: EQU 0        ; KEYBOARD
357 CHAN_S: EQU 5        ; TV SCREEN (UPPER HALF)
358 CHAN_R: EQU 10      ; RAM INSERTION
359 CHAN_P: EQU 15      ; ZX PRINTER
360
361 CH_SET: EQU 4000H-96*8 ; ADDRESS OF CHARACTER SET (STARTING WITH SPACE)
362 *EJECT
363
364 ;CALCULATOR COMMANDS. IN THE DESCRIPTIONS, T & S STAND FOR
365 ; THE TOP & SECOND FROM TOP ON THE CALCULATOR STACK.
366 ; WHERE NECESSARY, FULLER DESCRIPTIONS CAN BE FOUND AT THE
367 ; CODE FOR THE RELEVANT ROUTINES.
368
369 ;THE FOLLOWING COMMANDS HAVE THE STACK POINTERS HL & DE (BUT
370 ; NOT (STKNXT)) DECREMENTED FOR THEM BY CALCTR BEFORE THEY
371 ; ARE CALLED (STKDN).
372
373 IFJUMP: EQU 0 ;S,T -> S; RELATIVE JUMP CONDITIONAL ON VALUE OF T.
374 EXCH: EQU IFJUMP+1 ;(EXCHANGE) S,T -> T,S
375 LOSE: EQU EXCH+1 ;S,T -> S
376 SUB: EQU LOSE+1 ;(SUBTRACT) S,T -> S-T
377 TIMES: EQU SUB+1 ;S,T -> S*T
378 DIV: EQU TIMES+1 ;(DIVIDE) S,T -> S/T
379 POWER: EQU DIV+1 ;S,T -> S**T
380 OR: EQU POWER+1 ;S,T -> S OR T (SEE OR).
381 AND: EQU OR+1 ;S,T -> NUMERICAL S AND T (SEE NOAND).
382 GT: EQU AND+4 ;S,T -> NUMERICAL S>T
383
384 ;5 NUMERIC COMPARISON OPERATIONS HAVE NOT BEEN GIVEN
385 ; MNEMONICS. S,T -> S^T WHERE ^ IS (<=),(>=),(<),(>),(< OR =
386 ; SEE CMPSN.
387 ADD: EQU AND+7 ;S,T -> S+T
388 STGAND: EQU ADD+1 ;S,T -> S& AND& T (SEE STGAND).
389 CONCAT: EQU STGAND+7 ;S,T -> S$ +$ T$
390
391 ;ORDINARY OPERATIONS WITHOUT STKDN.
392
393 VALS: EQU CONCAT+1 ;T$ -> VAL$ T$
394 USRS: EQU VALS+1 ;T$ -> ADDRESS OF BIT PATTERN FOR CORRESPONDING
395 ; USER-DEFINED GRAPHIC
396 INKEY: EQU USRS+1 ;T -> INKEY$ #T
397 NEGATE: EQU INKEY+1 ;T -> -T
398 CODE: EQU NEGATE+1 ;T$ -> CODE T$
399 LO_MON: EQU CODE ;OPERATION CODE FOR LO_MON
400 VAL: EQU LO_MON+1 ;T$ -> VAL T$
401 LEN: EQU VAL+1 ;T$ -> LEN T$
402 SIN: EQU LEN+1 ;T -> SIN T
403 COS: EQU SIN+1 ;T -> COS T
404 TAN: EQU COS+1 ;T -> TAN T
405 ASN: EQU TAN+1 ;T -> ARCSIN T
406 ACS: EQU ASN+1 ;T -> ARCCOS T
407 ATN: EQU ACS+1 ;T -> ARCTAN T
408 LN: EQU ATN+1 ;T -> LN T
409 EXP: EQU LN+1 ;T -> EXP T
410 INT: EQU EXP+1 ;(INTEGER PART) T -> INT T
411 ROOT: EQU INT+1 ;T -> SQUARE ROOT OF T
412 SQN: EQU ROOT+1 ;T -> SQN T

```

```

413 ABS:      EQU SGN+1          ;(ABSOLUTE) T -> \T\
414 PEEK:    EQU ABS+1          ;T -> PEEK T
415 IN:      EQU PEEK+1        ;T -> IN T
416 USR:     EQU IN+1           ;T -> USR T
417 STR:     EQU USR+1         ;T -> STR$ T
418 CHR:     EQU STR+1         ;T -> CHR$ T
419 NOT:     EQU CHR+1         ;T -> BOOLEAN (T = 0)
420 ZERO?:   EQU NOT
421 DUP:     EQU NOT+1         ;(DUPLICATE) T -> T,T
422 INTDIV:  EQU DUP+1         ;(INTEGER DIVISION) S,T -> S MOD T, INT(S/T)
423 JUMP:    EQU INTDIV+1     ;PROGRAMME CONTROL - RELATIVE JUMP BY FOLLOWING BYTE
424 LITERAL: EQU JUMP+1       ;STACKS FOLLOWING NUMBER.
425 LOOP:    EQU LITERAL+1    ;LIKE ZILOG DJNZ
426 MINUS?:  EQU LOOP+1       ;T -> BOOLEAN (T < 0)
427 PLUS?:   EQU MINUS?+1    ;T -> BOOLEAN (T > 0)
428 QUIT:    EQU PLUS?+1     ;RETURNS CONTROL TO Z80
429 ANGLE:   EQU QUIT+1      ;T -> Y WHERE -1 <= Y <= +1 & SIN T = SIN (PI/2*Y)
430          ; MEMORY 0 := TRUE IF T IN 2ND OR 3RD QUADRANT
431 TRUNC:   EQU ANGLE+1     ;(TRUNCATE) T -> INTEGER TRUNCATION OF T TOWARDS 0.
432 XEQTB:   EQU TRUNC+1     ;EXECUTES (BREG) AS A CALCULATOR INSTRUCTION
433 XEY:     EQU XEQTB+1     ;S,T -> S * 10**T
434 FLOAT:   EQU XEY+1       ;T FORCED INTO FLOATING POINT FORM
435
436          ;THE FOLLOWING COMMANDS HAVE ADDED TO THEM AN OPERAND, N.
437
438 CBSV:    EQU 80H          ;SUMS N TERMS OF CHEBYSHEV SERIES (SEE CBSV).
439 CONST:   EQU CBSV+20H    ;(CONSTANT) T -> T, NTH CALCULATOR CONSTANT
440 MINUS1:  EQU CONST+6     ;CALCTR CONSTANT EQUAL TO -1
441 COPY:    EQU CONST+20H   ;T -> T; T COPIED TO NTH CALCULATOR MEMORY
442 MEMORY:  EQU COPY+20H   ;T -> T, CONTENTS OF NTH CALCULATOR MEMORY
443
444 OP_TK:   EQU LO_MON-LO_MON ; TOKEN FOR MONADIC OPTR C IS OP_TK+C
445 HI_MON:  EQU OP_TK+CHR   ; TOKEN FOR LAST MONADIC OPTR EXCEPTING NOT
446 MONOP:   EQU LO_MON.OR.OCOH ; OPERATION CODE FOR LO_MON, TOP 2 BITS SET.
447 LONOMO:  EQU OP_TK+SIN   ; TOKEN FOR 1ST (NUMBER) NUMBER OPTR AFTER -
448 HINOMO:  EQU OP_TK+USR   ; TOKEN FOR LAST (NUMBER) NUMBER OPTR
449
450 *LIST ON

```

APPENDIX C-1
64 COLUMN MODE

TIMEX COMPUTER CORPORATION

APPLICATION DEVELOPMENT LIBRARY

Application Software Component 001

64-COLUMN MODE

Date: 12/15/83
ASC Number: 001
Version: 002
Author: Carol Corcoran

Name: 64 Column Mode Support

Description:

This component provides support to the application programmer for using the 64-column mode feature of the TS 2068. The services include opening/closing the second display file (moving the machine stack, OS RAM routines and BASIC structures), PRINT position control, attribute control, clear screen and scroll screen services and display of characters. For ease of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of POKING the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk 7 (E000H/57344).

Version History

<u>Version</u>	<u>Description</u>	<u>Date</u>
001	Original	11/28/83
002	ADD: 1) Std. and User-Defined Graphics to WRCHAR and GTCHAR. 2) WRSTRG (Write String)	12/15/83

APPLICATIONS INSTRUCTIONS

Name: SETMODE (SETMDB from BASIC - parameter to VIMODE)

Input: MODE (0=normal, 6=64 column mode)

From machine code: Register A
From BASIC: In VIMODE

Description:

Sets specified video mode, opening or closing the second display file where needed. This involves determining if there is enough free RAM to open the second display file and if so, moving the BASIC structures and machine language variables area up and the UDC area down to make space for the machine stack and OS RAM routines at the top of memory. The second display file is cleared to zeroes. The affected system and internal variables are updated or initialized (see Usage Section). When returning to Mode 0 from 64 Column mode, the structures are returned to their normal locations.

Output: BC = 0 Successful
BC = 1 Invalid parameters (not equal to 0 or 6)
BC = 2 Not enough memory

Name: CLRSCN (CLRSCN from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)
Starting Line Number (0-23)

From Machine Code: Line Count In Register B
Starting Line In Register C

From BASIC: Starting Line Number in CLSCTL
Line Count in CLSCTL + 1

Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion
BC = 1 invalid parameters
(Line Number + Line Count < 1 or > 24)

Name: SETCUR (SETCUR from BASIC - parameters to LINCCL)

Input: Line Number (0-23)
Column Number (0-63)

From Machine Code: Line Number In Register B
Column Number In Register C

From BASIC: Column Number In LINCCL
Line Number In LINCCL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

Output: BC = 0 for successful completion
BC = 1 for invalid parameters (Line Number > 23,
Column Number > Line Length-1)

Name: SETATY (SETATB from BASIC - parameter to ATTCTL)

Input: Attribute Byte - bit 7 - FLASH
 bit 6 - BRIGHT
 bit 5 - P
 bit 4 - A
 bit 3 - PER
 bit 2 - I
 bit 1 - M
 bit 0 - K

From Machine Code: Register A
From BASIC: In ATTCTL

Description:

The specified INK color (0-7) is used to set the video mode hardware and to update VIDMOD. The complementary PAPER color is fixed by the INK selection. FLASH and BRIGHT are fixed at zero by the hardware. Note that in 64 column mode the entire screen has the same attributes.

Output: BC = 0 Successful

Name: SETMSK (SETMSB from BASIC - parameters to MSKCTL)

Input: Mask Byte - bit 0 - OVER
 bit 2 - INVERSE

From Machine Code: Register A
From BASIC: MSKCTL

Description:

The specified mask is stored for application to all subsequent display character operations. (OVER = 1 implies new character combined with old using an XOR operation; INVERSE = 1 implies character is inverted).

Output: BC = 0 for successful completion

Name: WRCHR (WRCHB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed
 20H TO 7FH - Std. VS2068 Character Set
 80H TO 8FH - Std. Graphics Set
 90H TO A4H - User-Defined Graphics Set

From Machine Code: Register A
From BASIC: in DATAB

Description:

Displays character at current cursor position, applying current mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLN (see Usage section) and the scroll count (variable SCRCLY) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRCTL and the new line started at the vacated line.

Output: BC = 0 for successful completion
 BC = 1 invalid character code
 BC = 3 for screen full

Name: WRSTRG (WRSTRS from BASIC - String Identifier in PARAMS)

Input: Character Code String

From machine code: Address of string in HL
Count in BC

From BASIC: String Variable Identifier in
System Variable PARAMS - 23747 (SCC3W)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCHR description and Usage Section on Automatic Scrolling). For the Screen Full condition, the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-ACH)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, POKE the code for the string variable identifier into PARAMS prior to invoking WRSTRG, e.g.

```
0005 LEY AB="-----string-----"  
0010 POKE 23747,CODE "a"  
0015 IFUSR(WRSTRG)<>0 THEN -----  
      (continue)
```

Output: BC = 0 Successful
BC = 2 BASIC - String not found
BC = 3 Screen Full - Remaining Count in STRGCT
(HL=Current Address in String)

Name: SCRCLL (SCRCLB from BASIC - parameters to SCRCTL)

Input: Line Count (1-23)
Starting Line Number (1-23)

From Machine Code: Line Count in B
Starting Line in C
From BASIC: Starting Line in SCRCTL
Line Count in SCRCTL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on "automatic" scrolling.

Output: BC = 0 Successful
BC = 1 Invalid Parameters
(Line Number + Line Count < 1 or > 24)

Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GETCHAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position. Note that in 64 column mode the entire screen has common attributes. The value returned will describe the current selection:

Output: BC = 1 for invalid parameters
BC = attribute byte (as for SETATT)

Name: GETCHAR (GETCHAR from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

From Machine Code: Line Number in B
Column Number in C

From BASIC: Column Number in GETCTL
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find
BC = 1 invalid parameters
BC = character code (20H-A4H)

Name: GETCUR (GETCUB from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCOL, the current print position (where the next character would be displayed).

Output: B = Line number (0-23)
C = Column number (0-63)

BASIC: LINCOL - Column number
LINCOL + 1 - Line number

NOTE: If the last character was printed at Col.63 of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

USAGSI

Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
----	----	-----
SCRCTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BOTLN	1	Bottom Line - Line number (0-23) after which test for scroll will be made.
SCRCLT	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.
CHTBL	2	Character Table (Base Address-100H)
GRYBL	2	Std.Graphics Character Table (Base-100H)
LINLEN	1	Line Length - (64 when in 64-Col.Mode)
CURPDS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFACDR	2	Current Display File Address
MASKB	1	Mask Byte (bit 0 = OVER) (bit 2 = INVERSE)

```

ATTBYT      1      Attribute Byte (bits 0 - 2 - INK)
                (bits 3 - 5 - PAPER)
                (bit 6 - BRIGHT (Set to zero
                (bit 7 - FLASH by M/W in
                64-col.mode)

GTINDX      1      "Get" Index - Used by GTCHAR
STRGCT      2      String Count - Contains remaining byte count
:
:
:
:
:

```

Initial values set via SETMODE (SETMDB) are as follows:

Variable Name	Value
SCRCTL	17C1H
BETLN	17H
SCRCLT	1H
CHTEL	3C00H
GRTSL	(Internal to Module)
LINLEN	40H
CURPCS	1841H
DFAGDR	4000H
MASKE	0H
ATTBYT	3BH
GTINDX	80H
STRGCT	0H

The following are the variables used for passing parameters in BASIC. The * indicates those initialized by SETMDB:

Variable Name	Size	Value
* DATAB	1	0H
* LINCOL	2	0H
* CLRCTL	2	1800H
* ATTCTL	1	3BH
* MSKCTL	1	0H
* GETCTL	2	0H
VIMODE	1	

In addition, VIDMOD, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Home Bank.

Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the remapping of certain structures when the second display file is open. NOTE: Machine code above RAMTOP is not moved.

The routine SETMODE (SETMDB) cannot be executed from a cartridge because of the necessity to enable the RDM Extension which disables the DCK Bank.

Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IY Register which must always contain the value SC2AM for access to the standard system variables.

Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BOTLN=23=24th line). Condition Code 3 (Screen Full) will be returned since SCRCLT will decrement to zero. If SCRCLT is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a PDKE or setting the variables BOTLN and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BOTLN be set to Line 21 (19H) and SCRCTL+1 be set to 21 (19H) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRCLT expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the "Screen Full" condition.

By setting the SCRCTL variable and invoking the SCRDL (SCRLB) routine any portion of the screen may be scrolled at any time.

NOTES:

1. All screen operations done by the system ROM (PRINT, LIST, Edit line I/O, CLS, scrolling, etc.) relate only to the main Display File. This means that only the even columns on the 64-column mode screen will be affected. You will want to execute the Clear Screen function in this module to guarantee that no data in the second display file interferes with the use of a system screen service, e.g. prior to doing a LIST.
2. The COPY command will print only the even columns of the screen to the 2040 Printer.
3. During tape operations, the border will not change while in 64-column mode since this is fixed by the hardware to conform to the paper color.
4. The SAVE filename SCREENS will save only the main Display File data. The second can be saved by a SAVE filename CODE 24576, 6144. Be careful that you have the computer in 64-column mode when you load this data or you will overwrite the OS RAM routines and "crash" the system!! (The count for saving the display file is for the data portion only since the Attribute File area from 7800M-7AFFM (30720-31487) is not used by the video mode M/M in 64-Column mode.

AOL - ASC 001 64-CCL.MODE SUPPORT
ID64.SRC

CR280/11 version 10.36.14

16-Mar-84 13:013

```

1      NAME AOL - ASC 001 64-CCL.MODE SUPPORT
2      |
3      |
4      |
5      |
6      |
7      |
8      |
9      |
10     |
11     |          *****
12     |          *                                     *
13     |          *           TITLE                     *
14     |          * APPLICATION DEVELOPMENT LIBRARY *
15     |          * NAME: 64-CCL.MODE SUPPORT        *
16     |          *                                     *
17     |          * ASC MOD 001                       *
18     |          * VERSION: 002                      *
19     |          * AUTHGR: C. CONCORAN             *
20     |          * DATE: 12/19/83                  *
21     |          *          *****
22     |
23     |          SUBTTL VERSION LEVEL CONTROL
24     |
25     |
26     |          VERSION          DATE          COMMENTS
27     |          -----          ---
28     |
29     |          001          11/28/83          ORIGINAL
30     |
31     |          002          12/19/83          ADD STANDARD AND USER-DEFINED
32     |                                     GRAPHICS TO WRCMAR AND GTCMAR
33     |
34     |                                     ADD WRSTRO (WRITE STRING)
35     |                                     CAPABILITY
36     |
37     |          SUBTTL DEFINITIONS
38     |
39     |
40     |
41     |          *****DEFINITIONS*****
42     |
43     |          INTERN  WRCMAR,WRSTRO,SETCUR,SETATT,SETMS4,CLRSCN,SCRCLL
44     |          INTERN  GTCMAR,GTATT,GETCUR
45     |          INTERN  WRCMR8,WRSTR8,SETCUR,SETAT8,SETMS8,CLRSC8,SCRCL8
46     |          INTERN  GTCMR8,GETAT8,GETCUR
47     |          INTERN  SETMODE,SETMDO
48     |
49     |
50     |          0001      EQU      24           | 24 LINES
51     |          1701      EQU      1701H      | SCRCLL CCLTRCL INITIALIZATION
52     |          1800      EQU      1800H      | CLEAR SCREEN CTL. INIT.
53     |          3C00      EQU      3C00H      | ROP CHAR.TABLE=100H
54     |          0395      EQU      ((GRPST)-100H) |STD.GRAPHICS CHARS.
55     |
56     |          0808      EQU      0808H      | ROUTINE IN ROM EXTENSION
57     |          1720      EQU      1720H      | ROUTINE IN HOME ROM
58     |
59     |          0PPH      EQU      0PPH       | HOME ROM EXT.SELECT PORT (BIT 7)
60     |          0P4H      EQU      0P4H       | DOCK HORIZONTAL SELECT PORT
61     |
62     |          9C40      EQU      9C40H      | SYSTEM VARIABLES
63     |          9C65      EQU      9C65H      |
64     |          9C70      EQU      9C70H      |
65     |          9C7D      EQU      9C7DH      |
66     |          9C82      EQU      9C82H      |
67     |          9CB4      EQU      9CB4H      |
68     |          9CC2      EQU      9CC2H      |
69     |          9CC3      EQU      9CC3H      | (LOCATION 23747)
70     |
71     |
72     |
73     |
74     |
75     |
76     |
77     |
78     |
79     |
80     |
81     |
82     |
83     |
84     |
85     |
86     |
87     |
88     |
89     |
90     |
91     |
92     |
93     |
94     |
95     |
96     |
97     |
98     |
99     |
100    |
101    |
102    |
103    |
104    |
105    |
106    |
107    |
108    |
109    |
110    |
111    |
112    |
113    |
114    |
115    |
116    |
117    |
118    |
119    |
120    |
121    |
122    |
123    |
124    |
125    |
126    |
127    |
128    |
129    |
130    |
131    |
132    |
133    |
134    |
135    |
136    |
137    |
138    |
139    |
140    |
141    |
142    |
143    |
144    |
145    |
146    |
147    |
148    |
149    |
150    |
151    |
152    |
153    |
154    |
155    |
156    |
157    |
158    |
159    |
160    |
161    |
162    |
163    |
164    |
165    |
166    |
167    |
168    |
169    |
170    |
171    |
172    |
173    |
174    |
175    |
176    |
177    |
178    |
179    |
180    |
181    |
182    |
183    |
184    |
185    |
186    |
187    |
188    |
189    |
190    |
191    |
192    |
193    |
194    |
195    |
196    |
197    |
198    |
199    |
200    |
201    |
202    |
203    |
204    |
205    |
206    |
207    |
208    |
209    |
210    |
211    |
212    |
213    |
214    |
215    |
216    |
217    |
218    |
219    |
220    |
221    |
222    |
223    |
224    |
225    |
226    |
227    |
228    |
229    |
230    |
231    |
232    |
233    |
234    |
235    |
236    |
237    |
238    |
239    |
240    |
241    |
242    |
243    |
244    |
245    |
246    |
247    |
248    |
249    |
250    |
251    |
252    |
253    |
254    |
255    |
256    |
257    |
258    |
259    |
260    |
261    |
262    |
263    |
264    |
265    |
266    |
267    |
268    |
269    |
270    |
271    |
272    |
273    |
274    |
275    |
276    |
277    |
278    |
279    |
280    |
281    |
282    |
283    |
284    |
285    |
286    |
287    |
288    |
289    |
290    |
291    |
292    |
293    |
294    |
295    |
296    |
297    |
298    |
299    |
300    |
301    |
302    |
303    |
304    |
305    |
306    |
307    |
308    |
309    |
310    |
311    |
312    |
313    |
314    |
315    |
316    |
317    |
318    |
319    |
320    |
321    |
322    |
323    |
324    |
325    |
326    |
327    |
328    |
329    |
330    |
331    |
332    |
333    |
334    |
335    |
336    |
337    |
338    |
339    |
340    |
341    |
342    |
343    |
344    |
345    |
346    |
347    |
348    |
349    |
350    |
351    |
352    |
353    |
354    |
355    |
356    |
357    |
358    |
359    |
360    |
361    |
362    |
363    |
364    |
365    |
366    |
367    |
368    |
369    |
370    |
371    |
372    |
373    |
374    |
375    |
376    |
377    |
378    |
379    |
380    |
381    |
382    |
383    |
384    |
385    |
386    |
387    |
388    |
389    |
390    |
391    |
392    |
393    |
394    |
395    |
396    |
397    |
398    |
399    |
400    |
401    |
402    |
403    |
404    |
405    |
406    |
407    |
408    |
409    |
410    |
411    |
412    |
413    |
414    |
415    |
416    |
417    |
418    |
419    |
420    |
421    |
422    |
423    |
424    |
425    |
426    |
427    |
428    |
429    |
430    |
431    |
432    |
433    |
434    |
435    |
436    |
437    |
438    |
439    |
440    |
441    |
442    |
443    |
444    |
445    |
446    |
447    |
448    |
449    |
450    |
451    |
452    |
453    |
454    |
455    |
456    |
457    |
458    |
459    |
460    |
461    |
462    |
463    |
464    |
465    |
466    |
467    |
468    |
469    |
470    |
471    |
472    |
473    |
474    |
475    |
476    |
477    |
478    |
479    |
480    |
481    |
482    |
483    |
484    |
485    |
486    |
487    |
488    |
489    |
490    |
491    |
492    |
493    |
494    |
495    |
496    |
497    |
498    |
499    |
500    |
501    |
502    |
503    |
504    |
505    |
506    |
507    |
508    |
509    |
510    |
511    |
512    |
513    |
514    |
515    |
516    |
517    |
518    |
519    |
520    |
521    |
522    |
523    |
524    |
525    |
526    |
527    |
528    |
529    |
530    |
531    |
532    |
533    |
534    |
535    |
536    |
537    |
538    |
539    |
540    |
541    |
542    |
543    |
544    |
545    |
546    |
547    |
548    |
549    |
550    |
551    |
552    |
553    |
554    |
555    |
556    |
557    |
558    |
559    |
560    |
561    |
562    |
563    |
564    |
565    |
566    |
567    |
568    |
569    |
570    |
571    |
572    |
573    |
574    |
575    |
576    |
577    |
578    |
579    |
580    |
581    |
582    |
583    |
584    |
585    |
586    |
587    |
588    |
589    |
590    |
591    |
592    |
593    |
594    |
595    |
596    |
597    |
598    |
599    |
600    |
601    |
602    |
603    |
604    |
605    |
606    |
607    |
608    |
609    |
610    |
611    |
612    |
613    |
614    |
615    |
616    |
617    |
618    |
619    |
620    |
621    |
622    |
623    |
624    |
625    |
626    |
627    |
628    |
629    |
630    |
631    |
632    |
633    |
634    |
635    |
636    |
637    |
638    |
639    |
640    |
641    |
642    |
643    |
644    |
645    |
646    |
647    |
648    |
649    |
650    |
651    |
652    |
653    |
654    |
655    |
656    |
657    |
658    |
659    |
660    |
661    |
662    |
663    |
664    |
665    |
666    |
667    |
668    |
669    |
670    |
671    |
672    |
673    |
674    |
675    |
676    |
677    |
678    |
679    |
680    |
681    |
682    |
683    |
684    |
685    |
686    |
687    |
688    |
689    |
690    |
691    |
692    |
693    |
694    |
695    |
696    |
697    |
698    |
699    |
700    |
701    |
702    |
703    |
704    |
705    |
706    |
707    |
708    |
709    |
710    |
711    |
712    |
713    |
714    |
715    |
716    |
717    |
718    |
719    |
720    |
721    |
722    |
723    |
724    |
725    |
726    |
727    |
728    |
729    |
730    |
731    |
732    |
733    |
734    |
735    |
736    |
737    |
738    |
739    |
740    |
741    |
742    |
743    |
744    |
745    |
746    |
747    |
748    |
749    |
750    |
751    |
752    |
753    |
754    |
755    |
756    |
757    |
758    |
759    |
760    |
761    |
762    |
763    |
764    |
765    |
766    |
767    |
768    |
769    |
770    |
771    |
772    |
773    |
774    |
775    |
776    |
777    |
778    |
779    |
780    |
781    |
782    |
783    |
784    |
785    |
786    |
787    |
788    |
789    |
790    |
791    |
792    |
793    |
794    |
795    |
796    |
797    |
798    |
799    |
800    |
801    |
802    |
803    |
804    |
805    |
806    |
807    |
808    |
809    |
810    |
811    |
812    |
813    |
814    |
815    |
816    |
817    |
818    |
819    |
820    |
821    |
822    |
823    |
824    |
825    |
826    |
827    |
828    |
829    |
830    |
831    |
832    |
833    |
834    |
835    |
836    |
837    |
838    |
839    |
840    |
841    |
842    |
843    |
844    |
845    |
846    |
847    |
848    |
849    |
850    |
851    |
852    |
853    |
854    |
855    |
856    |
857    |
858    |
859    |
860    |
861    |
862    |
863    |
864    |
865    |
866    |
867    |
868    |
869    |
870    |
871    |
872    |
873    |
874    |
875    |
876    |
877    |
878    |
879    |
880    |
881    |
882    |
883    |
884    |
885    |
886    |
887    |
888    |
889    |
890    |
891    |
892    |
893    |
894    |
895    |
896    |
897    |
898    |
899    |
900    |
901    |
902    |
903    |
904    |
905    |
906    |
907    |
908    |
909    |
910    |
911    |
912    |
913    |
914    |
915    |
916    |
917    |
918    |
919    |
920    |
921    |
922    |
923    |
924    |
925    |
926    |
927    |
928    |
929    |
930    |
931    |
932    |
933    |
934    |
935    |
936    |
937    |
938    |
939    |
940    |
941    |
942    |
943    |
944    |
945    |
946    |
947    |
948    |
949    |
950    |
951    |
952    |
953    |
954    |
955    |
956    |
957    |
958    |
959    |
960    |
961    |
962    |
963    |
964    |
965    |
966    |
967    |
968    |
969    |
970    |
971    |
972    |
973    |
974    |
975    |
976    |
977    |
978    |
979    |
980    |
981    |
982    |
983    |
984    |
985    |
986    |
987    |
988    |
989    |
990    |
991    |
992    |
993    |
994    |
995    |
996    |
997    |
998    |
999    |
1000   |
1001   |
1002   |
1003   |
1004   |
1005   |
1006   |
1007   |
1008   |
1009   |
1010   |
1011   |
1012   |
1013   |
1014   |
1015   |
1016   |
1017   |
1018   |
1019   |
1020   |
1021   |
1022   |
1023   |
1024   |
1025   |
1026   |
1027   |
1028   |
1029   |
1030   |
1031   |
1032   |
1033   |
1034   |
1035   |
1036   |
1037   |
1038   |
1039   |
1040   |
1041   |
1042   |
1043   |
1044   |
1045   |
1046   |
1047   |
1048   |
1049   |
1050   |
1051   |
1052   |
1053   |
1054   |
1055   |
1056   |
1057   |
1058   |
1059   |
1060   |
1061   |
1062   |
1063   |
1064   |
1065   |
1066   |
1067   |
1068   |
1069   |
1070   |
1071   |
1072   |
1073   |
1074   |
1075   |
1076   |
1077   |
1078   |
1079   |
1080   |
1081   |
1082   |
1083   |
1084   |
1085   |
1086   |
1087   |
1088   |
1089   |
1090   |
1091   |
1092   |
1093   |
1094   |
1095   |
1096   |
1097   |
1098   |
1099   |
1100   |
1101   |
1102   |
1103   |
1104   |
1105   |
1106   |
1107   |
1108   |
1109   |
1110   |
1111   |
1112   |
1113   |
1114   |
1115   |
1116   |
1117   |
1118   |
1119   |
1120   |
1121   |
1122   |
1123   |
1124   |
1125   |
1126   |
1127   |
1128   |
1129   |
1130   |
1131   |
1132   |
1133   |
1134   |
1135   |
1136   |
1137   |
1138   |
1139   |
1140   |
1141   |
1142   |
1143   |
1144   |
1145   |
1146   |
1147   |
1148   |
1149   |
1150   |
1151   |
1152   |
1153   |
1154   |
1155   |
1156   |
1157   |
1158   |
1159   |
1160   |
1161   |
1162   |
1163   |
1164   |
1165   |
1166   |
1167   |
1168   |
1169   |
1170   |
1171   |
1172   |
1173   |
1174   |
1175   |
1176   |
1177   |
1178   |
1179   |
1180   |
1181   |
1182   |
1183   |
1184   |
1185   |
1186   |
1187   |
1188   |
1189   |
1190   |
1191   |
1192   |
1193   |
1194   |
1195   |
1196   |
1197   |
1198   |
1199   |
1200   |
1201   |
1202   |
1203   |
1204   |
1205   |
1206   |
1207   |
1208   |
1209   |
1210   |
1211   |
1212   |
1213   |
1214   |
1215   |
1216   |
1217   |
1218   |
1219   |
1220   |
1221   |
1222   |
1223   |
1224   |
1225   |
1226   |
1227   |
1228   |
1229   |
1230   |
1231   |
1232   |
1233   |
1234   |
1235   |
1236   |
1237   |
1238   |
1239   |
1240   |
1241   |
1242   |
1243   |
1244   |
1245   |
1246   |
1247   |
1248   |
1249   |
1250   |
1251   |
1252   |
1253   |
1254   |
1255   |
1256   |
1257   |
1258   |
1259   |
1260   |
1261   |
1262   |
1263   |
1264   |
1265   |
1266   |
1267   |
1268   |
1269   |
1270   |
1271   |
1272   |
1273   |
1274   |
1275   |
1276   |
1277   |
1278   |
1279   |
1280   |
1281   |
1282   |
1283   |
1284   |
1285   |
1286   |
1287   |
1288   |
1289   |
1290   |
1291   |
1292   |
1293   |
1294   |
1295   |
1296   |
1297   |
1298   |
1299   |
1300   |
1301   |
1302   |
1303   |
1304   |
1305   |
1306   |
1307   |
1308   |
1309   |
1310   |
1311   |
1312   |
1313   |
1314   |
1315   |
1316   |
1317   |
1318   |
1319   |
1320   |
1321   |
1322   |
1323   |
1324   |
1325   |
1326   |
1327   |
1328   |
1329   |
1330   |
1331   |
1332   |
1333   |
1334   |
1335   |
1336   |
1337   |
1338   |
1339   |
1340   |
1341   |
1342   |
1343   |
1344   |
1345   |
1346   |
1347   |
1348   |
1349   |
1350   |
1351   |
1352   |
1353   |
1354   |
1355   |
1356   |
1357   |
1358   |
1359   |
1360   |
1361   |
1362   |
1363   |
1364   |
1365   |
1366   |
1367   |
1368   |
1369   |
1370   |
1371   |
1372   |
1373   |
1374   |
1375   |
1376   |
1377   |
1378   |
1379   |
1380   |
1381   |
1382   |
1383   |
1384   |
1385   |
1386   |
1387   |
1388   |
1389   |
1390   |
1391   |
1392   |
1393   |
1394   |
1395   |
1396   |
1397   |
1398   |
1399   |
1400   |
1401   |
1402   |
1403   |
1404   |
1405   |
1406   |
1407   |
1408   |
1409   |
1410   |
1411   |
1412   |
1413   |
1414   |
1415   |
1416   |
1417   |
1418   |
1419   |
1420   |
1421   |
1422   |
1423   |
1424   |
1425   |
1426   |
1427   |
1428   |
1429   |
1430   |
1431   |
1432   |
1433   |
1434   |
1435   |
1436   |
1437   |
1438   |
1439   |
1440   |
1441   |
1442   |
1443   |
1444   |
1445   |
1446   |
1447   |
1448   |
1449   |
1450   |
1451   |
1452   |
1453   |
1454   |
1455   |
1456   |
1457   |
1458   |
1459   |
1460   |
1461   |
1462   |
1463   |
1464   |
1465   |
1466   |
1467   |
1468   |
1469   |
1470   |
1471   |
1472   |
1473   |
1474   |
1475   |
1476   |
1477   |
1478   |
1479   |
1480   |
1481   |
1482   |
1483   |
1484   |
1485   |
1486   |
1487   |
1488   |
1489   |
1490   |
1491   |
1492   |
1493   |
1494   |
1495   |
1496   |
1497   |
1498   |
1499   |
1500   |
1501   |
1502   |
1503   |
1504   |
1505   |
1506   |
1507   |
1508   |
1509   |
1510   |
1511   |
1512   |
1513   |
1514   |
1515   |
1516   |
1517   |
1518   |
1519   |
1520   |
1521   |
1522   |
1523   |
1524   |
1525   |
1526   |
1527   |
1528   |
1529   |
1530   |
1531   |
1532   |
1533   |
1534   |
1535   |
1536   |
1537   |
1538   |
1539   |
1540   |
1541   |
1542   |
1543   |
1544   |
1545   |
1546   |
1547   |
1548   |
1549   |
1550   |
1551   |
1552   |
1553   |
1554   |
1555   |
1556   |
1557   |
1558   |
1559   |
1560   |
1561   |
1562   |
1563   |
1564   |
1565   |
1566   |
1567   |
1568   |
1569   |
1570   |
1571   |
1572   |
1573   |
1574   |
1575   |
1576   |
1577   |
1578   |
1579   |
1580   |
1581   |
1582   |
1583   |
1584   |
1585   |
1586   |
1587   |
1588   |
1589   |
1590   |
1591   |
1592   |
1593   |
1594   |
1595   |
1596   |
1597   |
1598   |
1599   |
1600   |
1601   |
1602   |
1603   |
1604   |
1605   |
1606   |
1607   |
1608   |
1609   |
1610   |
1611   |
1612   |
1613   |
1614   |
1615   |
1616   |
1617   |
1618   |
1619   |
1620   |
1621   |
1622   |
1623   |
1624   |
1625   |
1626   |
1627   |
1628   |
1629   |
1630   |
1631   |
1632   |
1633   |
1634   |
1635   |
1636   |
1637   |
1638   |
1639   |
1640   |
1641   |
1642   |
1643   |
1644   |
1645   |
1646   |
1647   |
1648   |
1649   |
1650   |
1651   |
1652   |
1653   |
1654   |
1655   |
1656   |
1657   |
1658   |
1659   |
1660   |
1661   |
1662   |
1663   |
1664   |
1665   |
1666   |
1667   |
1668   |
1669   |
1670   |
1671   |
1672   |
1673   |
1674   |
1675   |
1676   |
1677   |
1678   |
1679   |
1680   |
1681   |
1682   |
1683   |
1684   |
1685   |
1686   |
1687   |
1688   |
1689   |
1690   |
1691   |
1692   |
1693   |
1694   |
1695   |
1696   |
1697   |
1698   |
1699   |
1700   |
1701   |
1702   |
1703   |
1704   |
1705   |
1706   |
1707   |
1708   |
1709   |
1710   |
1711   |
1712   |
1713   |
1714   |
1715   |
1716   |
1717   |
1718   |
1719   |
1720   |
1721   |
1722   |
1723   |
1724   |
1725   |
1726   |
1727   |
1728   |
1729   |
1730   |
1731   |
1732   |
1733   |
1734   |
1735   |
1736   |
1737   |
1738   |
1739   |
1740   |
1741   |
1742   |
1743   |
1744   |
1745   |
1746   |
1747   |
1748   |
1749   |
1750   |
1751   |
1752
```

```

=6840          69 DRIVES      EQU 6840H 1
=12C0          70 INSERTSI  EQU 7800H-DRIVES
=0840          71 MCVESI   EQU DRIVES-6000H
=PTC0         72 DEST7    EQU 8PPPM-MOVE$2-1
=97C0         73 PIX      EQU DEST7-6000H
=1800         74          EQU 1800H
              75
              76
              77
              SUBTTL INPUTS AND ENTRIES
              80
              81
              82
              83
              84
              85
              86
              87
              88
              89
              90
              91
              92
              93
              94
              95
              96
              97
              98
              99
              100
              101
              102
              103
              104
              105
              106
              107
              108
              109
              110
              111
              112
              113
              114
              115
              116
              117
              118
              119
              120
              121
              122
              123
              124
              125
              126
              127
              128
              129
              130
              131
              132
              133
              134
              135
              136
              137
              138
              139
              140
              141
              142
              143
              144
              145
              146
              147
              148
              149
              150
              151
              152
              153
              154
              155
              156
              157
              158
              159
              160
              161
              162
              163
              164
              165
              166
              167
              168
              169
              170
              171
              172
              173
              174
              175
              176
              177
              178
              179
              180
              181
              182
              183
              184
              185
              186
              187
              188
              189
              190
              191
              192
              193
              194
              195
              196
              197
              198
              199
              200
              201
              202
              203
              204
              205
              206
              207
              208
              209
              210
              211
              212
              213
              214
              215
              216
              217
              218
              219
              220
              221
              222
              223
              224
              225
              226
              227
              228
              229
              230
              231
              232
              233
              234
              235
              236
              237
              238
              239
              240
              241
              242
              243
              244
              245
              246
              247
              248
              249
              250
              251
              252
              253
              254
              255
              256
              257
              258
              259
              260
              261
              262
              263
              264
              265
              266
              267
              268
              269
              270
              271
              272
              273
              274
              275
              276
              277
              278
              279
              280
              281
              282
              283
              284
              285
              286
              287
              288
              289
              290
              291
              292
              293
              294
              295
              296
              297
              298
              299
              300
              301
              302
              303
              304
              305
              306
              307
              308
              309
              310
              311
              312
              313
              314
              315
              316
              317
              318
              319
              320
              321
              322
              323
              324
              325
              326
              327
              328
              329
              330
              331
              332
              333
              334
              335
              336
              337
              338
              339
              340
              341
              342
              343
              344
              345
              346
              347
              348
              349
              350
              351
              352
              353
              354
              355
              356
              357
              358
              359
              360
              361
              362
              363
              364
              365
              366
              367
              368
              369
              370
              371
              372
              373
              374
              375
              376
              377
              378
              379
              380
              381
              382
              383
              384
              385
              386
              387
              388
              389
              390
              391
              392
              393
              394
              395
              396
              397
              398
              399
              400
              401
              402
              403
              404
              405
              406
              407
              408
              409
              410
              411
              412
              413
              414
              415
              416
              417
              418
              419
              420
              421
              422
              423
              424
              425
              426
              427
              428
              429
              430
              431
              432
              433
              434
              435
              436
              437
              438
              439
              440
              441
              442
              443
              444
              445
              446
              447
              448
              449
              450
              451
              452
              453
              454
              455
              456
              457
              458
              459
              460
              461
              462
              463
              464
              465
              466
              467
              468
              469
              470
              471
              472
              473
              474
              475
              476
              477
              478
              479
              480
              481
              482
              483
              484
              485
              486
              487
              488
              489
              490
              491
              492
              493
              494
              495
              496
              497
              498
              499
              500
              501
              502
              503
              504
              505
              506
              507
              508
              509
              510
              511
              512
              513
              514
              515
              516
              517
              518
              519
              520
              521
              522
              523
              524
              525
              526
              527
              528
              529
              530
              531
              532
              533
              534
              535
              536
              537
              538
              539
              540
              541
              542
              543
              544
              545
              546
              547
              548
              549
              550
              551
              552
              553
              554
              555
              556
              557
              558
              559
              560
              561
              562
              563
              564
              565
              566
              567
              568
              569
              570
              571
              572
              573
              574
              575
              576
              577
              578
              579
              580
              581
              582
              583
              584
              585
              586
              587
              588
              589
              590
              591
              592
              593
              594
              595
              596
              597
              598
              599
              600
              601
              602
              603
              604
              605
              606
              607
              608
              609
              610
              611
              612
              613
              614
              615
              616
              617
              618
              619
              620
              621
              622
              623
              624
              625
              626
              627
              628
              629
              630
              631
              632
              633
              634
              635
              636
              637
              638
              639
              640
              641
              642
              643
              644
              645
              646
              647
              648
              649
              650
              651
              652
              653
              654
              655
              656
              657
              658
              659
              660
              661
              662
              663
              664
              665
              666
              667
              668
              669
              670
              671
              672
              673
              674
              675
              676
              677
              678
              679
              680
              681
              682
              683
              684
              685
              686
              687
              688
              689
              690
              691
              692
              693
              694
              695
              696
              697
              698
              699
              700
              701
              702
              703
              704
              705
              706
              707
              708
              709
              710
              711
              712
              713
              714
              715
              716
              717
              718
              719
              720
              721
              722
              723
              724
              725
              726
              727
              728
              729
              730
              731
              732
              733
              734
              735
              736
              737
              738
              739
              740
              741
              742
              743
              744
              745
              746
              747
              748
              749
              750
              751
              752
              753
              754
              755
              756
              757
              758
              759
              760
              761
              762
              763
              764
              765
              766
              767
              768
              769
              770
              771
              772
              773
              774
              775
              776
              777
              778
              779
              780
              781
              782
              783
              784
              785
              786
              787
              788
              789
              790
              791
              792
              793
              794
              795
              796
              797
              798
              799
              800
              801
              802
              803
              804
              805
              806
              807
              808
              809
              810
              811
              812
              813
              814
              815
              816
              817
              818
              819
              820
              821
              822
              823
              824
              825
              826
              827
              828
              829
              830
              831
              832
              833
              834
              835
              836
              837
              838
              839
              840
              841
              842
              843
              844
              845
              846
              847
              848
              849
              850
              851
              852
              853
              854
              855
              856
              857
              858
              859
              860
              861
              862
              863
              864
              865
              866
              867
              868
              869
              870
              871
              872
              873
              874
              875
              876
              877
              878
              879
              880
              881
              882
              883
              884
              885
              886
              887
              888
              889
              890
              891
              892
              893
              894
              895
              896
              897
              898
              899
              900
              901
              902
              903
              904
              905
              906
              907
              908
              909
              910
              911
              912
              913
              914
              915
              916
              917
              918
              919
              920
              921
              922
              923
              924
              925
              926
              927
              928
              929
              930
              931
              932
              933
              934
              935
              936
              937
              938
              939
              940
              941
              942
              943
              944
              945
              946
              947
              948
              949
              950
              951
              952
              953
              954
              955
              956
              957
              958
              959
              960
              961
              962
              963
              964
              965
              966
              967
              968
              969
              970
              971
              972
              973
              974
              975
              976
              977
              978
              979
              980
              981
              982
              983
              984
              985
              986
              987
              988
              989
              990
              991
              992
              993
              994
              995
              996
              997
              998
              999
              1000

```

```

0032* P2 90          106          CP          90M          I TEST IF STD.GRAPHICS(800M-8PM)
0033* 30 09          107          JR          C,WRCM11
0034* 20 40 3C7B     108          LD          8C,(U00E) I USER-DEFINED GRAPHICS(10M-46M)
0035* 05            109          DEC          0          I ADJUST ADDRESS-100M
0036* 04 70          110          SUB          70M          I ADJUST FOR INDEX INTO TABLE
0037* 10 0C          111          JR          WRCM13
0038* 2C 40 0031*    112          WRCM11 LD          8C,(GRT0L) I ADDRESS OF GRAPHICS TABLE
0039* 04 40          113          SUB          60M          I ADJUST FOR INDEX INTO TABLE
0040* 10 04          114          JR          WRCM13
0041* 20 40 002F*    115          WRCM12 LD          8C,(CNT0L) I CHARACTER TABLE
0042* 00            116          WRCM13 ER          0E,HL          I 0E=POSN.IN DISPLAY FILE
0043* 24 00          117          LD          M,3          I CODE IS INDEX INTO TABLE
0044* 0F            118          LD          L,0
0045* 29            119          ADD          HL,HL
0046* 20            120          ADD          HL,HL
0047* 20            121          ADD          HL,HL
0048* 09            122          ADD          HL,HL
0049* 21            123          POP          BC          I HL=PIXEL PATTERN IN TABLE
0050* 00            124          EX          0E,HL
0051* 79            125          LD          A,C          I TEST IF END OF LINE
0052* 30            126          DEC          A
0053* 3A 0033*       127          LD          A,(LINLEN)
0054* 20 05          128          JR          NZ,WRCM16
0055* 3C            129          INC          A          I START OF NEW LINE
0056* 05            130          DEC          B          I BUMP LINE NO.
0057* 4F            131          LD          C,A          I LINLEN=1=START OF LINE
0058* 18 01          132          JR          WRCM18
0059* 3C            133          WRCM16 INC          A
0060* 09            134          WRCM18 CP          C          I TEST IF START OF LINE
0061* 05            135          PUSH        DE
0062* CC 00C6*       136          CALL        Z,TVPUL0 I TEST IF OFF SCREEN
0063* 01            137          POP         DE
0064* 01            138          WRCM2  HERE TO PUT CHARACTER IN DISPLAY FILE
0065* C5            139          WRCM2  PUSH        BC          I CURSOR POSITION
0066* 25            140          WRCM2  PUSH        HL          I DISPLAY FILE ADRS.
0067* 3A 0038*       141          LD          A,(BASE0)
0068* 06 PP          142          LD          0,-1
0069* 1F            143          RRA
0070* 30 01          144          JR          C,WRCM3 I IF XORING NEW INTO OLD
0071* 04            145          INC          B          I 0=-1 IF XORING =0 IF NOT
0072* 1F            146          WRCM3  RRA
0073* 1F            147          RRA
0074* 9F            148          SBC          A,B
0075* 4F            149          LD          C,A          I C=0 FOR INVERSE =0 IF NOT
0076* 3E 00          150          LD          A,B          I SCAN COUNT
0077* A7            151          AND          A
0078* 00            152          EX          0E,HL
0079* 00            153          WRCM5  EX          AF,AF
0080* 1A            154          LD          A,(CDE)
0081* A0            155          AND          B          I CLR BIT ROW
0082* AE            156          XOR          (HL)
0083* A9            157          XOR          C
0084* 12            158          LD          (DE),A
0085* 00            159          EX          AF,AF
0086* 14            160          INC          D          I NEXT SCAN
0087* 23            161          INC          HL          I NEXT BYTE IN CHAR.FILE
0088* 3D            162          DEC          A          I TEST IF MORE SCANS
0089* 20 P4          163          JR          NZ,WRCM5
0090* 01            164          WRCM7  POP         HL
0091* C1            165          WRCM7  POP         BC
0092* 0C            166          DEC          C          I ADJUST CURSOR POSITION
0093* 79            167          LD          A,C          I TEST IF EVEN/ODD
0094* CB 47          168          LD          D,A
0095* 2D 06          169          JR          NZ,CCL00D
0096* 7C            170          CCL00D LD          A,M
0097* P0 20          171          OR          20M
0098* 67            172          LD          M,A
0099* 10 05          173          JR          WRCM7
0100* 7C            174          CCL00D LD          A,M
0101* 00 0F          175          AND          0FFH
0102* 67            176          LD          M,A
0103* 23            177          INC          HL          I DPI NEXT BYTE
0104* CC 0485*       178          WRCM7  CALL        STPCS4 I STORE NEW POSITION
0105* 0E 00          179          G0J00EY LD          C,0 I RETURN BC=0
0106* 04 00          180          ERRRET LD          0,0 I ENTER HERE WITH C NON-ZERO
0107* C9            181          RET
0108* 00            182          I
0109* 0E 01          183          INVPAR LD          C,1 I RETURN BC=1 FOR INVALID PARAMETERS
0110* 10 P9          184          JR          ERRRET
0111* 00            185          I
0112* 3E 10          186          TVPUL0 LD          A,SCRZI I TEST IF NEW LINE IS OFF SCREEN
0113* 90            187          SUB          0          I A=LINE NO.
0114* 97            188          LD          D,A          I SAVE IN D
0115* 3A 0020*       189          LD          A,(BOTLN) I GET BOTTOM LINE
0116* 0A            190          CP          0
0117* 02 0456*       191          JP          NC,JPP05M I ON SCREEN - RETURN TO WRITE CHAR.
0118* 00            192          I VIA UPDATE AND STORE POSITION
0119* 3A 002E*       193          LD          A,(SCRCLCT) I TEST SCRCLL COUNT
0120* 3C            194          DEC          A
0121* 20 00          195          JR          NZ,TVPUL1 I DO AUTOMATIC SCROLL USING SCRCLL
0122* 30 01          196          LD          A,1 I REINITIALISE TO 1
0123* 32 002E*       197          LD          (SCRCLCT),A
0124* C1            198          POP         BC
0125* C1            199          WRCM7  POP         BC
0126* 0E 03          200          LD          C,3 I DISCARD RETURN TO WRITE CHAR.
0127* 10 00          201          JR          ERRRET I RETURN BC=3 FOR SCREEN FULL
0128* 32 002E*       202          TVPUL1 LD          (SCRCLCT),A I UPDATE VARIABLE
0129* 2C 40 0028*    203          LD          BC,(SCRCLT) I GET SCRCLL CONTROLS (STARTING LINE
0130* 00            204          I AND LINE COUNT)
0131* C3 0150*       205          JP          SCRL0 I RETURN TO WRITE CHAR. VIA SCRCLL
0132* 00            206          I
0133* 00            207          SUBTTL WRITE STRING
0134* 00            208          I
0135* 00            209          I
0136* 00            210          I
0137* 00            211          I
0138* 00            212          I
0139* 3A SCC3        213          WRSTRO LD          A,(PARAMS) I HERE FROM BASIC ENTRY TO
0140* 46 1F          214          AND          1FH          I WRITE CHARACTER STRING TO SCREEN.
0141* P0 40          215          OR          40H          I STRING IDENTIFIER (CM.CODE) IN
0142* 57            216          LD          D,A          I SYSTEM VARIABLE PARAM AT SCC3M
0143* 2A 3C40        217          LD          HL,(VARS) I GET STRING ID
0144* 7E            218          WASTRI LD          A,(L) I MASK OFF UPPER BITS
0145* 00            219          I STRING VARIABLE IDENTIFIER
0146* 00            220          I SAVE IN D
0147* 00            221          I FIND STRING
0148* 00            222          I

```



```

00F8 06 7F          299      AND 07F          I TEST IF END OF VARS AREA
00FA 28 35          300      JR Z,NOSTRG      I NO FIND - RETURN BC=2
00FC 8A             301      CP 0             I TEST IF MATCH
00FD 28 08          302      JR Z,WRSTR2     I POUND STRING
00FF 05             303      PUSH DE         I SAVE STRING ID
0100 CC 1720        304      CALL RECLEM     I RETURNS ADDR. OF NEXT VAR. IN DE
0103 08             305      EX DE,ML        I ADDR. TO ML
0104 01             306      POP DE          I RESTORE STRING ID
0105 18 P0          307      JR WRSTR1       I LOOK AGAIN
0107 23             308      WRSTR2 INC ML     I GET LENGTH
0108 4E             309      LD C,(ML)
0109 23             310      INC ML ...
010A 44             311      LD B,(ML)
010B 23             312      INC ML          I FIRST TEXT CODE
010C 78             313      LD A,B          I TEST IF NULL STRING
010D 81             314      OR C
010E C8             315      RET Z           I RETURN IF 50
316 I
317 I
318 I
319 I
320 I
321 I
322 WRSTRG LO A,(ML) I GET CODE
0110 09             323      PUSH ML         I SAVE ADDRESS
0111 C9             324      PUSH BC         I AND COUNT
0112 CD 0040        325      CALL WRCMAR     I WRITE "A"
0113 79             326      LO A,C          I TEST IF GOOD
0114 80             327      OR 0
0117 20 09          328      JR NZ,WRSTX1   I EXIT IF INVALID CODE OR
329 I
330 WRSTR3 POP BC     I IP SCREEN FULL
0119 C1             331      POP ML         I COUNT
011A 01             332      POP ML         I ADDRESS
011B 23             333      INC ML         I NEXT CHAR.
011C 08             334      DEC BC         I ADJUST COUNT
011D 78             335      LD A,B          I TEST IF DONE
011E 81             336      OR C
011F 20 EE          337      JR NZ,WRSTRG   I WRITE NEXT CHAR.
0121 C9             338      RET            I RETURN BC=0
339 I
340 WRSTX1 LO A,C      I SAVE ERROR
0122 79             341      CP 1            I TEST IF UNRECOGNIZED CODE
0123 FE 01          342      JR Z,WRSTR3     I CONTINUE
0124 28 P2          343      POP ML         I REMAINING COUNT TO ML
0127 E1             344      LD (STRGCT),ML I STORE REMAINING COUNT
0128 22 0038        345      POP ML         I ADDRESS TO ML
012C 0E 03          346      LO C,3          I RETURN BC=3
012E 06 00          347      WRSTR2 LO B,0
0130 C9             348      RET
349 I
350 NOSTRG LO C,2     I RETURN BC=2 IF STRING NOT PCUND
0131 0E 02          351      JR WRSTX2
0133 18 P9          352
353 I
354 SUBTYL POSITION CONTROL
355 I
356 SETCOD: I
0135 ED 48 0007     357      LD BC,(LINCCL) I HERE FROM BASIC ENTRY. PARAMETERS
358 I
359 I
360 I
361 I
362 I
363 I
364 I
365 I
366 I
367 I
368 I
369 I
370 I
371 I
372 I
373 I
374 I
375 I
376 I
377 I
378 I
379 I
380 I
381 I
382 I
383 I
384 I
385 I
386 I
387 I
388 I
389 I
390 I
391 I
392 I
393 I
394 I
395 I
396 I
397 I
398 I
399 I
400 I
401 I
402 I
403 I
404 I
405 I
406 I
407 I
408 I
409 I
410 I
411 I
412 I
413 I
414 I
415 I
416 I
417 I
418 I
419 I
420 I
421 I
422 I
423 I
424 I
425 I
426 I
427 I
428 I
429 I
430 I
431 I
432 I
433 I
434 I
435 I
436 I
437 I
438 I
439 I
440 I
441 I
442 I
443 I
444 I
445 I
446 I
447 I
448 I
449 I
450 I
451 I
452 I
453 I
454 I
455 I
456 I
457 I
458 I
459 I
460 I
461 I
462 I
463 I
464 I
465 I
466 I
467 I
468 I
469 I
470 I
471 I
472 I
473 I
474 I
475 I
476 I
477 I
478 I
479 I
480 I
481 I
482 I
483 I
484 I
485 I
486 I
487 I
488 I
489 I
490 I
491 I
492 I
493 I
494 I
495 I
496 I
497 I
498 I
499 I
500 I
501 I
502 I
503 I
504 I
505 I
506 I
507 I
508 I
509 I
510 I
511 I
512 I
513 I
514 I
515 I
516 I
517 I
518 I
519 I
520 I
521 I
522 I
523 I
524 I
525 I
526 I
527 I
528 I
529 I
530 I
531 I
532 I
533 I
534 I
535 I
536 I
537 I
538 I
539 I
540 I
541 I
542 I
543 I
544 I
545 I
546 I
547 I
548 I
549 I
550 I
551 I
552 I
553 I
554 I
555 I
556 I
557 I
558 I
559 I
560 I
561 I
562 I
563 I
564 I
565 I
566 I
567 I
568 I
569 I
570 I
571 I
572 I
573 I
574 I
575 I
576 I
577 I
578 I
579 I
580 I
581 I
582 I
583 I
584 I
585 I
586 I
587 I
588 I
589 I
590 I
591 I
592 I
593 I
594 I
595 I
596 I
597 I
598 I
599 I
600 I
601 I
602 I
603 I
604 I
605 I
606 I
607 I
608 I
609 I
610 I
611 I
612 I
613 I
614 I
615 I
616 I
617 I
618 I
619 I
620 I
621 I
622 I
623 I
624 I
625 I
626 I
627 I
628 I
629 I
630 I
631 I
632 I
633 I
634 I
635 I
636 I
637 I
638 I
639 I
640 I
641 I
642 I
643 I
644 I
645 I
646 I
647 I
648 I
649 I
650 I
651 I
652 I
653 I
654 I
655 I
656 I
657 I
658 I
659 I
660 I
661 I
662 I
663 I
664 I
665 I
666 I
667 I
668 I
669 I
670 I
671 I
672 I
673 I
674 I
675 I
676 I
677 I
678 I
679 I
680 I
681 I
682 I
683 I
684 I
685 I
686 I
687 I
688 I
689 I
690 I
691 I
692 I
693 I
694 I
695 I
696 I
697 I
698 I
699 I
700 I
701 I
702 I
703 I
704 I
705 I
706 I
707 I
708 I
709 I
710 I
711 I
712 I
713 I
714 I
715 I
716 I
717 I
718 I
719 I
720 I
721 I
722 I
723 I
724 I
725 I
726 I
727 I
728 I
729 I
730 I
731 I
732 I
733 I
734 I
735 I
736 I
737 I
738 I
739 I
740 I
741 I
742 I
743 I
744 I
745 I
746 I
747 I
748 I
749 I
750 I
751 I
752 I
753 I
754 I
755 I
756 I
757 I
758 I
759 I
760 I
761 I
762 I
763 I
764 I
765 I
766 I
767 I
768 I
769 I
770 I
771 I
772 I
773 I
774 I
775 I
776 I
777 I
778 I
779 I
780 I
781 I
782 I
783 I
784 I
785 I
786 I
787 I
788 I
789 I
790 I
791 I
792 I
793 I
794 I
795 I
796 I
797 I
798 I
799 I
800 I
801 I
802 I
803 I
804 I
805 I
806 I
807 I
808 I
809 I
810 I
811 I
812 I
813 I
814 I
815 I
816 I
817 I
818 I
819 I
820 I
821 I
822 I
823 I
824 I
825 I
826 I
827 I
828 I
829 I
830 I
831 I
832 I
833 I
834 I
835 I
836 I
837 I
838 I
839 I
840 I
841 I
842 I
843 I
844 I
845 I
846 I
847 I
848 I
849 I
850 I
851 I
852 I
853 I
854 I
855 I
856 I
857 I
858 I
859 I
860 I
861 I
862 I
863 I
864 I
865 I
866 I
867 I
868 I
869 I
870 I
871 I
872 I
873 I
874 I
875 I
876 I
877 I
878 I
879 I
880 I
881 I
882 I
883 I
884 I
885 I
886 I
887 I
888 I
889 I
890 I
891 I
892 I
893 I
894 I
895 I
896 I
897 I
898 I
899 I
900 I
901 I
902 I
903 I
904 I
905 I
906 I
907 I
908 I
909 I
910 I
911 I
912 I
913 I
914 I
915 I
916 I
917 I
918 I
919 I
920 I
921 I
922 I
923 I
924 I
925 I
926 I
927 I
928 I
929 I
930 I
931 I
932 I
933 I
934 I
935 I
936 I
937 I
938 I
939 I
940 I
941 I
942 I
943 I
944 I
945 I
946 I
947 I
948 I
949 I
950 I
951 I
952 I
953 I
954 I
955 I
956 I
957 I
958 I
959 I
960 I
961 I
962 I
963 I
964 I
965 I
966 I
967 I
968 I
969 I
970 I
971 I
972 I
973 I
974 I
975 I
976 I
977 I
978 I
979 I
980 I
981 I
982 I
983 I
984 I
985 I
986 I
987 I
988 I
989 I
990 I
991 I
992 I
993 I
994 I
995 I
996 I
997 I
998 I
999 I
1000 I

```

```

0177* C3          411          PUSH BC
0180* 47          412          LOOP LD B,A          I B=LINES THIS BLOCK
0181* 08 00       413          LD C,B          I C=SCAN COUNT
0182* 70          414          LDCPO LD B,B          I
0183* C0          415          PL3M BC          I SAVE SCAN COUNT
0184* 0F          416          BRCA
0185* 0F          417          BRCA
0186* 0F          418          BRCA          I CALCULATE NO. OF BYTES
0187* 0F          419          LD C,A          I
0188* 4F          420          LD B,B          I BC=3200 OP LINES
0189* 76 00       421          LOOP1 EX DE,HL          I
0190* 08          422          LD HL,-20H          I GET ADRS. OP PREV. LINE
0191* 19          423          ADD HL,DE          I
0192* 08          424          EX DE,HL          I TO DE
0193* 08 00       425          PUSH BC          I SAVE COUNT
0194* 0F          426          PUSH HL          I SAVE ADDRESS
0195* 0F          427          LDIR          I DO MOVE
0196* 0F          428          POP HL          I
0197* 7C          429          POP BC          I
0198* C0 0F       430          LD A,M          I
0199* 20 05       431          BIT S,A          I TEST IF OP1
0200* 76 20       432          JR NZ,NXTSC          I NO
0201* 07          433          OR ZM          I
0202* 08 0F       434          LD M,A          I DO OP2
0203* 08 0F       435          JR LOOP1          I
0204* 08 0F       436          AND ODFM          I BACK TO OP1
0205* 08 0F       437          LD M,A          I
0206* 08 0F       438          INC M          I NEXT SCAN LINE
0207* 08 0F       439          POP BC          I SCAN COUNT
0208* 08 0F       440          DEC C          I
0209* 08 0F       441          JR NZ,LOOP8          I MORE SCANS
0210* 08 0F       442          POP BC          I RESTOTAL LINE COUNT
0211* 08 0F       443          LD A,B          I
0212* 08 0F       444          AND A          I
0213* 08 0F       445          JR I,SCALTY          I DONE
0214* 08 0F       446          LD L,B          I START OP NEXT BLOCK
0215* 08 0F       447          JR TESTAD          I DO REMAINING LINES)
0216* 08 0F       448          POP BC          I ORIG-BC
0217* 08 0F       449          LD A,C          I STARTING LINE
0218* 08 0F       450          ADD A,B          I ADD NO. OF LINES MOVED
0219* 08 0F       451          DEC A          I LAST LINE SCROLLED
0220* 08 0F       452          LD C,A          I
0221* 08 0F       453          LD B,L          I CLEAR 1 LINE
0222* 08 0F       454          JR CLRSC0          I CLEAR VACATED LINE AND RETURN
0223* 08 0F       455          I
0224* 08 0F       456          BRBLK LD C,A          I
0225* 08 0F       457          LD A,B          I
0226* 08 0F       458          SUB C          I ADJUST TOTAL LINE COUNT BY NO.
0227* 08 0F       459          LD B,A          I OP LINES THIS BLOCK
0228* 08 0F       460          PUSH BC          I
0229* 08 0F       461          LD A,C          I LOAD A NO. OF LINES THIS BLOCK
0230* 08 0F       462          JR LOOP          I
0231* 08 0F       463          I
0232* 08 0F       464          STBLK DEC B          I ADJUST TOTAL LINE COUNT-1
0233* 08 0F       465          PUSH BC          I
0234* 08 0F       466          LD C,B          I SCAN COUNT
0235* 08 0F       467          STBLK0 PUSH BC          I SAVE SCAN COUNT
0236* 08 0F       468          STBLK1 EX DE,HL          I
0237* 08 0F       469          LD HL,-720H          I
0238* 08 0F       470          ADD HL,DE          I
0239* 08 0F       471          EX DE,HL          I ADRS. OF PREV. LINE
0240* 08 0F       472          LD BC,32          I BYTE COUNT
0241* 08 0F       473          PUSH HL          I
0242* 08 0F       474          LDIR          I DO MOVE
0243* 08 0F       475          POP HL          I
0244* 08 0F       476          LD A,M          I
0245* 08 0F       477          BIT S,A          I
0246* 08 0F       478          JR NZ,STBLK2          I
0247* 08 0F       479          OR ZM          I DO OP2
0248* 08 0F       480          LD M,A          I
0249* 08 0F       481          JR STBLK1          I
0250* 08 0F       482          STBLK2 AND ODFM          I BACK TO OP1
0251* 08 0F       483          LD M,A          I
0252* 08 0F       484          INC M          I TO NEXT SCAN LINE
0253* 08 0F       485          POP BC          I
0254* 08 0F       486          DEC C          I TEST IF MORE SCANS
0255* 08 0F       487          JR NZ,STBLK0          I REMAINING LINE COUNT
0256* 08 0F       488          POP BC          I
0257* 08 0F       489          LD A,B          I
0258* 08 0F       490          AND A          I
0259* 08 0F       491          JR I,SCALTY          I ADJUST TO LINE 1
0260* 08 0F       492          LD DE,-720H          I
0261* 08 0F       493          ADD HL,DE          I
0262* 08 0F       494          JP TESTAD          I BC=LINE COUNT
0263* 08 0F       495          I HL=OP ADDRESS
0264* 08 0F       496          I
0265* 08 0F       497          SUBTL CLEAR SCREEN
0266* 08 0F       498          I
0267* 08 0F       499          I
0268* 08 0F       500          I
0269* 08 0F       501          CLRSD0: LD BC,(CLRCTL)          I HERE PRGM BASIC ENTRY TO CLEAR SCREEN
0270* 08 0F       502          I          I GET CONTRL INPD.
0271* 08 0F       503          I
0272* 08 0F       504          CLRSCN: I ENTRY WITH BC=LINE COUNT AND
0273* 08 0F       505          I          I STARTING LINE NO. FOR CLEAR
0274* 08 0F       506          LD A,B          I
0275* 08 0F       507          AND A          I TEST VALIDITY
0276* 08 0F       508          JP I,INVPAR          I ERROR IF COUNT=0
0277* 08 0F       509          ADD A,C          I
0278* 08 0F       510          CP 1          I
0279* 08 0F       511          JP C,INVPAR          I ERROR IF B<C1
0280* 08 0F       512          CP 25          I
0281* 08 0F       513          JP NC,INVPAR          I ERROR IF B<C24
0282* 08 0F       514          CALL CLRSC0          I DO SERVICE
0283* 08 0F       515          JP GOODREY          I RETURN BC=0
0284* 08 0F       516          I
0285* 08 0F       517          I
0286* 08 0F       518          CLRSC0: PUSH BC          I
0287* 08 0F       519          LD A,SCR52          I CONVERT TO INTERNAL FORMAT

```

```

0210* 91          520      SUB      C
0211* 47          521      LD      B,A
0212* 3A 0033*   522      LD      A,(LIMLEN)
0213* 3C          523      INC     A           ; SET TO COL-0
0214* 4F          524      LD      C,A
0217* CC 0474*   525      CALL   LMBU           ; GET ADDRESS
021A* 50          526      LD      D,B
021B* 59          527      LD      E,C           ; SAVE POSITION
021C* C1          528      POP     BC           ; ORIG. BC
021D* D5          529      PUSH   DE
021E* 70          530      CLRSC1 LD      A,L           ; GET # OF LINES THIS BLOCK
021F* 07          531      RLCA
0220* 07          532      RLCA
0221* 07          533      RLCA
0222* 4F          534      LD      C,A
0223* 3E 00      535      LD      A,B
0224* 91          536      SUB     C
0225* 80          537      CP      0           ; COMPARE TO TOTAL LINE COUNT
0227* 30 3C      538      JR      C,CLRSC7
0229* 70          539      CLRSC2 LD      A,B           ; NO. OF LINES
022A* 06 00      540      LD      B,B           ; REMAINING LINES
022C* C5          541      PUSH   BC
022D* 47          542      CLRSC3 LD      B,A
022E* 0E 00      543      LD      C,B           ; SCAN COUNT
0230* 70          544      CLRSC4 LD      A,B
0231* C5          545      PUSH   BC
0232* E6 07      546      AND     7
0234* 0F          547      ORCA
0235* 0F          548      ORCA
0236* 0F          549      ORCA
0237* 4F          550      LD      C,A
0238* 06 00      551      LD      B,B           ; BC=32AND. OF LINES
023A* 00          552      DEC     C           ; (=0 IF 256 FOR 8 LINES)
023B* C5          553      CLRSC5 PUSH   BC           ; ADJUST
023C* E9          554      PUSH   HL
023D* 94          555      LD      D,M
023E* 5D          556      LD      E,L
023F* 36 00      557      LD      (HL),00      ; CLEAR DP
0241* 13          558      INC     DE
0242* ED 00      559      LDIA
0244* E1          560      POP     HL
0245* C1          561      POP     BC
0246* 7C          562      LD      A,M
0247* C8 6F      563      BIT     $,A
0249* 2C 05      564      JR      NZ,CLRSC6
024B* F4 2C      565      OR      20H
024D* 67          566      LD      M,A           ; DO DP2
024E* 18 E8      567      JR      CLRSC5
0250* E4 0F      568      CLRSC6 AND     00FH           ; RESTORE GP1
0252* 67          569      LD      M,A
0253* 24          570      INC     M
0254* C1          571      POP     BC           ; NEXT SCAN ROW
0255* 0C          572      DEC     C           ; POP SCAN COUNT
0256* 20 00      573      JR      NZ,CLRSC4
0258* C1          574      POP     BC           ; TOTAL LINE COUNT
0259* 70          575      LD      A,B
025A* A7          576      AND     A
025B* 20 05      577      JR      Z,CLRSC7
025D* 2E 00      578      LD      L,0
025F* C3 0218*   579      JP      CLRSC1           ; HL=START OF NEXT BLOCK
0262* C1          580      CLRSC7 POP     BC           ; B=REMAINING LINE COUNT
0263* C3 0456*   581      JP      UPDPOSH       ; POSITION
0264* 4F          582      CLRSC7 LD      C,A           ; RETURN VIA UPDATE AND STORE POSITION
0267* 70          583      LD      A,B           ; SAVE NO. LINES THIS BLOCK
0268* 91          584      SUB     C           ; ADJUST TOTAL LINE COUNT
0269* 47          585      LD      B,A
026A* C5          586      PUSH   BC           ; REMAINING LINE COUNT
026B* 70          587      LD      A,C           ; LINES THIS BLOCK
026C* 10 8F      588      JR      CLRSC3
026E* 3A 0011*   594      SETAB01 LD      A,(ATTCTL)      ; HERE FROM BASIC ENTRY TO SET ATTRIBUTES
0271* E6 07      597      SETATT: AND     7           ; GET INK SELECTION
0273* 4F          599      LD      C,A           ; SAVE
0274* 3A SCC2*   600      LD      A,(VIDMOD)    ; TEST IF DP2 OPEN
0277* A7          601      AND     A           ; INVALID IF DP2 NOT OPEN
0278* CA 00C2*   602      JP      Z,INVPAR
027B* 79          603      LD      A,C
027C* 17          604      RLA
027E* 17          605      RLA
027F* 95          606      RLA
0280* F4 06      607      PUSH   AF           ; SHIFT TO M/W POSITION
0282* 47          608      OR      6           ; SAVE ROTATED VALUE
0283* D8 FF      609      LD      B,A           ; SET M/W MODE
0285* E6 C0      610      IN      A,(HREXPT)   ; VALUE FOR M/W
0287* 90          611      AND     000H        ; PRESERVE BITS 7 AND 6
0288* D3 FF      612      OR      B           ; SET VIDEO MODE VALUE
028A* F4 40      613      OUT   (HREXPT),A    ; SET M/W
028C* 32 SCC2*   614      OR      40H         ; UPDATE VIDMOD
028E* F1          615      LD      (VIDMOD),A
0290* 2F          616      POP     AF           ; INK SELECTION IN BITS 3-5
0291* 51          617      CPL
0292* 32 0039*   619      CR      C           ; GET COMPLEMENTARY COLOR
0295* C3 0080*   620      LD      (ATTBYT),A   ; COMBINE WITH INK
0296* 3A 0015*   625      SETM001 LD      A,(MSKCTL)    ; HERE FROM BASIC ENTRY TO SET MASK
0298* 32 0039*   627      SETMSK: LD      (MASKB),A   ; GET CTRL INFC.
0299* C3 0C80*   629      JP      GOODRET      ; ENTRY WITH MASK VALUE IN A
029E* 3A 0015*   625      SETM001 LD      A,(MSKCTL)    ; STORE MASK FOR APPLICATION TO FUTURE
0298* 32 0039*   627      SETMSK: LD      (MASKB),A   ; DISPLAYS
0299* C3 0C80*   629      JP      GOODRET

```

```

632          SUBTTL "GET" ROUTINES
633          I
634          I
02A1"          635          GTCH001          I HERE FROM BASIC ENTRY TO GET CHARACTER
636          I (RETURNS CODE FOR CHARACTER AT
637          I POSITION SPECIFIED IN GETCTL)
638          I GET CONTROL INFO.
02A1" EC 40 0019"          639          LD BC,(GETCTL)
640          I
02A5"          640          GTCHAR1          I ENTRY WITH POSITION IN BC
02A5" CD 0A3A"          641          CALL TSTPAR          I TEST PARAMETERS
02A6" CD 0A43"          642          CALL CONVFM          I CONVERT TO INTERNAL FORMAT
02A6" CD 0A55"          643          CALL CALCPDS          I GET DISPLAY FILE ADRS.
02A6" ED 50 002P"          644          LD DE,(CHTBL)          I CHAR.TABLE
02B2" 06 40          645          LD B,90          I NO. OF PRINTABLE CHARACTERS
02B4" 3E 80          646          LD A,80H          I SET ADJUSTMENT INDEX
02B6" 32 003A"          647          GTCH1          LD (GTINDX),A
02B9" 14          648          INC D          I ADJUST TO START OF TABLE
02B8" E0          649          EX DE,HL          I DP ADRS. IN DEICHR.SET IN HL
02B8" C5          650          GTCH2          PUSH BC          I SAVE COUNT
02B8" D5          651          PUSH DE          I SAVE ADRS. IN DP
02B8" E5          652          PUSH HL          I SAVE ADRS. IN CHAR.TABLE
02B8" 1A          653          LD A,(DE)          I SCAN ROW PRGM DP
02B8" AE          654          XOR (HL)          I TEST AGAINST CHAR. SET
02C0" 20 10          655          JR Z,GTCM3          I MATCH
02C2" P5          656          PUSH AP
02C3" 3A 003A"          657          LD A,(GTINDX)
02C6" FE 90          658          CP 90H          I TEST IF STD.GRAPHICS
02C6" 20 03          659          JR NZ,GTCM23
02CA" P1          660          POP AP
02C6" 10 27          661          GTCH23          JR GTCM4          I DO NOT TEST FOR INVERSE
02C0" P1          662          POP AP
02C6" 3C          663          GTCH23          INC A          I TEST INVERSE
02C6" 20 23          664          JR NZ,GTCM4
02D1" 3D          665          DEC A          I A=-1 FOR INVERSE
02D2" 4F          666          GTCH3          LD C,A          I C=0 FOR MATCH -1 FOR INVERSE
02D3" 06 07          667          LD S,7
02D5" 14          668          GTCH31          INC D          I NEXT SCAN IN DP
02D6" 23          669          INC HL          I NEXT BYTE IN CHAR.SET
02D7" 1A          670          LD A,(DE)
02D8" AE          671          XOR (HL)
02D9" A9          672          XOR C
02DA" 20 10          673          JR NZ,GTCM4          I NO MATCH
02DC" 10 P7          674          OJNZ GTCM31          I FOR NEXT SCAN
675          I
676          I
677          I
678          I
679          LD A,C          I
680          POP BC          I
681          POP BC          I
682          POP BC          I
683          LD C,A          I B=COUNT C=-1 IF MATCH ON INVERSE
02E3" 3A 003A"          684          LD A,(GTINDX)          I ADJUST CHAR.CODE
02E6" 90          685          SUB B          I A=CHAR.CODE
02E7" FE 20          686          CP 20H          I TEST IF SPACE
02E9" 20 05          687          JR NZ,GTCM32
02E8" 2C          688          INC C          I TEST IF MATCH ON INVERSE SPACE
02E8" 20 02          689          JR NZ,GTCM32
02E8" 3E 8F          690          LD A,8FH          I SET CODE FOR GRAPHICS BLOCK
02F0" 4F          691          GTCH32          LD C,A
02F1" 06 00          692          LD B,0
02F3" C9          693          RET          I CHAR.CODE IN A AND IN BC
694          I
02F4" 11          695          GTCH4          POP HL          I HERE WHEN NO MATCH
02F5" B1 0000          696          LD DE,0          I LOCK AT NEXT CHAR. IN SET
02F6" 19          697          ADD HL,DE          I CHAR. SET
02F6" D3          698          POP DE          I CHAR. IN DP
02F6" C1          699          POP BC          I CHAR.COUNT
02F6" 10 8E          700          OJNZ GTCM2          I LOCK AGAIN
701          I
702          I
703          EX DE,HL          I
704          LD A,(GTINDX)          I
705          CP 80H          I TEST IF STD.CHAR.SET
706          JR NZ,GTCM5          I TRY GRAPHICS
707          LD DE,(CRTBL)          I STD.GRAPHICS TABLE
708          LD B,16          I NO. OF ENTRIES
709          LD A,90H          I INDEX
710          JR GTCM1          I
711          CP 90H          I TEST IF STD.GRAPHICS
712          JR NZ,GTCM6          I DONE IF NOT
713          LD DE,(UDG)          I TRY USER-DEFINED GRAPHICS AREA
714          DEC D          I ADJUST ADDRESS-100H
715          LD B,21          I NO OF ENTRIES
716          LD A,0A5H          I INDEX
717          JR GTCM1          I
718          LD C,0          I
719          XOR A          I HERE WHEN NO MATCH ANYWHERE
720          RET          I SET BC AND A=ZERO
721          I
722          I
723          I GET ATTRIBUTE BYTE
724          I NOTE THAT IN 64-COL.MODE ALL SCREEN POSITIONS
725          I HAVE THE SAME ATTRIBUTES, THEREFORE IT IS
726          I NOT NECESSARY TO USE THE "GETCTL" POSITION
727          I PARAMETERS
0321"          728          GETARD:          I HERE FROM BASIC ENTRY TO GET ATTRIBUTE
729          I
0321" 3A 0039"          730          GETATT: LD A,(ATTBYT)          I GET ATTRIBUTE BYTE
0324" 4F          731          LD C,A          I PUT IN BC
0325" 06 00          732          LD B,0
0327" C9          733          RET
734          I
0328"          734          I GET CURSOR POSITION
735          GETC00:          I HERE FROM BASIC ENTRY

```

```

0320* 1C 48 0034*
0321* CD 0448*
0322* 3A 0033*
0323* 89
0324* 20 08
0325* 08 00
0326* 78 00
0327* 3C
0328* 47
0329* 78 18
0330* 38 02
0331* 06 17
0332* 83 43 0007*
0333* C9
736 GETCUR:
737 LD BC,(CURPOS) ; GET INTERNAL POSITION
738 CALL CONVPM ; CONVERT TO USER FORMAT
739 LD A,(LINLEN)
740 CP C ; TEST IF END OF LINE(=64)
741 JR NZ,GETC1 ; NO
742 LD C,8 ; NEXT POSN. START OF NEXT LINE
743 LD A,B
744 INC A ; BUMP TO NEXT LINE
745 LD B,A
746 CP 24 ; TEST IF OFF SCREEN
747 JR C,GETC1 ; NO
748 LD B,23 ; POSN. IS AT BOTTOM LINE
749 GETC1 LD (LINCCL),BC ; STORE FOR BASIC PROGRAM
750 RET ; VALUES ALSO IN BC
751
752
753 SUBTTL VIDEO MODE CONTROL
754
755
756 STMOB0: ; HERE FROM BASIC ENTRY WITH MODE IN
757 ; "VIMODE"
758 LD A,(VIMODE)
759
760 SETMOCE: ; ENTRY WITH MODE IN A
761
762 AND A
763 JR Z,SETM01 ; TEST FOR VALIDITY
764 CP 6
765 JP NZ,INVPAR ; INVALID PARAMETERS
766 LD HL,SCINIT ; INITIALIZE VARIABLES
767 LD (SCRCTL),HL ; SCROLL CONTROL
768 LD A,23
769 LD (BOTLN),A ; BOTTOM LINE
770 LD A,1
771 LD (SCRCL7),A ; SCROLL COUNT
772 LD A,64
773 LD (LINLEN),A ; LINE LENGTH
774 LD HL,CLINIT ;
775 LD (CLRCTL),HL ; CLEAR SCREEN CONTROL
776 XOR A
777 LD (MASKB),A ; MASK BYTE
778 LD (STRGCT),A ; WSTRG REMAINING COUNT
779 LD (STRGCT+1),A ;
780 LD HL,CHRSET ; STD.CHAR.SET
781 LD (CNTSL),HL ;
782 LD HL,GRPHST ; STD. GRAPHICS SET
783 LD (GRTB),HL ;
784
785
786 LD (DATAB),A ; INITIALIZE BASIC INPUTS
787 LD (LINCCL),A ; DATA BYTE
788 LD (LINCCL+1),A ; COLUMN
789 LD (MSRCTL),A ; MASK CONTROL
790 LD (GETCTL),A ; "GET" COLUMN
791 LD (GETCTL+1),A ; "GET" LINE
792 LD A,6 ; SET M/M TO 64-CCL.MODE
793
794 SETM01 LD B,A ; SAVE MODE
795 LD A,(VIMOD)
796 AND A ; TEST CURRENT MODE
797 JR NZ,SETM02
798 DR B ; CURRENT MODE=0 TEST IF NEW MODE=0
799 JP Z,GODDRET ; RETURN BC=0
800 LD HL,CHSERISZ
801 LD DE,MOVESZ
802 ADD HL,DE ; HL=TOTAL ROOM NEEDED
803 LD CE,(STKEND)
804 ADD HL,DE ; ADD MEMORY IN USE
805 JP C,EXTNRH ; NOT ENOUGH MEMORY TO OPEN DPZ
806 LD DE,(CRANTOP)
807 AND A
808 SBC HL,DE
809 JP NC,EXTNRH ; NOT ENOUGH MEMORY
810 SETM02 CALL ENBLEXT ; ENABLE RDM EXT.
811 LD A,B ; MODE TO A
812 CALL GETVAL ; M/M VALUE TO B; VIMOD VALUE TO C
813 PUSH BC ; SAVE
814 LD A,B
815 CALL CHMGVID ; CALL RTN. TO OPEN/CLOSE 2ND DP
816 CALL ENBLNOME ; REENABLE HOME BANK
817 POP BC
818 LD A,C
819 LD (VIMOD),A ; UPDATE VIMOD
820 UPDATE BIT 6,A ; UPDATE SYSTEM VARS. BASED ON MODE
821 JP Z,GODDRET ; DONE IF MODE 0
822 SXT4 LD A,CC1H ; 64/80-COL. MODE
823 OR 0 ; STORE ATTRIBUTE BYTE BASED
824 CPL ; ON INK SELECTION
825 SRL B
826 SRL B
827 SRL B
828 OR B
829 LD (ATTBYT),A ; ATTRIBUTE BYTE
830 LD (ATTCTL),A ; BASIC PARAMETER
831 UPD01 LD A,(LINLEN) ; GET LINE LENGTH
832 INC A
833 LD C,A ; COLUMN
834 LD B,SCRISZ ; BC=HOME POSITION (LN.0/CCL.0)
835 LD HL,0
836 LD (COORDS),HL ; INITIALIZE PLOT POSITION
837 CALL UPDPOSN ; UPDATE AND STORE HOME POSITION
838 JP GODDRET ; RETURN BC=0
839 EXTNRH LD C,2 ; RETURN BC=2 FOR NO ROOM
840 JP ERRRET
841

```



```

947
948
949
0458  CD 0470
0459  3A 0033
0461  3C
0462  91
0463  C8 47
0465  28 0A
0467  F9
0468  3E 20
046A  84
046B  67
046C  F1
046D  A7
046E  1F
046F  5F
0470  16 20
0472  19
0473  C9

0474

0474  3E 18
0476  90
0477  57
0478  0F
0479  0F
047A  0F
047B  E4 20
047D  4F
047E  7A
047F  E4 18
0481  F4 40
0483  67
0484  C9

0485  EC 43 0034
0487  22 0036
048C  C9

048D

048D  EC 48 0034
0491  2A 0035
0494  C9

947
948
949
950  CALCPDS CALL LNBUI
951      LD A,(LINLEN)
952      INC A
953      SUB C
954      BIT 0,A
955      JR Z,CALCP1
956      PUSH AF
957      LD A,ZOH
958      OR M
959      LD M,A
960      PGP AF
961  CALCP1 AND A
962      RRA
963      LD E,A
964      LD D,0
965      ADD HL,DE
966      RET
967
968
969  LNBUI
970
971      LD A,SCR52
972      SUB B
973      LD D,A
974      RRCA
975      RRCA
976      RRCA
977      AND 0E0H
978      LD L,A
979      LD A,D
980      AND 1BH
981      OR 40H
982      LD M,A
983      RET
984
985  STPOSH:
986      LD (CURPOS),BC
987      LD (CPADRS),HL
988      RET
989
990  LDPOSH:
991      LD BC,(CL4POS)
992      LD HL,(CPADRS)
993      RET
994
995
996
997
998  SUBYTEL GRAPHICS CHAR.SET
1000
1001
1002
1003
1004  GRPST  00000000b
1005      00000000b
1006      00000000b
1007      00000000b
1008      00000000b
1009      00000000b
1010      00000000b
1011      00000000b
1012
1013      00001111b
1014      00001111b
1015      00001111b
1016      00001111b
1017      00000000b
1018      00000000b
1019      00000000b
1020      00000000b
1021
1022      11110000b
1023      11110000b
1024      11110000b
1025      11110000b
1026      11110000b
1027      00000000b
1028      00000000b
1029      00000000b
1030      00000000b
1031
1032      11111111b
1033      11111111b
1034      11111111b
1035      11111111b
1036      00000000b
1037      00000000b
1038      00000000b
1039      00000000b
1040
1041
1042      00000000b
1043      00000000b
1044      00000000b
1045      00000000b
1046      00001111b
1047      00001111b
1048      00001111b
1049      00001111b
1050

```

```

I RETURNS DP ADRS. IN HL. PRESERVES
I LINE/COLUMN POSITION IN BC
I GET DISPLAY FILE ADRS.
I TEST WHICH DP
I IN DP2
I COL/2 IS POSITION IN DISPLAY FILE
I GET DISPLAY FILE ADDRESS
I FOR START OF LINE IN B
I STORE CURSOR POSITION
I LOAD CURSOR POSITION
I PIXEL PATTERNS FOR STD.GRAPHICS SET
I 0=BACKGROUND(PAPER) 1=FOREROUND(INK)
I code 00 graphics space
I code 01 graphics
I code 02 graphics
I code 03 graphics
I code 04 graphics

```

INSDBL	409*						
INSERT	70*	400					
INVPAR	180	152	263*	379	302	384	900
		511	513	602	765	916	
LDPQSN	175	990*					
LINCOL	97*	357	749*	767*	788*		
LINLEN	194*	207	392	522	739	773*	831
		917	929	951			
LNBU	395	525	550	769*			
LOGP	412*	462					
LOGPO	414*	441					
LOGPI	421*	435					
MASKB	198*	221	628*	777*			
MOVESI	71*	72	801				
MSKCTL	113*	625	789*				
MOSTRG	305	349*					
NXTSC	432	434*					
PARAMS	68*	293					
PARERR	919*	920					
PRAMT	64*						
RAMTOP	65*	806					
RECLEN	56*	304					
SCINIT	50*	766					
SCRCTL	128*	293	372	767*			
SCRLO	285	385	388*				
SCRLO	45	131*					
SCRLOO	131	370*					
SCRCLT	143*	273	277*	282*	771*		
SCRCLT	445	448*	491				
SCRCLL	43	374*					
SCRSL	49*	266	389	519	834	933	971
SETO	965	884*					
SETABO	114	594*					
SETATB	45	114*					
SETATT	43	597*					
SETCBO	100	355*					
SETCUB	45	100*					
SETCUR	43	359*					
SETMBO	118	624*					
SETMD1	763	794*					
SETMD2	797	810*					
SETMDB	47	127*					
SETMDD	47	760*					
SETMSB	45	118*					
SETMSK	43	627*					
STBLK	400	464*					
STBLK0	467*	487					
STBLK1	468*	481					
STBLK2	478	482*					
STKEND	62	803					
STMDBO	127	756*					
STPQSN	258	943	787*				
STRGCT	164*	343*	778*	779*			
SXTA	822*						
TBSTAB	398*	447	494				
YSTO1	863	879*					
YSTPAR	368	641	912*				
YSTPR1	914	917*					
YVPUL1	275	282*					
YVPUL0	216	266*					
UDG	63*	188	713				
UPDATE	820*						
UPDP03	271	362	581	837	948*		
UPDT1	831*						
VARS	61*	297					
VIMDBO	67*	689	615*	795	819*		
VIMDBE	124*	758					
WRCH0	94	170*					
WRCH11	187	192*					
WRCH12	189	195*					
WRCH13	191	196*	196*				
WRCH14	200	213*					
WRCH15	212	214*					
WRCH2	219*						
WRCH3	224	226*					
WRCH9	233*	243					
WRCH7	244*						
WRCH48	43	174*	325				
WRCH80	45	94*					
WRCHXT	253	258*					
WRSTR0	98	293*					
WRSTR1	288*	307					
WRSTR2	302	308*					
WRSTR3	310*	341					
WRSTR8	48	95*					
WRSTR6	48	322*	336				
WRSTR1	328	339*					
WRSTR2	346*	358					

APPENDIX C-2

80 COLUMN MODE

Date: 12/16/83

ASC Number: 002

Version: 001

Author: C. Corcoran/D. Boyle

Name: 80 Column Mode Support

Description:

This component provides support to the application programmer for using the 80-column mode feature of the TS 2068. 80-Column Mode is implemented by using the 64-Column Mode feature of the 2068 and modifying the character width from 8 to 6 pixels. The services include opening/closing the second display file (moving the machine stack, OS RAM routines and BASIC structures), PRINT position control, attribute control, clear screen and scroll screen services and display of characters. For ease of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of POKing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk 7 (E000H/57344).

APPLICATION SERVICES

Name: SETMODE (SETMOD from BASIC - parameter to VIMODE)

Input: MODE (0=normal, 8=80 column mode)

From machine code: Register A
From BASIC: In VIMODE

Description:

Sets specified video mode, opening or closing the second display file where needed. This involves determining if there is enough free RAM to open the second display file and if so, moving the BASIC structures and machine language variables area up and the UDG area down to make space for the machine stack and OS RAM routines at the top of memory. The second display file is cleared to zeroes. The affected system and internal variables are updated or initialized (see Usage Section). When returning to Mode 0 from 80 Column mode, the structures are returned to their normal locations.

Output: BC = 0 Successful
BC = 1 Invalid parameters (not equal to 0 or 8)
BC = 2 Not enough memory

Name: CLRSCN (CLRSCB from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)
Starting Line Number (0-23)

From Machine Code: Line Count In Register B
Starting Line In Register C

From BASIC: Starting Line Number in CLSCTL
Line Count in CLSCTL + 1

Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion
BC = 1 invalid parameters
(Line Number + Line Count < 1 or > 24)

Name: SETCUR (SETCUB from BASIC - parameters to LINCGL)

Input: Line Number (0-23)
Column Number (0-79) or (0-84)

From Machine Code: Line Number In Register B
Column Number In Register C

From BASIC: Column Number In LINCGL
Line Number In LINCGL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

Output: BC = 0 for successful completion
BC = 1 for invalid parameters (Line Number > 23,
Column Number > Line Length-1)

Name: SETATT (SETATS from BASIC - parameter to ATTCTL)

Input: Attribute Byte - bit 7 - FLASH
bit 6 - BRIGHT
bit 5 - P
bit 4 - A
bit 3 - PER
bit 2 - I
bit 1 - W
bit 0 - K

From Machine Code: Register A
From BASIC: In ATTCTL

Description:

The specified INK color (0-7) is used to set the video mode hardware and to update VIDMOD. The complementary PAPER color is fixed by the INK selection. FLASH and BRIGHT are fixed at zero by the hardware. Note that in 80 column mode the entire screen has the same attributes.

Output: BC = 0 Successful

Name: WRCHR (WRCHB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed

20H TD 7FH - Std. TS2068 Character Set
80H TD 8FH - Std. Graphics Set
90H TD A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAB

Description:

Displays character at current cursor position, applying current mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLN (see Usage section) and the scroll count (variable SCRCLY) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRCTL and the new line started at the vacated line.

Note that only the first 6 bits of each byte in the User Defined Graphics area will be transferred to the display file.

Output: BC = 0 for successful completion
BC = 1 invalid character code
BC = 3 for screen full

Name: SETMSK (SETMSB from BASIC - parameters to MSACTL)

Input: Mask Byte - bit 0 - OVER
 bit 2 - INVERSE

From Machine Code: Register A
 From BASIC: MSACTL

Description:

The specified mask is stored for application to all subsequent display character operations. (OVER = 1 implies new character combined with old using an XOR operation; INVERSE = 1 implies character is inverted).

Output: BC = 0 for successful completion

Name: WRSTRG (WRSTRB from BASIC - String Identifier to PARAMS)

Input: Character Code String
 From machine code: Address of string in HL
 Count in BC
 From BASIC: String Variable Identifier in
 System Variable PARAMS - 23747 (5CC3H)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCMR description and Usage Section on Automatic Scrolling). For the Screen Full condition the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-A4H)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, PDKE the code for the string variable identifier into PARAMS prior to invoking WRSTRB, e.g.

```
0005 LET @s="----string-----"
0010 PDKE 23747,CDDE "a"
0015 IFUSR(WRSTRB)<>0 THEN ----
      (continue)
```

Output: BC = 0 Successful
 BC = 2 BASIC - String not found
 BC = 3 Screen Full - Remaining Count in STRGCT
 (HL =Current Address in String)

Name: SCRLL (SCRLB from BASIC - parameters to SCRCTL)

Input: Line Count (1-23)
 Starting Line Number (1-23)
 From Machine Code: Line Count In B
 Starting Line In C
 From BASIC: Starting Line In SCRCTL
 Line Count In SCRCTL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on "automatic" scrolling.

Output: BC = 0 Successful
 BC = 1 Invalid Parameters
 (Line Number + Line Count < 1 or > 24)

Name: GTCMAR (GTCMRB from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

From Machine Code: Line Number in B
Column Number in C

From BASIC: Column Number in GETCTL
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find
BC = 1 invalid parameters
BC = character code (20H-44H)

Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GTCMAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position. Note that in 80 column mode the entire screen has common attributes. The value returned will describe the current selection:

Output: BC = 1 for invalid parameters
BC = attribute byte (as for SETATT)

Name: GETCUR (GETCUB from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCBL, the current print position (where the next character would be displayed).

Output: B = Line number (0-23)
C = Column number (0-79) or (0-84)

BASIC: LINCBL - Column number
LINCBL + 1 - Line number

NOTE: If the last character was printed at Col.79 (84) of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

MSSB:

Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
SCRCTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BDTLN	1	Bottom line - line number (0-23) after which test for scroll will be made.
SCRCTY	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.

CMTBL	2	Character Table (Base Address=100H)
GRTBL	2	Std.Graphics Character Table (Base=100H)
LINLEN	1	Line Length - (80 or 85 when in 80-Col.Mode)
CURPOS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFADDR	2	Current Display File Address
MASKB	1	Mask Byte (bit 0 = EVER) (bit 2 = INVERSE)
ATTBYT	1	Attribute Byte (bits 0 - 2 - INK) (bits 3 - 5 - PAPER) (bit 6 - BRIGHT (Set to zero by M/A in 80-col.mode) (bit 7 - FLASH
GTINDX	1	"Get" Index - Used by GETCHAR
STRGCT	2	String Count - Contains remaining byte count when EC=3 (Screen Full) is returned from the Write String service WRSTRG (WRSTRB).
MARGIN	1	Margin - Margin Adjust (0-2)
DFBIT	1	Display File Bit - Current Bit Position

Initial values set via SETMODE (SETMOB) are as follows:

Variable Name	Value
SCRCTL	1701H
BOTLN	17H
SCRLCT	1H
CMTBL	(Internal to Module)
GRTBL	(Internal to Module)
LINLEN	50H
CURPOS	1851H
DFADDR	4000H
MASKB	0H
ATTBYT	38H
GTINX	80H
STRGCT	0H
MARGIN	1H
DFBIT	7H

The following are the variables used for passing parameters in BASIC. The # indicates those initialized by SETMOB:

Variable Name	Size	Value
* DATAB	1	0H
* LINCGL	2	0H
* CLRCTL	2	1800H
* ATTCTL	1	38H
* MSACTL	1	0H
* GETCTL	2	0H
VIMODE	1	

In addition, VIDMOD, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Home Bank.

Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the remapping of certain structures when the second display file is open. NOTE: Machine code above RAMTOP is not moved.

The routine SETMODE (SETMOB) cannot be executed from a cartridge because of the necessity to enable the RDM Extension which disables the DQCK Bank.

Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IV Register which must always contain the value 3C34h for access to the standard system variables.

Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BOTLN=23=24th line). Condition Code 3 (Screen Full) will be returned since SCRLCY will decrement to zero. If SCRLCY is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a POKE or setting the variables BOTLN and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BOTLN be set to line 21 (15H) and SCRCTL+1 be set to 21 (15H) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRLCY expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the 'Screen Full' condition.

By setting the SCRCTL variable and invoking the SCROLL (SCRLB) routine any portion of the screen may be scrolled at any time.

Margin Control:

In 80-Column Mode, there are actually 85 character positions per line. The variable MARGIN determines the offset of the beginning of the 80 column line from the left side of the screen and has valid offset values of 0, 1 or 2. An offset value of 1 centers the 80 column line on the screen; 0 begins at the extreme left side; 2 terminates at the extreme right side. The default value is 1. When MARGIN is set to 0, the variable LINLEN can be set to 85 to permit access to the 5 extra print positions. Whenever MARGIN and/or LINLEN are modified, a 'Set Cursor' operation should be done to insure the integrity of the print position.

NOTE: Since the different MARGIN values result in different pixel positions for the columns, care must be taken in mixing line length and margin values on the same line.

ADDITIONAL NOTES:

1. All screen operations done by the system ROM (PRINT, LIST, Edit line I/O, CLS, scrolling, etc.) relate only to the main Display File and work with standard 8-pixel wide characters. This means that every alternate 8 pixels across the screen will be affected. You will want to execute the Clear Screen function in this module to guarantee that no data in the second display file interferes with the use of a system screen service; e.g., prior to doing a LIST.
2. The COPY command will print only every other grouping of 8 pixels across the screen to the 2040 Printer.
3. During tape operations, the border will not change while in 80-column mode since this is fixed by the hardware to conform to the paper color.
4. The SAVE filename SCREENS will save only the main Display File data. The second can be saved by a SAVE filename CODE 24576,6144. Be careful that you have the computer in 80-column mode when you load this data or you will overwrite the 85 RAM routines and "crash" the system! (The count for saving the display file is for the data portion only since the Attribute File area from 7800H-7AFFH (30720-31487) is not used by the video mode M/M in 80-Column mode.

```

1      NAME AOL - ASC 002 00-CCL-MODE SUPPORT
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20

```

SUBTTL DEFINITIONS

```

22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75

```

```

=0010
=1701
=0478
=0778
=000E
=1720
=00PP
=00PA
=3C40
=3C65
=3C70
=3C70
=3C82
=3C84
=3CC2
=3CC3
=0040
=12C0
=0040
=07C0
=1000

```

PARAMETER INPUTS AND ENTRY POINTS FROM BASIC

					WITH MODULE LOADED AT 57344(0000H) DECIMAL ADDRESS
0000	20	DEPB	20H	CHAR.CODE FROM BASIC	57344
0001	C3 003P	JP	WRCHRB	ENTRY TO WRITE CHARACTER	57349
0004	C3 0171	JP	WRSTRB	ENTRY TO WRITE STRING	57349
0007	0000			(see PARAMS above)	
		DEPB	0	SET CURSOR PARAMETERS	
				LINCCL=CGL (0-79)	57351
				LINCCL=1=LINE (0-23)	57352
0009	C3 0:84	JP	SETCURB	ENTRY TO SET CURSOR POSN.	57353
000C	1800	DEPB	1800H	CLEAR SCREEN CONTRCLS	
				CLRCYL=STARTING LINE NO.	57356
				CLRCYL=1=LINE COUNT	57357
000E	C3 0270	JP	CLRSCLB	ENTRY TO CLEAR SCREEN	57358
0011	30	DEPB	30H	ATTRIBUTE CONTROL	57361
				BIT 7=PLASH (0 IN 00-CCL-)	
				0=BRIGHTCO IN 00-CCL-)	
				5	
				4=PAPER COLOR	
				3	
				2	
				1=INK COLOR	
				0	
0012	C3 02F3	JP	SETAT0B	ENTRY TO SET ATTRIBUTES	57362
0015	00	DEPB	0	MASK CONTROL FROM BASIC:	57365
				BIT 2=INVERSE	
				0=OVER	
0016	C3 031D	JP	SETMS0B	ENTRY TO SET MASK CONTRCLS	57366
0019	0000	DEPB	0	"GET" CONTROL FROM BASIC	57369
				FORMAT IS AS FOR LINCCL	
0018	C3 0326	JP	GETCHRB	ENTRY TO GET CHARACTER	57371
001E	C3 03CD	JP	GETAT0B	ENTRY TO GET ATTRIBUTE	57374
0021	C3 03DA	JP	GETCURB	ENTRY TO GET CURSOR POSN.	57377
0024	00	DEPB	0	VIDED MODE CONTROL	57380
				0=NORMAL	
				0=00-CCL-MODE	
0025	C3 03P1	JP	SETMDBB	ENTRY TO SET VIDED MODE	57381
0028	1701	DEPB	1701H	SCRCLL CONTROL	
				SCRCTL=STARTING LINE NO.	57384
				SCRCTL=1=LINE COUNT	57385
002A	C3 01CA	JP	SCRLOB	ENTRY TO SCRCLL	57386

```

116          SUBTTL BYMER VARIABLES
117          I
0020* 17      118 BOTLN      DEPB      17H      I BOTTOM LINE (LINE AFTER      97309
119          I WHICH SCREEN IS CONSIDERED
120          I PULL. AUTOMATIC SCROLL
121          I WILL BE DONE IF SCRLCT
002E* 01      122          I DOES NOT DECREMENT TO 0.
123          I SCRLCT      DEPB      1      I SCROLL COUNT - 1 PLUS NO.    97390
124          I OF TIMES AUTOMATIC SCROLL
125          I WILL BE DONE.
002F* 047E* 126 CNTBL      DEPW      CHRSET   I ADDRESS OF CHARACTER TABLE  97391
0021* 077E* 127 CNTBL      DEPW      GRPHST   I ADDRESS OF STD. GRAPHICS TBL. 97393
0023* 90      128 LINLEN      DEPW      90H      I LINE LENGTH(80 OR 93)        97395
0024* 1091    129 CURPOS      DEPW      1091H   I CURRENT POSITION                97396
130          I (INTERNAL FORMAT)
0026* 4090    131 DPADRS      DEPW      4090H   I CURRENT DISPLAY FILE ADDR.    97398
0028* 00      132 MASKB      DEPB      0        I MASK TO BE APPLIED TO        97400
133          I DISPLAY CHARACTERS
0029* 30      134 ATTYBY      DEPB      30H      I CURRENT ATTRIBUTES            97401
135          I (30=BLACK OR WHITE)
002A* 00      136 STINDX      DEPB      00H      I INDEX FOR ADJUSTING          97402
137          I CH.CODE IN CTRCHAR
002D* 0000    138 STRGCT      DEPW      0H      I REMAINING COUNT FROM WSTRG   97403
139          I WHEN "SCREEN PULL"
002D* 01      140 MARGIN      DEPB      1        I MARGIN ADJUST (0-2)          97405
002F* 07      141 DPRINT      DEPB      7        I BIT POSN. IN LINE(7,1,3,9)   97406
142
143          SUBTTL WRITE CHARACTER
144          I
003P*         145 WRCH01      I HERE FROM BASIC ENTRY TO DISPLAY THE
003P* 3A 0000* 146          LD      A,(DATAB)      I CHARACTER WHOSE CODE IS IN "DATAB"
147          I
0041*         148          I
0041* CB 0530* 149          WRCH01      CALL  LDPCSN      I ENTRY WITH CODE IN A
150          I LOAD REGISTERS FOR CURRENT POSITION
151          I BC=CURSOR POSITION FROM "CURPOS"
152          I ML=DISPLAY FILE ADDRESS FROM "DPADRS"
153          I A=CODE FOR CHAR. TO BE DISPLAYED
154          I TEST VALID RANGE
0043* FE 20    154          CP      20=          I < 20H
0047* 0A 0147* 155          JP      C,INVPAR      I
0044* FE 45    156          CP      9A5H          I
004C* 02 0147* 157          JP      NC,INVPAR      I > 44H
004F* C9      158          PUSH  BC          I SAVE CURSOR POSITION
0050* FE 80    159          CP      80H          I TEST IF ASCII PRINTABLE (20H-7FH)
0052* 30 19    160          JR      C,WRCH12      I YES
0054* FE 10    161          CP      90H          I TEST IF STD. GRAPHICS(80-0FH)
0056* 30 09    162          JR      C,WRCH11      I
0058* 80 40 SCTB 163          LD      BC,(UO6G)      I USER -DEFINED GRAPHICS(90-44H)
005C* 05      164          DEC      B            I ADJUST ADDRESS-100H
005D* 06 70    165          SUB      70=          I ADJUST PCR INDEX INTO TABLE
0061* 10 0C    166          JR      WRCH13      I
0061* EC 48 0031* 167          WRCH11      LD      BC,(STRBL)      I STD. GRAPHICS TABLE
0065* C0 60    168          SUB      60=          I ADJUST PCR INDEX INTO TABLE
0067* 10 04    169          JR      WRCH13      I
0069* 80 48 002P* 170          WRCH12      LD      BC,(CNTBL)      I CHARACTER TABLE
006A* 00      171          WRCH13      EX      DE,HL          I DE=POSN. IN DISPLAY FILE
006E* 26 00    172          LD      M,C          I
173          LD      L,A          I CODE IS INDEX INTO TABLE
0071* 20      174          ADD      HL,HL          I
0072* 20      175          ADD      HL,HL          I
0073* 20      176          ADD      HL,HL          I
0074* 09      177          ADD      HL,BC          I HL=PIXEL PATTERN IN TABLE
0075* C1      178          POP      BC          I
0076* EB      179          EX      DE,HL          I DE=PIXEL PATTERN IN TABLE/HL=OP
0077* 79      180          LD      A,C          I TEST IF END OF LINE
0078* 30      181          DEC      A            I
0079* 3A 0033* 182          LD      A,(LINLEN)      I
007C* 20 05    183          JR      NZ,WRCH16      I
007E* 3C      184          INC      A            I START OF NEW LINE
007F* 05      185          DEC      B            I BUMP LINE NO.
0080* 4P      186          LD      C,A          I LINLEN=1+START OF LINE
0081* 10 01    187          JR      WRCH15      I
0083* 3C      188          WRCH14      INC      A            I
0084* 09      189          WRCH15      CP      C            I TEST IF START OF LINE
0085* C3      190          PUSH  DE          I
0086* CC 0140* 191          CALL  Z,VPUL0      I TEST IF PAST "BOTTOM" LINE
0089* 01      192          POP      DE          I
193          I HERE TO PUT CHARACTER IN DISPLAY FILE
008A* C5      194          WRCH2      PUSH  BC          I CURSOR POSITION
008B* 83      195          PUSH  HL          I OF ADDRESS
008C* 3A 0030* 196          LD      A,(A540)      I GET OVER AND INVERSE CONTROLS
008F* 06 FF    197          LD      B,-1          I
0091* 1P      198          ORA          I
0092* 30 01    199          JR      C,WRCH3      I IF XORING NEW INTO OLD
0094* 04      200          INC      B            I B=-1 IF XORING =0 IF NOT
0095* 1P      201          WRCH3      ORA          I
0096* 1P      202          ORA          I
0097* 9P      203          SBC      A,A          I
0098* 4P      204          LD      C,A          I C=-1 FOR INVERSE =0 IF NOT
0099* 1E 00    205          LD      A,B          I SCAN COUNT
009B* 00      206          WRCH5      EX      AP,A"          I SAVE SCAN COUNT IN ALTERNATE
009C* C9      207          PUSH  BC          I SAVE MASK VALUES
009D* 05      208          PUSH  DE          I SAVE CHAR. TABLE ADDR
009E* 00 21 0000 209          LD      IX,D          I SAVE PTR. TO STACK
00A2* 00 39    210          ADD      IX,SP          I
00A4* 56      211          LD      D,(HL)          I READ SCAN LINE FROM OP
00A5* 85      212          PUSH  HL          I SAVE ADDRESS
00A6* 7C      213          LD      A,M          I
00A7* EE 20    214          XOR      20H          I TOGGLE TO ALTERNATE OP
00A9* 67      215          LD      M,A          I
00AA* C0 6P    216          BIT      5,A          I TEST WHICH OP
00AC* 20 01    217          JR      NZ,WRCH6      I IN OP2
00AE* 23      218          INC      HL          I NEXT BYTE
00AP* 98      219          WRCH6      LD      E,(HL)          I READ SCAN LINE FROM ALTERNATE OP
00B0* 00      220          EX      DE,HL          I SCANS TO HL. LAST OP POSN. IN DE
00B1* 78      221          LD      A,B          I TEST IF "OVER" IS ACTIVE
00B2* A7      222          AND      A            I
00B3* 20 1A    223          JR      NZ,XOROR      I YES-RETAIN OLD DATA
00B5* 3A 003E* 224          LD      A,(DPRINT)      I NO- CLEAR OLD DATA AT CHAR. POSN.
00B8* 04 07    225          SUB      7          I
00BA* 80 44    226          NEG          I
00BC* 01 03FF 227          LD      BC,03FFH      I MASK

```



```

008F* 26 00      228      JR      Z,MORR1      I NO NEED TO ADJUST(CHAR.STARTS AT BIT 7)
00C1* 37        229      SCP
00C2* C8 10     230      NEXT1  RR      B      I SHIFT MASK "A" TIMES
00C4* C8 19     231      RR      C      I (6 0'S START AT BIT 1 OF 0, BIT 3 OF 0,
00C6* 3D        232      DEC      A      I OR BIT 3 OF 0)
00C7* 20 P9     233      JR      NZ,NEXT1
00C9* 7C        234      MORR1  LD      A,M      I SCAN LINE FROM DP
00CA* 40        235      AND      B
00CB* 67        236      LD      M,A      I RETAIN ADJACENT CHAR.PIXELS
00CC* 7D        237      LD      A,L      I CLEAR CURRENT CHAR.PIXELS
00CD* 41        238      AND      C
00CF* 6P        239      LD      L,A      I ML=GLD BIT ROW
00D0* 3A 003E*  240      MORR1  LD      A,(DPBIT) I GET STARTING BIT POSN.
00D2* 04 07     241      SUB      7
00D4* ED 44     242      NEG
00D6* E5        243      PUSH    ML      I SAVE SCANS
00D7* 00 6E 00  244      LD      L,(IX)   I PTR. TO CHAR.SET
00DA* 00 46 01  245      LD      M,(IX+1)
00DD* 44        246      LD      B,(ML)
00DE* 0E 00     247      LD      C,J      I SCAN LINE FROM CHAR.SET TO BC
00E0* P5        248      PUSH    AP      I (PIXELS IN 0, BITS 7-2)
00E1* 78        249      LD      A,B      I SAVE A AND FLAGS
00E2* E6 PC     250      AND     DPCH     I PIXELS FROM CHAR. SET
00E4* 47        251      LD      B,A      I LIMIT TO 6 PIXELS
00E5* P1        252      POP     AP
00E6* E1        253      POP     ML      I RESTORE A AND FLAGS
00E7* 28 08     254      JR      Z,MORR2  I RESTORE SCANS
00E9* A7        255      AND     A      I NO SHIFT NEEDED
00EA* C8 10     256      NEXT2  RR      B      I CLEAR CARRY
00EC* C8 19     257      RR      C      I SHIFT "A" TIMES
00EE* 3C        258      DEC     A
00F0* 2C P9     259      JR      NZ,NEXT2
00F1* 7C        260      MORR2  LD      A,M      I SCAN FROM DP
00F2* A8        261      XOR     B      I INSERT/COMBINE NEW CHAR.
00F3* 67        262      LD      M,A
00F4* 7D        263      LD      A,L
00F5* A9        264      XOR     C
00F6* 6P        265      LD      L,A
00F7* 0C 7E 02  266      LD      A,(IX+2) I DP XOR CH.SET->ML
00FA* A7        267      AND     A      I TEST IF INVERSE ACTIVE
00FB* 28 1A     268      JR      Z,MORR3  I MASK TO INVERT
00FD* 3A 003E*  269      LD      A,(DPBIT)
0100* D6 07     270      SUB      7
0102* ED 44     271      NEG
0104* 01 PC00   272      LD      BC,DPCH00 I MASK TO INVERT
0107* 28 08     273      JR      Z,MORR3
0109* A7        274      AND     A
010C* C8 10     275      NEXT3  RR      B      I SHIFT MASK "A" TIMES
010E* C8 19     276      RR      C
0110* 3D        277      DEC     A
0111* 23 P9     278      JR      NZ,NEXT3
0112* 7C        279      MORR3  LD      A,M      I INVERT CHARACTER
0113* A8        280      XOR     B
0114* 67        281      LD      M,A
0115* 7D        282      LD      A,L
0116* A9        283      XOR     C
0117* 6P        284      LD      L,A
0118* E5        285      MORR3  EX     DE,ML     I DP ADRS. TO ML-SCAN LINE IN DE
0119* 73        286      LD      L,(ML),E I WRITE SCAN LINE TO CP
011A* E1        287      POP     ML      I ALTERNATE DP
011B* 72        288      LD      L,(ML),D I WRITE SCAN LINE TO CP
011C* 01        289      POP     DE      I CHAR.SET
011D* C1        290      POP     BC      I OVER/INVERSE INDICATORS
011E* 08        291      EX     AP,AP*   I RESTORE SCAN COUNT
011F* 24        292      INC     M      I NEXT SCAN IN DP
0120* 13        293      INC     DE      I NEXT BYTE IN CHAR.FILE
0121* 3C        294      DEC     A      I TEST IF MORE SCANS
0122* C2 000E*  295      JP      NZ,WRCM5
0124* E1        296      WRCM5  POP     ML      I TOP SCAN IN DP
0125* C1        297      POP     BC      I CURSOR POSITION
0126* 00        298      DEC     C      I ADJUST CURSOR POSITION
0127* 3A 003E*  299      LD      A,(DPBIT) I ADJUST BIT POSITION
012A* 58 06     300      SUB      6
012C* 32 003E*  301      LD      L,(DPBIT),A I STORE UPDATED POSITION
012E* 30 0E     302      JR      NZ,WRCM5 I NEXT CHAR. IN SAME BYTE
0131* C6 08     303      ADD     A,B      I NEXT CHAR. IN NEXT BYTE
0133* 32 003E*  304      LD      L,(DPBIT),A I STORE ADJUSTED VALUE
0136* 7C        305      LD      A,M      I ADJUST DP ADRS TO NEXT BYTE
0137* E2 20     306      XOR     ZM      I TOGGLE TO ALTERNATE DP
0139* 67        307      LD      M,A
013A* C8 6P     308      BIT     S,A      I TEST WHICH DP
013C* 20 01     309      JR      NZ,WRCM5 I DONE IF DP2
013E* 23        310      INC     ML      I NEXT BYTE IN DP1
013F* CC 055D*  311      WRCM5  CALL    STPCS5N I STORE NEW POSITION
0142* 0E 00     312      GOODREY LD      C,0      I RETURN BC=0
0144* 06 00     313      ERRRET LD      B,0      I ENTER HERE WITH C NON-ZERO
0146* C9        314      RET
0147* 0E 01     315
0149* 18 P9     316      INVPAR LD      C,1 I RETURN BC=1 FOR INVALID PARAMETERS
014B* 3E 10     317      JR      ERRRET
014D* 9D        318
014E* 57        319      I
014F* 3A 002D*  320      TVPULQ LD      A,SCRSZ I TEST IF NEW LINE IS OFF SCREEN
0152* 8A        321      SUB     B      I A=LINE NO.
0153* 02 0507*  322      LD      D,A      I SAVE IN D
0156* 3A 002E*  323      LD      A,(BOTLN) I GET BOTTOM LINE
0159* 3C        324      CP      B
015A* 28 00     325      JP      NC,UPDPDSN I ON SCREEN - RETURN TO WRITE CHAR.
015B* 3E 01     326      LD      A,(SCRCLCT) I VIA UPDATE AND STORE POSITION
015C* 3E 01     327      DEC     A      I TEST SCROLL COUNT
015D* 32 002E*  328      JR      NZ,TVPUL1 I DO AUTOMATIC SCROLL USING SCRCLT
0161* C1        329      LD      A,1      I REINITIALIZE TO 1
0162* C1        330      LD      L,(SCRCLCT),A
0163* 0E 03     331      POP     BC
0164* 18 0D     332      POP     BC      I DISCARD RETURN TO WRITE CHAR.
0167* 31 002E*  333      LD      C,J      I RETURN BC=J FOR SCREEN FULL
016A* 6D 48 002D* 334      JR      ERRRET
016E* C3 0104*  335      TVPUL1 LD      (SCRCLCT),A I UPDATE VARIABLE
016F* 337      LD      BC,(SCRCLT) I GET SCROLL CONTROLS (STARTING LINE
0170* 338      JP      SCRL0 I AND LINE COUNT)
0171* 339      I      I RETURN TO WRITE CHAR. VIA SCROLL

```

```

341          SUBTYL WRITE STRING
342          |
343          |
344          |
345          |
346          WRSTR0 LD A,C(PARAM0)
347          AND IPR
348          OR 40H
349          LD D,A
350          LD HL,C(VARS)
351          WRSTR1 LD A,C(L)
352          AND 07FH
353          JR I,NOSTRG
354          CP 0
355          JR I,WRSTR2
356          PUSH 00
357          CALL 00CLEM
358          BR 00=ML
359          POP 00
360          JR WRSTR1
361          WRSTR2 INC HL
362          LD C,(HL)
363          INC HL
364          LD B,(HL)
365          INC HL
366          LD A,B
367          OR C
368          RET Z
369          |
370          |
371          |
372          |
373          |
374          WRSTRG LD A,(HL)
375          PUSH HL
376          PUSH BC
377          CALL WRCHAR
378          LD A,C
379          BR B
380          JR NZ,WRSTRX1
381          WRSTR3 POP BC
382          POP HL
383          INC HL
384          DEC BC
385          LD A,B
386          OR C
387          JR NZ,WRSTRG
388          RET
389          |
390          |
391          WRSTR4 LD A,C
392          CP 1
393          JR I,WRSTR3
394          POP HL
395          LD (STRGCT),HL
396          POP HL
397          LD C,3
398          WRSTR2 LD B,0
399          RET
400          |
401          NOSTRG LD C,2
402          JR WRSTR2
403          |
404          |
405          |
406          |
407          SUBTYL POSITION CONTRL
408          |
409          SETC001
410          |
411          LD BC,(L(NCOL))
412          |
413          SETCUR:
414          CALL TSTPAR
415          CALL CONVPF
416          CALL UPCPOSN
417          JP GOODRET
418          |
419          |
420          SUBTYL SCROLL
421          |
422          |
423          SCRL0C1:
424          |
425          LD BC,(SCRCTL)
426          |
427          SCRL1:
428          |
429          |
430          LD A,B
431          AND A
432          JP I,INVPAR
433          ADD A,C
434          CP 1
435          JP C,INVPAR
436          CP 25
437          JP NC,INVPAR
438          CALL SCRL0
439          JP GOODRET
440          |
441          SCRL0 PUSH BC
442          LD A,SCRSZ
443          SUB C
444          LD B,A
445          LD A,(LINLEN)
446          INC A
447          LD C,A
448          CALL LMBU
449          POP BC
450          PUSH BC
451          LD A,L
452          AND A
453          JR I,STBLK
454          RLCA
455          RLCA
456          RLCA

```

HERE FROM BASIC ENTRY TO
WRITE CHARACTER STRING TO SCREEN.
STRING IDENTIFIER (CM,CGDE) IN
SYSTEM VARIABLE PARAM0 AT SCC3M
MASK OFF UPPER BITS
STRING VARIABLE IDENTIFIER
SAVE IN D
PING STRING
TEST IF END OF VARS AREA
NO FIND - RETURN BC=2
TEST IF MATCH
FOUND STRING
SAVE STRING ID
RETURNS ADDR. OF NEXT VAR. IN DE
ADDR. TO HL
RESTORE STRING ID
GET LENGTH
FIRST TEXT CODE
TEST IF NULL STRING
RETURN IF 00
ENTRY FROM MACHINE CODE WITH
ADDRESS OF CHAR. CODE "STRING"
IN HL AND LENGTH IN BC
GET CODE
SAVE ADDR. AND
COUNT
WRITE "A"
TEST IF GOOD
EXIT IF INVALID CODE OR
IF SCREEN FULL
COUNT
ADDRESS
NEXT CHAR.
ADJUST COUNT
TEST IF DONE
WRITE NEXT CHAR.
RETURN BC=0
SAVE BR00?
TEST IF UNRECOGNIZED CODE
CONTINUE
REMAINING COUNT TO HL
STORE REMAINING COUNT
ADDRESS TO HL
RETURN BC=3 FOR SCREEN PULL
RETURN BC=2 IF STRING NOT FOUND
HERE FROM BASIC ENTRY. PARAMETERS
IN LINC0L
ENTRY WITH LINE/COL. IN BC REG.
TEST PARAMETERS FOR VALIDITY
CONVERT TO INTERNAL FORMAT
CALCULATE AND STORE POSITION
RETURN BC=0
HERE FROM BASIC ENTRY TO SCROLL
SCREEN
GET CONTRL INPD.
HERE WITH CONTR0LS IN BC
B=NO. OF LINES
C=STARTING LINE NO.
TEST VALIDITY
ERROR IF COUNT=0
ERROR IF B+C<1
ERROR IF B+C>24
OD SCROLL
RETURN BC=0
GET STARTING LINE IN INTERNAL FORMAT
COL. 0
SAVE ORIG. BC FOR EXIT
TEST IF AT START OF BLOCK
YES

```

0171 3A SCC3
0172 06 1F
0173 P6 40
0174 57
0175 2A SC40
0176 7E
0177 06 7F
0178 20 35
0179 0A
0180 20 00
0181 00
0182 CD 1720
0183 00
0184 01
0185 10 P0
0186 23
0187 4E
0188 23
0189 44
0190 23
0191 70
0192 01
0193 C0
0194 7E
0195 E5
0196 C5
0197 CD 0042
0198 79
0199 00
019C 20 09
019E C1
019F 01
01A0 23
01A1 00
01A2 70
01A3 01
01A4 20 EE
01A6 C9
01A7 79
01A8 PE 01
01AA 20 P2
01AC E1
01AD 22 0039
01B0 21
01B1 00 03
01B3 00 00
01B5 C9
01B6 0E 02
01B8 10 P9
01BA ED 40 0007
01BE CD 04EB
01C1 CD 04FC
01C4 CD 0507
01C7 C3 0142
01CA EC 40 0020
01CE
01CF 70
01CP 47
01D0 CA 0147
01D3 01
01D4 PE 01
01D6 DA 0147
01D9 PE 19
01DB 02 0147
01DE CD 0144
01E1 C3 0142
01E4 C5
01E5 JE 10
01E7 91
01E8 47
01E9 3A 0033
01EC 3C
01ED 0F
01EE CC 033F
01F1 C1
01F2 C5
01F3 70
01F4 47
01F5 20 51
01F7 07
01F8 07
01F9 07

```

017A	4F	457	LD	C,A	I GET NO. OF LINES THIS BLOCK	
017B	3E 00	458	LD	A,B		
017D	91	459	SUB	C		
017E	80	460	CP	B	I TEST AGAINST TOTAL LINES	
017F	30 3F	461	JR	C,GRBLR	I TOTAL GREATER THAN THIS BLOCK	
0201	70	462	INSOBLR	LD	A,B	
0202	06 00	463	LD	B,0	I REMAINING COUNT	
0204	C5	464	PUSH	BC		
0205	47	465	LCOP	LD	B,A	I B=LINES THIS BLOCK
0206	0E 00	466	LD	C,B	I C=SCAN COUNT	
0208	70	467	LCOP0	LD	A,B	
0209	C3	468	PUSH	BC	I SAVE SCAN COUNT	
020A	0F	469	BRCA			
020B	0F	470	BRCA			
020C	0F	471	BRCA		I CALCULATE NO. OF BYTES	
020D	4F	472	LD	C,A		
020E	06 00	473	LD	B,0	I BC=3200 OF LINES	
0210	80	474	LOOP1	EX	DE,ML	
0211	21 PFE0	475	LD	ML,-20H	I GET ADRS. OF PREV. LINE	
0214	19	476	ADD	ML,DE		
0219	80	477	EX	DE,ML	I TO DE	
0216	C3	478	PUSH	BC	I SAVE COUNT	
0217	85	479	PUSH	ML	I SAVE ADDRESS	
0218	8C 00	480	LDIR		I DO MOVE	
021A	81	481	POP	ML		
021B	C1	482	POP	BC		
021C	7C	483	LD	A,M		
021D	CB 6F	484	BIT	S,A	I TEST IF DP1	
021P	20 05	485	JR	NZ,NTSC	I NO	
0221	P6 20	486	OR	ZOH		
0223	67	487	LD	M,A	I DO DP2	
0224	10 EA	488	JR	LOOP1		
0226	86 DP	489	AND	ODPH	I BACK TO DP1	
0228	67	490	LD	M,A		
0229	24	491	INC	M	I NEXT SCAN LINE	
022A	C1	492	POP	BC	I SCAN COUNT	
022B	0D	493	DEC	C		
022C	20 DA	494	JR	NZ,LOOP0	I MORE SCANS	
022E	C1	495	POP	BC	I RESIDUAL LINE COUNT	
022P	70	496	LD	A,B		
0230	47	497	AND	A		
0231	20 04	498	JR	Z,SCRLK1	I DONE	
0233	2E 00	499	LD	L,0	I START OF NEXT BLOCK	
0235	10 AC	500	JR	TESTAD	I DO REMAINING LINE(S)	
0237	C1	501	SCRLK1	POP	BC	I ORIG. BC
0238	79	502	LD	A,C	I STARTING LINE	
0239	80	503	ADD	A,0	I ADD NO. OF LINES MOVED	
023A	3D	504	DEC	A	I LAST LINE SCROLLED	
023B	4F	505	LD	C,A		
023C	06 01	506	LD	B,1	I CLEAR 1 LINE	
023E	10 52	507	JR	CLRSC0	I CLEAR VACATED LINE AND RETURN	
		508				
0240	4F	509	GRBLR	LD	C,A	
0241	70	510	LD	A,0		
0242	91	511	SUB	C	I ADJUST TOTAL LINE COUNT BY NO.	
0243	47	512	LD	B,A	I OF LINES THIS BLOCK	
0244	C3	513	PUSH	BC		
0245	79	514	LD	A,C	I LOAD A NO. OF LINES THIS BLOCK	
0246	10 BD	515	JR	LOOP		
		516				
0248	05	517	STBLR	DEC	B	I ADJUST TOTAL LINE COUNT-1
0249	C3	518	PUSH	BC		
024A	0E 00	519	LD	C,0	I SCAN COUNT	
024C	C3	520	STBLR2	PUSH	BC	I SAVE SCAN COUNT
024D	80	521	STBLR1	EX	DE,ML	
024E	21 PFE0	522	LD	ML,-720H		
0251	19	523	ADD	ML,CE		
0252	80	524	EX	DE,ML	I ADRS. OF PREV. LINE	
0253	01 0020	525	LD	BC,32	I BYTE COUNT	
0256	E5	526	PUSH	ML		
0257	ED 80	527	LDIR		I DO MOVE	
0259	E1	528	POP	ML		
025A	7C	529	LD	A,M		
025B	CB 6F	530	BIT	S,A		
025D	20 05	531	JR	NZ,STBLR2		
025P	P6 20	532	OR	ZOH	I DO DP2	
0261	67	533	LD	M,A		
0262	10 E9	534	JR	STBLR1		
0264	86 DP	535	AND	ODPH	I BACK TO DP1	
0266	67	536	LD	M,A		
0267	24	537	INC	M	I TO NEXT SCAN LINE	
0268	C1	538	POP	BC		
0269	0D	539	DEC	C	I TEST IF MORE SCANS	
026A	20 E0	540	JR	NZ,STBLR0	I REMAINING LINE COUNT	
026C	C1	541	POP	BC		
026D	70	542	LD	A,B		
026E	A7	543	AND	A		
026F	20 C6	544	JR	Z,SCRLK1		
0271	11 PFE0	545	LD	DE,-7E0H	I ADJUST TO LINE 1	
0274	19	546	ADD	ML,DE		
0275	C3 01F3	547	JP	TESTAD	I SC=LINE COUNT	
		548			I ML=OP ADDRESS	
		549				
		551		SUBTTL	CLEAR SCREEN	
		552				
		553				
0278		554		CLRS901		
0278	ED 40 000C	555	LD	BC,(CLRCTL)	I HERE FROM BASIC ENTRY TO CLEAR SCREEN	
		556			I GET CONTROL INFO.	
027C		557		CLRS001		
		558				
027C	70	559	LD	A,B	I ENTRY WITH BC=LINE COUNT AND	
027D	A7	560	AND	A	I STARTING LINE NO. FOR CLEAR	
027E	CA 0147	561	JP	Z,INVPAR		
0281	81	562	ADD	A,C	I TEST VALIDITY	
0282	FE 01	563	CP	1	I ERROR IF COUNT=0	
0284	DA 0147	564	JP	C,INVPAR	I ERROR IF B=C<1	
0287	FE 19	565	CP	Z3		
0289	D2 0147	566	JP	NC,INVPAR	I ERROR IF B=C>24	

```

020C* CO 0292*      367      CALL CLRSC0      I DO SERVICE
020P* C3 0142*      368      JP      G0C0RET  I RETURN BC=0
369
370
371      CLRSC0 PUSH BC
372      LD      A,CLRSZ I CONVERT TO INTERNAL FORMAT
373      SUB      C
374      LD      B,A
375      LD      A,(LINLEN)
376      INC      A      I SET TO COL-0
377      LD      C,A
378      CALL LNOU      I GET ADDRESS
379      LD      D,B
380      LD      E,C      I SAVE POSITION
381      POP      BC      I ORIG. BC
382      PUSH    DE
383      CLRSC1 LD      A,L      I GET # OP LINES THIS BLOCK
384      RLCA
385      RLCA
386      RLCA
387      LD      C,A
388      LD      A,0
389      SUB      C
390      CP      B      I COMPARE TO TOTAL LINE COUNT
391      JR      C,CLRSC7
392      CLRSC2 LD      A,0      I NO. OF LINES
393      LD      B,0      I REMAINING LINES
394      PUSH    BC
395      CLRSC3 LD      B,A      I SCAN COUNT
396      LD      C,0
397      CLRSC4 LD      A,0
398      PUSH    BC
399      AND      7
400      RRCA
401      RRCA
402      RRCA
403      LD      C,A      I BC=32*NO. OF LINES
404      LD      B,0      I (=0 IF 256 FOR 0 LINES)
405      DEC      C      I ADJUST
406      CLRSC5 PUSH    BC
407      PUSH    HL
408      LD      D,M
409      LD      E,L
410      LD      (HL),00      I CLEAR OP
411      INC      DE
412      LOIR
413      POP      HL
414      POP      BC
415      LD      A,M
416      BIT      5,A
417      JR      NZ,CLRSC6
418      OR      Z0M
419      LD      M,A      I DO OP2
420      JR      CLRS5
421      CLRSC6 AND      0DFH      I RESTORE OP1
422      LD      M,A      I NEXT SCAN RM
423      INC      M      I POP SCAN COUNT
424      POP      BC
425      DEC      C
426      JR      NZ,CLRSC4
427      POP      BC      I TOTAL LINE COUNT
428      LD      A,0
429      AND      A
430      JR      I,CLRSX7
431      LD      L,C      I HL=START OF NEXT BLOCK
432      JP      CLRS1    I B=REMAINING LINE COUNT
433      CLRSX7 POP      BC      I POSITION
434      JP      UPDOPSN  I RETURN VIA UPDATE AND STORE POSITION
435      CLRS7  LD      C,A      I SAVE NO. LINES THIS BLOCK
436      LD      A,B
437      SUB      C      I ADJUST TOTAL LINE COUNT
438      LD      B,A
439      PUSH    BC      I REMAINING LINE COUNT
440      LD      A,C      I LINES THIS BLOCK
441      JR      CLRS3
442      SUBTL  ATTRIBUTE CONTROL
443
444
445
446
447      SETAB0: LD      A,(ATYCTL)      I HERE FROM BASIC ENTRY TO SET ATTRIBUTES
448      LD      A,C      I GET CONTROL INFO.
449
450      SETATT: I ENTRY WITH ATTRIBUTE BYTE IN A
451      AND      7      I GET INK SELECTION
452      LD      C,A      I SAVE
453      LD      A,(VIDMOD)      I TEST IF OP2 OPEN
454      AND      A
455      JP      I,INVPAR      I INVALID IF OP2 NOT OPEN
456      LD      A,C
457      RLA
458      RLA
459      RLA
460      PUSH    AP      I SHIPT TO M/W POSITION
461      OR      6      I SAVE ROTATED VALUE
462      LD      B,A      I SET M/W MODE
463      IN      A,(MREXPY)      I VALUE FOR M/W
464      AND      0COM      I
465      OR      B      I PRESERVE BITS 7 AND 6
466      OUT      (MREXPY),A      I SET VIDED MODE VALUE
467      OR      40M      I SET M/W
468      LD      (VIDMOD),A      I UPDATE VIDMOD
469      POP      AP
470      CPL      A      I INK SEL. IN BITS 3-5
471      OR      C      I GET COMPLEMENTARY CCLR
472      LD      (ATYBYT),A      I COMBINE WITH INK
473      JP      G0C0RET      I SAVE IN VARIABLE
474
475
476
477      SETM0: LD      A,(MSCTL)      I HERE FROM BASIC ENTRY TO SET MASK
478      LD      A,C      I GET CONTRL INFO.
479
480      SETMSK: I ENTRY WITH MASK VALUE IN A
481      LD      (MASKB),A      I STORE MASK FOR APPLICATION TO FUTURE
482      JP      G0C0RET      I DISPLAYS
483

```

```

685          SUBTTL "GET" ROUTINES
686          I
687          I
0320*          688          GTCN00:          I HERE FROM BASIC ENTRY TO GET CHARACTER
689          689          I (RETURNS CODE FOR CHARACTER AT
690          690          I POSITION SPECIFIED IN GETCTL)
691          691          LO BC,(GETCTL)          I GET CONTRL INPG.
692          I
0320*          692          I GTCN01:          I ENTRY WITH POSITION IN BC
0320*          693          CALL TSTPAD          I TEST PARAMETERS
0320*          694          LD A,(CSPBIT)          I SAVE CURRENT BIT POSITION
0320*          695          PUSH AP
0320*          696          CALL CONVPM          I CONVERT TO INTERNAL FORMAT
0320*          697          CALL CA,CPOS          I GET OP ADDRESS AND BIT POS.
0320*          698          LD DE,(CNTBL)          I CHAR.TABLE
0320*          699          LD D,96          I NO. OF PRINTABLE CHARACTERS
0320*          700          LD A,00H          I SET ADJUSTMENT INDEX
0320*          701          GTCN1:          LD (GTINDEX),A
0320*          702          INC D          I ADJUST TO START OF TABLE
0320*          703          GTCN2:          PUSH BC          I SAVE COUNT
0320*          704          PUSH HL          I SAVE ADDR. IN OP
0320*          705          PUSH DE          I SAVE ADDR. IN CHAR.TABLE
0320*          706          CALL GTC2BC          I GET SCAN LINE FROM OP TO REG.B
0320*          707          I
0320*          708          I
0320*          709          GTCN22:          LD A,B          I 6 BITS TO A
0320*          710          AND OPCM          I
0320*          711          LD B,A          I
0320*          712          RR DE,HL          I CHAR.SET PTR. TO HL
0320*          713          LD A,(HL)          I PIXEL ROW TO A
0320*          714          AND OPCM          I MASK TO 6 BITS
0320*          715          XOR B          I TEST AGAINST OP
0320*          716          JR Z,GTCN2          I MATCH
0320*          717          PUSH AP          I
0320*          718          LD A,(GTINDEX)          I TEST IF LOGGING AT STD.GRAPHICS
0320*          719          CP 96          I
0320*          720          JR NZ,GTCN23          I
0320*          721          POP AP          I
0320*          722          LD A,GTCN4          I DO NOT TEST INVERSE IF STD.GR.
0320*          723          GTCN23:          POP AP          I
0320*          724          CP OPCM          I TEST IF MATCH ON INVERSE
0320*          725          JR NZ,GTCN4          I DOES NOT MATCH
0320*          726          GTCN3:          LD C,A          I C=0 FOR MATCH -4 FOR INVERSE
0320*          727          LD B,7          I SCAN COUNT
0320*          728          GTCN31:          RR DE,HL          I OP ADDR. TO HL
0320*          729          PUSH SC          I SAVE MATCH AND SCAN COUNT
0320*          730          INC W          I NEXT SCAN IN OP
0320*          731          INC DE          I NEXT ROW IN CHAR.SET
0320*          732          CALL GTC2BC          I NEXT SCAN ROW TO BC
0320*          733          LD A,B          I
0320*          734          AND OPCM          I
0320*          735          LD B,A          I
0320*          736          RR DE,HL          I CHAR.SET PTR. TO HL
0320*          737          LD A,(HL)          I CHAR.SET ROW
0320*          738          AND OPCM          I MASK TO 6 BITS
0320*          739          XOR B          I TEST MATCH
0320*          740          POP BC          I GET PREV.MATCH AND SCAN COUNT
0320*          741          XOR C          I
0320*          742          JR NZ,GTCN4          I NO MATCH-START AGAIN AT NEXT
0320*          743          GTCN31:          GTCN31          I DO NEXT SCAN
0320*          744          LD A,C          I A=0 IF MATCH ON INVERSE
0320*          745          ADD A,3          I SET TO -1 IF MATCH ON INVERSE
0320*          746          POP BC          I HERE IF MATCH-(SP)=PTR. TO CHAR.
0320*          747          POP BC          I IN TABLE(SP+2)=CHAR.IN OP:
0320*          748          POP BC          I (SP+4)=CHAR.COUNT
0320*          749          LD C,A          I B=COUNT C=-1 IF MATCH ON INVERSE
0320*          750          LD A,(GTINDEX)          I CONVERT MATCH TO CHAR.CODE
0320*          751          SUB B          I
0320*          752          CP 20H          I TEST IF MATCH ON SPACE
0320*          753          JR NZ,GTCN32          I NO
0320*          754          INC C          I TEST IF MATCH ON INVERSE
0320*          755          JR NZ,GTCN32          I
0320*          756          LD A,0FH          I LOAD CODE FOR GRAPHICS BLOCK
0320*          757          GTCN32:          LD C,A          I RETURN IN REG. C OF BC PAIR
0320*          758          LD B,0          I AND IN REG. A
0320*          759          POP AP          I
0320*          760          LD (CSPBIT),A          I RESTORE CSPBIT
0320*          761          LD A,C          I CHAR.CODE TO A
0320*          762          RET          I
0320*          763          I
0320*          764          I GTCN4:          POP HL          I PTR. TO CHR. SET
0320*          765          LD DE,B          I MOVE ON TO NEXT CHARACTER
0320*          766          ADD HL,DE          I
0320*          767          LD B,L          I
0320*          768          LD D,H          I
0320*          769          POP HL          I OP ADDRESS
0320*          770          POP BC          I CHAR. COUNT
0320*          771          GTCN2:          GTCN2          I DECREMENT CHAR.COUNT AND LOCK AGAIN
0320*          772          I
0320*          773          I
0320*          774          LD A,(GTINDEX)          I TEST IF DONE
0320*          775          CP 00H          I TEST IF STD.CHG.SET
0320*          776          JR NZ,GTCN5          I TRY GRAPHICS
0320*          777          LD DE,(CNTBL)          I GRAPHICS TABLE
0320*          778          LD B,10          I NO. OF ENTRIES
0320*          779          LD A,00H          I
0320*          780          GTCN5:          LD A,00H          I TEST IF STD.GRAPHICS
0320*          781          CP 00H          I
0320*          782          JR NZ,GTCN4          I TRY USER-DEFINED GRAPHICS
0320*          783          LD DE,(UDG)          I
0320*          784          OR D          I ADJUST ADDRESS-100H
0320*          785          LD B,02          I NO. OF ENTRIES
0320*          786          LD A,0ASH          I INDEX ADJUST
0320*          787          JP GTCN1          I
0320*          788          I
0320*          789          I
0320*          790          I NO MATCH AT ALL

```

```

03C6* P1          789  GETM6 POP AP          | RESTORE ORIG. OPBIT
03C7* 32 0038*   790  LD (DPBIT),A         |
03C8* 48         791  LD C,B              | BC=0
03C9* 8P        792  XOR A               | A=0
03CC* C9        793  RET
794
795 |
796 | GET ATTRIBUTE BYTE
797 | NOTE THAT IN 80-CCL.MODE ALL SCREEN POSITIONS
798 | HAVE THE SAME ATTRIBUTES, THEREFORE IT IS
799 | NOT NECESSARY TO USE THE "GETCTL" POSITION
800 | PARAMETERS
801
802 |
803 | GETATTR: | HERE FROM BASIC ENTRY TO GET ATTRIBUTE
804 |
805 | GETATT: LD A,(ATTBYT) | GET ATTRIBUTE BYTE
806 | LD C,A | PUT IN BC
807 | LD B,0
808 | RET
809
810 |
811 | GETCUR: | GET CURSOR POSITION
812 | HERE FROM BASIC ENTRY
813 |
814 | LD BC,(CURPOS) | GET INTERNAL POSITION
815 | CALL CONVPM | CONVERT TO USER FORMAT
816 | LD A,(CLINLEN) | TEST IF END OF LINE
817 | CP C | IF =00 OR 85
818 | JR NZ,GETC1 | NO
819 | LD C,0 | NEXT POSN. START OF NEXT LINE
820 | LD A,B
821 | INC A | BUMP TO NEXT LINE
822 | LD B,A
823 | CP 24 | TEST IF OFF SCREEN
824 | JR C,GETC1 | NO
825 | LD B,23 | POSN. IS AT BOTTOM LINE
826 | LD (LINCCL),BC | STORE FOR BASIC PROGRAM
827 | RET | VALUES ALSO IN BC
828
829 |
830 | SUBTTL VIDEO MODE CONTROL
831 |
832 |
833 |
834 |
835 |
836 |
837 |
838 |
839 |
840 |
841 |
842 |
843 |
844 |
845 |
846 |
847 |
848 |
849 |
850 |
851 |
852 |
853 |
854 |
855 |
856 |
857 |
858 |
859 |
860 |
861 |
862 |
863 |
864 |
865 |
866 |
867 |
868 |
869 |
870 |
871 |
872 |
873 |
874 |
875 |
876 |
877 |
878 |
879 |
880 |
881 |
882 |
883 |
884 |
885 |
886 |
887 |
888 |
889 |
890 |
891 |
892 |
893 |
894 |
895 |
896 |
897 |
898 |
899 |
900 |
901 |

```

```

0407* C8 38
0408* C8 38
0409* C8 38
0410* 80
0411* 32 0039*
0412* 32 0011*
0413* 3A 0033*
0414* 3C
0415* 3C
0416* 06 18
0417* 21 0000
0418* 22 5C7D
0419* C0 8507*
0420* C3 0142*
0421* 0E 02
0422* C3 0144*

```

```

044C* 4F
044D* C8 47
044E* 20 16
044F* PE 80
0450* 28 1A
0451* C8 7F
0452* C0
0453* 47
0454* PE 02
0455* C8
0456* E6 C6
0457* E8 06
0458* C0
0459* 3E 40
0460* B1
0461* 4F
0462* AF
0463* C9
0464* PE 01
0465* C1
0466* 06 01
0467* 0E 81
0468* C9
0469* AF
0470* 47
0471* C9

```

```

04D2* F5
04D3* 08 FF
04D4* F4 80
04D5* 03 FF
04D6* 3E 03
04D7* D3 F4
04D8* F1
04D9* C9
04DA* F5
04DB* AF
04DC* D3 F4
04DD* 08 FF
04DE* E6 3F
04DF* D3 FF
04E0* F1
04E1* C9

```

```

04E8* 2E 17
04E9* 88
04EA* 30 04
04EB* F1
04EC* C3 0147*
04ED* 3A 0033*
04EE* 3D
04EF* 89
04F0* 38 F5
04F1* C9

```

```

04FC* 3A 0033*

```

```

902 SRL B
903 SRL B
904 SRL B
905 OR B
906 LD (ATYBYT),A ; ATTRIBUTE BYTE
907 LD (ATTCTL),A ; BASIC PARAMETER
908 LD A,(CLINLEN)
909 TMC A
910 LD C,A
911 LD B,18H ; BC=HOME POSITION (LN.0/COL.0)
912 LD HL,0
913 LD (CDBRDS),HL ; PLOT POSITION
914 CALL UPDPOSN ; UPDATE AND STORE HOME POSITION
915 JP GCORET ; RETURN BC=0
916 EXTRM LD C,2 ; RETURN BC=2 FOR NO ROOM
917 JP EBRRET
918
920 SUBTTL INTERNAL SUB-RTNS.
921
922
923
924
925 ; INPUT: VIDEO MODE IN A
926 ; OUTPUT: M/W SETTING IN B
927 ; VIDMOD SETTING IN C
928 ; RETURNS NZ STATUS IF MODE INVALID
929
930 ; VALID VALUES: INPUT M/W VIDMOD
931 ; #=DISPLAY FILE ACTIVE AT SCREEN
932
933 ;
934 ; DPI ONLY 0 0 0
935 ; DPI# DPI 80 0 80
936 ; DPI E DPI 1 1 81
937 ; HIGH RES.CH. 2 2 2
938 ; 64/80-COLUMN 06 - 3E 06 - 3E 46 - 7E
939
940 GETVAL LD C,A
941 BIT 0,A
942 JR NZ,TST01
943 CP 80H
944 JR Z,SETO
945 BIT 7,A
946 RET NZ
947 LD B,A
948 CP 2
949 RET Z
950 AND 0C6H
951 XOR 6
952 RET NZ
953 LD A,40H
954 OR C
955 LD C,A
956 XOR A
957 RET
958 TST01 CP 1
959 RET NZ
960 LD B,1
961 LD C,01H
962 RET
963 SET0 XOR A
964 LD B,A
965 RET
966
967
968 ; ROUTINES TO ENABLE CHUNK 0 IN ROM EXT.
969 ; FOR ACCESS TO CHNGVID ROUTINE
970
971 ENBLEXT PUSH AP
972 IN A,(HREXPT)
973 OR 80H ; SELECT ROM EXT.
974 OUT (HREXPT),A
975 LD A,3
976 OUT (CHMSPT),A
977 POP AP
978 RET
979
980 ENBLMCHM PUSH AP
981 XOR A
982 OUT (CHMSPT),A
983 IN A,(HREXPT)
984 AND 03FH
985 OUT (HREXPT),A
986 POP AP
987 RET
988
989
990 ; TEST PARAMETER VALIDITY
991 ; IF NOT VALID, DISCARDS RETURN
992 ; AND EXITS VIA INVPAR
993 ; TEST LINE >23
994
995 TSTPAR LD A,23
996 CP B
997 JR NC,TSTPRI
998 PARERR POP AP
999 JP INVPAR
1000 TSTPRI LD A,(CLINLEN)
1001 DEC A
1002 CP C ; TEST COL. >LINE LENGTH-1
1003 JR C,PARERR
1004 RET
1005
1006
1007 ; CONVERT LINE/COL. FROM USER TO
1008 ; INTERNAL FORMAT AND VICE VERSA
1009 ; USER FORMAT: LINES 0-23
1010 ; COL. 0-79(84)
1011 ; INTERNAL FORMAT:
1012 ; LINES 24-1
1013 ; COL. (04)81-2
1014 ; (COL.1 END OF LINE)
1015
1016 CONVPN LD A,(CLINLEN)

```

```

04PP 3C
0500 91
0501 4P
0502 3E 10
0504 90
0509 47
0506 C9

0507
0507 CC 050C
050A 10 44

050C C0 053P
050F 3A 0533
0511 3C
0513 91
0514 90
0515 9F
0516 07
0517 03
0518 C0 3P
051A C0 3P
051C C0 47
051E 20 04
0520 90
0521 3E 20
0523 04
0524 47
0525 91
0526 47
0527 1P
0528 9F
0529 1A 0030
052C 03
0530 9F
0532 16 00
0533 19
0534 10 07
0535 91
053A 04 03
053B 20 02
053C 9F
0539 30
053A 03
053B 32 0030
053C C9

053P
053P 3E 10
0541 90
0542 97
0543 0P
0544 0P
0545 0P
0546 06 10
0548 4P
0549 7A
054A 06 10
054C 06 40
054E 47
054P C9

0550
0550 ED 43 0034
0554 22 0036
0557 C9

0558
0558 EC 4B 0034
055C 2A 0036
055P C9

0560 85
0561 40
0562 7C
0563 8E 20
0565 07
0566 C0 4P
0569 20 01
056A 23
056B 4E
056C 1A 0030
056P C0 07
0571 ED 44
0573 20 07
0575 C0 11
0577 C0 10
0579 30
057A 20 09
057C 81
057D C9

1011 INC A
1012 SUB C
1013 LO C.A
1014 LO A.SCRSI
1015 SUB B
1016 LO B.A
1017 RET
1018
1019
1020
1021 UPOPSM
1022
1023 CALL CALCPBS
1024 JR STPSM
1025
1026
1027
1028
1029
1030
1031 CALCPBS CALL LNSU
1032 LO A.(LNLN)
1033 INC A
1034 SUB C
1035 PUSH AP
1036 LO B.A
1037 ADD A.A
1038 ADD A.E
1039 SRL A
1040 SRL A
1041 BIT B.A
1042 JR Z.CALCP1
1043 PUSH AP
1044 LO A.20H
1045 OR M
1046 LO M.A
1047 POP AP
1048 CALCP1 AND A
1049 ORA
1050 LO B.A
1051 LO A.(MARGIN)
1052 ADD A.B
1053 LO C.A
1054 LO B.B
1055 ADD M.BE
1056 LO C.7
1057 POP AP
1058 AND 3
1059 JR Z.CALCP2
1060 LO B.A
1061 ORC A
1062 CALCP2 ADD A.E
1063 LO (DPOIT),A
1064 RET
1065
1066
1067 LNSU
1068
1069 LO A.SCRSI
1070 SUB B
1071 LO B.A
1072 BRCA
1073 BRCA
1074 BRCA
1075 AND 0EJH
1076 LO L.A
1077 LO A.D
1078 AND 10H
1079 OR 40H
1080 LO M.A
1081 RET
1082
1083 STPSM
1084 LO (CURPCS),BC
1085 LO (CPADR3),HL
1086 RET
1087
1088 LCPCSM
1089 LO BC.(CURPCS)
1090 LO HL.(CPADR3)
1091 RET
1092
1093
1094
1095
1096
1097
1098 GYC2BC PUSH HL
1099 LO B.(HL)
1100 LO A.M
1101 XOR 20H
1102 LO M.A
1103 BIT 5.A
1104 JR NZ.GYC2B1
1105 INC HL
1106 GYC2B1 LO C.(HL)
1107 LO A.(DPOIT)
1108 SUB 7
1109 NEG
1110 JR Z.GYC2B2
1111 GYC2B2 RL C
1112 RL B
1113 ORC A
1114 JR NZ.GYC2B1
1115 GYC2B2 POP HL
1116 RET
1117
1118
1119 INCLUDE CMST00
1120 SUBTTL CHAR.SET FOR 40/80 COL.MODES

```

CONVERT

ENTER HERE WITH SC-LINE/COL.IN
INTERNAL FORMAT
CALCULATE POSITION

STORE UPDATED POSITION AND RETURN

ROUTINE TO CALCULATE POSITION IN DP
UPDATES BIT POSITION IN VARIABLE DPOIT
RETURNS DP ADDRESS IN HL. PRESERVES
LINE/COL.POSITION IN SC

GET DISPLAY FILE ADDR.FOR LINE.
TEST WHICH DP

SAVE COL.NO.(C-(LNLN-1))

(C-(C0-COL.PBS.) / 4 + 64-CCL.PCS.

000 COLS.ARE IN DP2

IN DP2

CLEAR CARRY
COL/2 IS POSITION IN DISPLAY FILE

GET ADJUSTMENT VALUE

DE CONTAINS OFFSET INTO LINE
HL CONTAINS DP ADDRESS

A=COL.PCS.FOR 00 COLS.

STARTS AT BIT 7
STARTS AT BIT 1,3 OR 5

COMPOS. HL=DP ADDRESS

GET DISPLAY FILE ADDRESS
FOR START CP LINE IN 0

STORE CURSOR POSITION

LOAD CURSOR POSITION

RTH. USED BY GYCHAR TO PUT
6 PIXELS FROM DP IN 8 REG.BITS 7-2
FOR MATCHING AGAINST CHAR.SET

SAVE DP ADDR.
SCAN LINE FROM DP

TOGGLE TO ALTERNATE DP
TEST WHICH DP

SC CONTAINS CHAR.AT DPOIT

A=NO.OF BITS OFFSET IN SC
STARTS AT BIT 7

SHIFT 6 PIXELS FOR CHARACTER
TO B, BITS 7-2

CHAR.IN B, BITS 7-2


```

1121
1122
1123 | Dot patterns for characters on the TV screen!
1124 | character with code x is at offsets 8x (top line) to 8x+7 (bottom line)
1125 | 0 = white, 1 = black, no margins between character spaces either
1126 | vertically or horizontally
1127
057E' 00 1128 CHRST defb 00070000b | code 10 0000
057F' 00 1129 defb 000C0000b
0580' 00 1130 defb 000C0000b
0581' 00 1131 defb 00090000b
0582' 00 1132 defb 000C0000b
0583' 00 1133 defb 000C0000b
0584' 00 1134 defb 000C0000b
0585' 00 1135 defb 000C0000b
1136
0586' 00 1137 defb 000C0000b | code 21 1
0587' 10 1138 defb 00010000b
0588' 10 1139 defb 00010000b
0589' 10 1140 defb 00010000b
058A' 10 1141 defb 00010000b
058B' 00 1142 defb 000C0000b
058C' 10 1143 defb 00010000b
058D' 00 1144 defb 03000000b
1145
058E' 00 1146 defb 00070000b | code 22 0
058F' 40 1147 defb 01001000b
0590' 40 1148 defb 01001000b
0591' 00 1149 defb 000C0000b
0592' 00 1150 defb 03000000b
0593' 00 1151 defb 03000000b
0594' 00 1152 defb 00000000b
0595' 00 1153 defb 00000000b
1154
0596' 00 1155 defb 000C0000b | code 23 0
0597' 20 1156 defb 001C1000b
0598' 7C 1157 defb 01111100b
0599' 20 1158 defb 001C1000b
059A' 20 1159 defb 001C1000b
059B' 7C 1160 defb 01111100b
059C' 20 1161 defb 00101000b
059D' 00 1162 defb 00000000b
1163
059E' 00 1164 defb 00000000b | code 24 0
059F' 10 1165 defb 00010000b
05A0' 7C 1166 defb 01111100b
05A1' 50 1167 defb 01000000b
05A2' 7C 1168 defb 01111100b
05A3' 14 1169 defb 00C10100b
05A4' 7C 1170 defb 01111100b
05A5' 10 1171 defb 00010000b
1172
05A6' 00 1173 defb 000C0000b | code 25 1
05A7' 44 1174 defb 0110C100b
05A8' 00 1175 defb 0110C000b
05A9' 10 1176 defb 00010000b
05AA' 10 1177 defb 00010000b
05AB' 2C 1178 defb 00101000b
05AC' 4C 1179 defb 01001000b
05AD' 00 1180 defb 00000000b
1181
05AE' 00 1182 defb 00000000b | code 26 0
05AF' 10 1183 defb 00010000b
05B0' 20 1184 defb 00101000b
05B1' 10 1185 defb 00010000b
05B2' 2C 1186 defb 00101000b
05B3' 40 1187 defb 01001000b
05B4' 34 1188 defb 00110100b
05B5' 00 1189 defb 00000000b
1190
05B6' 00 1191 defb 00070000b | code 27 0
05B7' 10 1192 defb 00010000b
05B8' 20 1193 defb 001C0000b
05B9' 00 1194 defb 000C0000b
05BA' 00 1195 defb 00000000b
05BB' 00 1196 defb 00000000b
05BC' 00 1197 defb 00000000b
05BD' 30 1198 defb 00000000b
1199
05BE' 00 1200 defb 00000000b | code 28 0
05BF' 00 1201 defb 00010000b
05C0' 10 1202 defb 00010000b
05C1' 10 1203 defb 00010000b
05C2' 10 1204 defb 00010000b
05C3' 10 1205 defb 00010000b
05C4' 00 1206 defb 00010000b
05C5' 00 1207 defb 00000000b
1208
05C6' 00 1209 defb 000C0000b | code 29 0
05C7' 20 1210 defb 001C0000b
05C8' 10 1211 defb 00010000b
05C9' 10 1212 defb 00010000b
05CA' 10 1213 defb 00010000b
05CB' 10 1214 defb 00010000b
05CC' 20 1215 defb 00100000b
05CD' 00 1216 defb 00000000b
1217
05CE' 00 1218 defb 00000000b | code 2A 0
05CF' 00 1219 defb 00000000b
05D0' 20 1220 defb 00100000b
05D1' 10 1221 defb 00010000b
05D2' 7C 1222 defb 01111100b
05D3' 10 1223 defb 00000000b
05D4' 20 1224 defb 00101000b
05D5' 00 1225 defb 00000000b
1226
05D6' 00 1227 defb 00000000b | code 2B 0
05D7' 00 1228 defb 00000000b
05D8' 10 1229 defb 03000000b
05D9' 10 1230 defb 00010000b
05DA' 7C 1231 defb 01111100b

```

0500	10	1232	defb 00010000b		
050C	10	1233	defb 00010300b		
050E	00	1234	defb 00002000b		
		1235			
050F	00	1236	defb 000C0000b	I code 2C	.
0500	00	1237	defb 002C5000b		
0500	00	1238	defb 000C0000b		
0501	00	1239	defb 00002000b		
0502	00	1240	defb 000C0000b		
0503	10	1241	defb 00010000b		
0504	10	1242	defb 00010000b		
0505	20	1243	defb 00100000b		
		1244			
0506	00	1245	defb 00000000b	I code 2D	-
0507	00	1246	defb 000C0000b		
0508	00	1247	defb 000C0000b		
0509	00	1248	defb 000C0000b		
050A	7C	1249	defb 01111000b		
050B	00	1250	defb 00000000b		
050C	00	1251	defb 00000000b		
050D	00	1252	defb 00000000b		
		1253			
050E	00	1254	defb 000C0000b	I code 2E	.
050F	00	1255	defb 00000000b		
0510	00	1256	defb 00000000b		
0511	00	1257	defb 00000000b		
0512	00	1258	defb 000C0000b		
0513	3C	1259	defb 00110000b		
0514	32	1260	defb 00110000b		
0515	00	1261	defb 000C0000b		
		1262			
0516	00	1263	defb 000C0000b	I code 2F	/
0517	00	1264	defb 00000000b		
0518	04	1265	defb 00C00100b		
0519	00	1266	defb 00000000b		
051A	10	1267	defb 00010000b		
051B	20	1268	defb 00100000b		
051C	40	1269	defb 01000000b		
051D	00	1270	defb 00000000b		
		1271			
051E	00	1272	defb 00000000b	I code 30	0
051F	30	1273	defb 00110000b		
0600	4C	1274	defb 01001000b		
0601	04	1275	defb 01010100b		
0602	04	1276	defb 01010100b		
0603	64	1277	defb 01100100b		
0604	38	1278	defb 00110000b		
0605	00	1279	defb 000C0000b		
		1280			
0606	00	1281	defb 00000000b	I code 31	1
0607	30	1282	defb 00110000b		
0608	38	1283	defb 01010000b		
		1284	defb 00010000b		
0609	10	1285	defb 00010000b		
060A	10	1286	defb 00010000b		
060B	10	1287	defb 01111000b		
060C	7C	1288	defb 00000000b		
060D	00	1289			
		1290	defb 00000000b	I code 32	2
060E	00	1291	defb 00110000b		
060F	30	1292	defb 01000100b		
0610	44	1293	defb 00000000b		
0611	04	1294	defb 00110000b		
0612	30	1295	defb 01000000b		
0613	40	1296	defb 01111000b		
0614	7C	1297	defb 00000000b		
0615	00	1298			
		1299	defb 00000000b	I code 33	3
0616	00	1300	defb 00110000b		
0617	30	1301	defb 01000100b		
0618	44	1302	defb 00010000b		
0619	10	1303	defb 00000000b		
061A	04	1304	defb 01000100b		
061B	44	1305	defb 00110000b		
061C	38	1306	defb 00000000b		
061D	00	1307			
		1308	defb 000C0000b	I code 34	4
061E	00	1309	defb 00000000b		
0620	10	1310	defb 00010000b		
0621	20	1311	defb 001C0000b		
0622	40	1312	defb 01000000b		
0623	7C	1313	defb 01111000b		
0624	00	1314	defb 00000000b		
0625	00	1315	defb 00000000b		
		1316			
0626	00	1317	defb 00000000b	I code 35	5
0627	7C	1318	defb 01111000b		
0628	40	1319	defb 01000000b		
0629	78	1320	defb 01111000b		
062A	04	1321	defb 00000100b		
062B	44	1322	defb 01000100b		
062C	38	1323	defb 00110000b		
062D	00	1324	defb 000C0000b		
		1325			
062E	00	1326	defb 00000000b	I code 36	6
062F	30	1327	defb 00110000b		
0630	40	1328	defb 01000000b		
0631	78	1329	defb 01111000b		
0632	44	1330	defb 010C0100b		
0633	44	1331	defb 010CC100b		
0634	38	1332	defb 00110000b		
0635	00	1333	defb 00000000b		
		1334			
0636	00	1335	defb 00000000b	I code 37	7

0637	7C	1336	defb 01111100b		
0638	04	1337	defb 00001000b		
0639	08	1338	defb 00001000b		
063A	10	1339	defb 00010000b		
063B	20	1340	defb 00100000b		
063C	28	1341	defb 00100000b		
063D	00	1342	defb 00000000b		
		1343			
063E	00	1344	defb 00000000b	I code 38	8
063F	38	1345	defb 00111000b		
0640	44	1346	defb 01001000b		
0641	38	1347	defb 00111000b		
0642	44	1348	defb 01001000b		
0643	44	1349	defb 01001000b		
0644	38	1350	defb 00111000b		
0645	00	1351	defb 00000000b		
		1352			
0646	00	1353	defb 00000000b	I code 39	9
0647	38	1354	defb 00111000b		
0648	44	1355	defb 01001000b		
0649	44	1356	defb 01001000b		
064A	3C	1357	defb 00111000b		
064B	04	1358	defb 00000000b		
064C	38	1359	defb 00111000b		
064D	00	1360	defb 00000000b		
		1361			
064E	00	1362	defb 00000000b	I code 3A	A
064F	00	1363	defb 00000000b		
0650	00	1364	defb 00000000b		
0651	10	1365	defb 00010000b		
0652	00	1366	defb 00000000b		
0653	00	1367	defb 00000000b		
0654	10	1368	defb 00010000b		
0655	00	1369	defb 00000000b		
		1370			
0656	00	1371	defb 00000000b	I code 3B	B
0657	00	1372	defb 00000000b		
0658	1C	1373	defb 00C10000b		
0659	00	1374	defb 00000000b		
065A	00	1375	defb 00100000b		
065B	10	1376	defb 00010000b		
065C	10	1377	defb 00010000b		
065D	20	1378	defb 00100000b		
		1379			
065E	00	1380	defb 00000000b	I code 3C	C
065F	00	1381	defb 00000000b		
0660	00	1382	defb 00000000b		
0661	10	1383	defb 00010000b		
0662	20	1384	defb 00100000b		
0663	10	1385	defb 00010000b		
0664	00	1386	defb 00000000b		
0665	00	1387	defb 00000000b		
		1388			
0666	00	1389	defb 00000000b	I code 3D	D
0667	00	1390	defb 00000000b		
0668	00	1391	defb 00000000b		
0669	7C	1392	defb 01111100b		
066A	00	1393	defb 00000000b		
066B	7C	1394	defb 01111100b		
066C	00	1395	defb 00000000b		
066D	00	1396	defb 00000000b		
		1397			
066E	00	1398	defb 00000000b	I code 3E	E
066F	00	1399	defb 00000000b		
0670	20	1400	defb 00100000b		
0671	10	1401	defb 00010000b		
0672	00	1402	defb 00000000b		
0673	10	1403	defb 00010000b		
0674	20	1404	defb 00100000b		
0675	00	1405	defb 00000000b		
		1406			
0676	00	1407	defb 00000000b	I code 3F	F
0677	38	1408	defb 00111000b		
0678	44	1409	defb 01001000b		
0679	00	1410	defb 00000000b		
067A	10	1411	defb 00010000b		
067B	00	1412	defb 00000000b		
067C	10	1413	defb 00010000b		
067D	00	1414	defb 00000000b		
		1415			
067E	00	1416	defb 00000000b	I code 40	0
067F	38	1417	defb 00111000b		
0680	44	1418	defb 01001000b		
0681	3C	1419	defb 01011000b		
0682	3C	1420	defb 01011000b		
0683	48	1421	defb 01000000b		
0684	3C	1422	defb 00111000b		
0685	00	1423	defb 00000000b		
		1424			
0686	00	1425	defb 00000000b	I code 41	1
0687	38	1426	defb 00111000b		
0688	44	1427	defb 01001000b		
0689	44	1428	defb 01001000b		
068A	7C	1429	defb 01111100b		
068B	44	1430	defb 01001000b		
068C	44	1431	defb 01001000b		
068D	00	1432	defb 00000000b		
		1433			
068E	00	1434	defb 00000000b	I code 42	2
068F	78	1435	defb 01111000b		
0690	44	1436	defb 01001000b		
0691	78	1437	defb 01111000b		
0692	44	1438	defb 01001000b		
0693	44	1439	defb 01001000b		

0694	78	1440	defb 01111000b		
0695	00	1441	defb 00000000b		
		1442			
0696	00	1443	defb 00000000b	I code 43	C
0697	30	1444	defb 00111000b		
0698	44	1445	defb 01C01000b		
0699	40	1446	defb 01000000b		
069A	40	1447	defb 01C00000b		
069B	44	1448	defb 01000100b		
069C	30	1449	defb 00111000b		
069D	00	1450	defb 00000000b		
		1451			
069E	00	1452	defb 00000000b	I code 44	D
069F	70	1453	defb 01111000b		
06A0	40	1454	defb 01C01000b		
06A1	44	1455	defb 01000100b		
06A2	44	1456	defb 01C00100b		
06A3	40	1457	defb 01000000b		
06A4	70	1458	defb 01111000b		
06A5	00	1459	defb 00000000b		
		1460			
06A6	00	1461	defb 00000000b	I code 45	E
06A7	7C	1462	defb 01111100b		
06A8	40	1463	defb 01000000b		
06A9	70	1464	defb 01111000b		
06AA	40	1465	defb 01000000b		
06AB	40	1466	defb 01000000b		
06AC	7C	1467	defb 01111100b		
06AD	00	1468	defb 00000000b		
		1469			
06AE	00	1470	defb 00000000b	I code 46	F
06AF	7C	1471	defb 01111100b		
06B0	40	1472	defb 01000000b		
06B1	70	1473	defb 01111000b		
06B2	40	1474	defb 01000000b		
06B3	40	1475	defb 01000000b		
06B4	40	1476	defb 01000000b		
06B5	00	1477	defb 00000000b		
		1478			
06B6	00	1479	defb 00000000b	I code 47	G
06B7	30	1480	defb 00111000b		
06B8	44	1481	defb 01000100b		
06B9	40	1482	defb 01000000b		
06BA	3C	1483	defb 01000100b		
06BB	44	1484	defb 01000100b		
06BC	30	1485	defb 00111000b		
06BD	00	1486	defb 00000000b		
		1487			
06BE	00	1488	defb 00000000b	I code 48	H
06BF	44	1489	defb 01000100b		
06C0	44	1490	defb 01C00100b		
06C1	7C	1491	defb 01111100b		
		1492			
06C2	44	1493	defb 01000100b		
06C3	44	1494	defb 01000100b		
06C4	44	1495	defb 01000100b		
06C5	00	1496	defb 00000000b		
		1497			
06C6	00	1498	defb 00000000b	I code 49	I
06C7	7C	1499	defb 01111100b		
06C8	10	1500	defb 00010000b		
06C9	10	1501	defb 00010000b		
06CA	10	1502	defb 00010000b		
06CB	10	1503	defb 00010000b		
06CC	7C	1504	defb 01111100b		
06CD	00	1505	defb 00000000b		
		1506			
06CE	00	1507	defb 00000000b	I code 4A	J
06CF	04	1508	defb 00001000b		
06D0	04	1509	defb 00001000b		
06D1	04	1510	defb 00001000b		
06D2	44	1511	defb 01000100b		
06D3	44	1512	defb 00111100b		
06D4	3C	1513	defb 00000000b		
06D5	00	1514			
		1515			
06D6	00	1516	defb 00000000b	I code 4B	K
06D7	40	1517	defb 01001000b		
06D8	30	1518	defb 01010000b		
06D9	40	1519	defb 01100000b		
06DA	30	1520	defb 01010000b		
06DB	48	1521	defb 01001000b		
06DC	44	1522	defb 01000100b		
06DD	00	1523	defb 00000000b		
		1524			
06DE	00	1525	defb 00000000b	I code 4C	L
06DF	40	1526	defb 01000000b		
06E0	40	1527	defb 01000000b		
06E1	40	1528	defb 01000000b		
06E2	40	1529	defb 01000000b		
06E3	40	1530	defb 01111100b		
06E4	7C	1531	defb 00000000b		
06E5	00	1532			
		1533			
06E6	00	1534	defb 00000000b	I code 4D	M
06E7	44	1535	defb 01000100b		
06E8	6C	1536	defb 01101100b		
06E9	34	1537	defb 01010100b		
06EA	44	1538	defb 01000100b		
06EB	44	1539	defb 01000100b		
06EC	44	1540	defb 01000100b		
06ED	00	1541	defb 00000000b		
		1542			
06EE	C0	1543	defb 00000000b	I code 4E	N
06EF	44		defb 01000100b		

06C2*	44	1492	00fb 01300100b		
06C3*	44	1493	00fb 01002100b		
06C4*	44	1494	00fb 01302100b		
06C5*	00	1495	00fb 00000000b		
06C6*	00	1496	00fb 00000000b	I code 43	C
06C7*	7C	1497	00fb 01111100b		
06C8*	10	1498	00fb 00010000b		
06C9*	10	1499	00fb 00010000b		
06CA*	10	1500	00fb 00010000b		
06CB*	10	1501	00fb 00012000b		
06CC*	10	1502	00fb 00010000b		
06CD*	7C	1503	00fb 01111100b		
06CE*	00	1504	00fb 00000000b		
06CF*	00	1505	00fb 00000000b	I code 44	D
06D0*	04	1506	00fb 00000100b		
06D1*	04	1507	00fb 00000100b		
06D2*	04	1508	00fb 00000100b		
06D3*	44	1509	00fb 00000100b		
06D4*	44	1510	00fb 01000100b		
06D5*	44	1511	00fb 01000100b		
06D6*	3C	1512	00fb 00111100b		
06D7*	00	1513	00fb 00000000b		
06D8*	00	1514	00fb 00300000b	I code 45	E
06D9*	44	1515	00fb 01001700b		
06DA*	30	1516	00fb 01012000b		
06DB*	00	1517	00fb 01100700b		
06DC*	30	1518	00fb 01010000b		
06DD*	44	1519	00fb 01010000b		
06DE*	44	1520	00fb 01021000b		
06DF*	44	1521	00fb 01021000b		
06E0*	00	1522	00fb 00000000b		
06E1*	00	1523	00fb 00000000b		
06E2*	00	1524	00fb 00000000b	I code 46	F
06E3*	40	1525	00fb 01022000b		
06E4*	40	1526	00fb 01023000b		
06E5*	40	1527	00fb 01023000b		
06E6*	40	1528	00fb 01023000b		
06E7*	40	1529	00fb 01023000b		
06E8*	7C	1530	00fb 01111100b		
06E9*	00	1531	00fb 00000000b		
06EA*	00	1532	00fb 00000000b	I code 47	G
06EB*	44	1533	00fb 01000100b		
06EC*	6C	1534	00fb 01101100b		
06ED*	34	1535	00fb 01010100b		
06EE*	44	1536	00fb 01020100b		
06EF*	44	1537	00fb 01020100b		
06F0*	44	1538	00fb 01020100b		
06F1*	44	1539	00fb 01020100b		
06F2*	00	1540	00fb 00000000b		
06F3*	00	1541	00fb 00000000b	I code 48	H
06F4*	00	1542	00fb 00000000b		
06F5*	44	1543	00fb 01000100b		
06F6*	44	1544	00fb 01100100b		
06F7*	34	1545	00fb 01010100b		
06F8*	4C	1546	00fb 01001000b		
06F9*	44	1547	00fb 01001000b		
06FA*	44	1548	00fb 01001000b		
06FB*	00	1549	00fb 00000000b		
06FC*	00	1550	00fb 00000000b		
06FD*	00	1551	00fb 00000000b	I code 49	I
06FE*	30	1552	00fb 00111000b		
06FF*	44	1553	00fb 01000100b		
0700*	44	1554	00fb 01000100b		
0701*	44	1555	00fb 01000100b		
0702*	44	1556	00fb 01000100b		
0703*	44	1557	00fb 01000100b		
0704*	44	1558	00fb 01000100b		
0705*	00	1559	00fb 00000000b	I code 50	P
0706*	00	1560	00fb 01111000b		
0707*	30	1561	00fb 01111000b		
0708*	44	1562	00fb 01000100b		
0709*	44	1563	00fb 01000100b		
070A*	44	1564	00fb 01111000b		
070B*	44	1565	00fb 01000100b		
070C*	30	1566	00fb 01000100b		
070D*	00	1567	00fb 00000000b		
070E*	00	1568	00fb 00000000b		
070F*	00	1569	00fb 00000000b	I code 51	Q
0710*	30	1570	00fb 00111000b		
0711*	44	1571	00fb 01000100b		
0712*	44	1572	00fb 01000100b		
0713*	44	1573	00fb 01000100b		
0714*	34	1574	00fb 01010100b		
0715*	30	1575	00fb 00111000b		
0716*	00	1576	00fb 00000000b		
0717*	00	1577	00fb 00000000b		
0718*	00	1578	00fb 00000000b	I code 52	R
0719*	70	1579	00fb 01111000b		
071A*	44	1580	00fb 01000100b		
071B*	44	1581	00fb 01000100b		
071C*	70	1582	00fb 01111000b		
071D*	40	1583	00fb 01000100b		
071E*	44	1584	00fb 01000100b		
071F*	00	1585	00fb 00000000b		
0720*	00	1586	00fb 00000000b	I code 53	S
0721*	30	1587	00fb 00111000b		
0722*	40	1588	00fb 01000100b		
0723*	30	1589	00fb 00111000b		
0724*	04	1590	00fb 00000100b		
0725*	44	1591	00fb 01000100b		
0726*	30	1592	00fb 01000100b		
0727*	00	1593	00fb 00111000b		
0728*	00	1594	00fb 00000000b		
0729*	00	1595	00fb 00000000b		

0710	00	1596	defb 00000000	I code 94	T
0711	7C	1597	defb 01111000		
0720	10	1598	defb 00010000		
0721	10	1599	defb 00010000		
0722	10	1600	defb 00010000		
0723	10	1601	defb 00010000		
0724	10	1602	defb 00010000		
0725	00	1603	defb 00000000		
		1604			
0726	00	1605	defb 00000000	I code 95	U
0727	44	1606	defb 01000100		
0728	44	1607	defb 01000100		
0729	44	1608	defb 01000100		
072A	44	1609	defb 01000100		
072B	44	1610	defb 01000100		
072C	30	1611	defb 00111000		
072D	00	1612	defb 00000000		
		1613			
072E	00	1614	defb 00000000	I code 96	V
072F	44	1615	defb 01000100		
0730	44	1616	defb 01000100		
0731	44	1617	defb 01000100		
0732	44	1618	defb 01000100		
0733	20	1619	defb 00101000		
0734	10	1620	defb 00010000		
0735	00	1621	defb 00000000		
		1622			
0736	00	1623	defb 02000000	I code 97	W
0737	44	1624	defb 01000100		
0738	44	1625	defb 01000100		
0739	44	1626	defb 01000100		
073A	54	1627	defb 01010100		
073B	54	1628	defb 01010100		
073C	20	1629	defb 00101000		
073D	00	1630	defb 00000000		
		1631			
073E	00	1632	defb 00000000	I code 98	X
073F	44	1633	defb 01000100		
0740	20	1634	defb 00101000		
0741	10	1635	defb 00010000		
0742	10	1636	defb 00010000		
0743	20	1637	defb 00101000		
0744	44	1638	defb 01000100		
0745	00	1639	defb 00000000		
		1640			
0746	00	1641	defb 00000000	I code 99	Y
0747	44	1642	defb 01000100		
0748	44	1643	defb 01000100		
0749	20	1644	defb 00101000		
074A	1C	1645	defb 02000000		
074B	10	1646	defb 00010000		
074C	10	1647	defb 00010000		
		1648			
074D	00	1649	defb 00000000	I code 9A	Z
074E	7C	1650	defb 01111000		
0750	04	1651	defb 00000100		
0751	00	1652	defb 00000000		
0752	10	1653	defb 00010000		
0753	20	1654	defb 00100000		
0754	7C	1655	defb 01111000		
0755	00	1656	defb 00000000		
		1657			
0756	00	1658	defb 00000000	I code 9B	[
0757	30	1659	defb 00110000		
0758	20	1661	defb 00100000		
0759	20	1662	defb 00100000		
075A	20	1663	defb 00100000		
075B	20	1664	defb 00100000		
075C	30	1665	defb 00110000		
075D	00	1666	defb 00000000		
		1667			
075E	00	1668	defb 00000000	I code 9C	\
075F	00	1669	defb 00000000		
0760	40	1670	defb 01000000		
0761	20	1671	defb 00100000		
0762	10	1672	defb 00010000		
0763	00	1673	defb 00000000		
0764	04	1674	defb 00000000		
0765	00	1675	defb 00000000		
		1676			
0766	00	1677	defb 00000000	I code 9D]
0767	70	1678	defb 01110000		
0768	10	1679	defb 00010000		
0769	10	1680	defb 00010000		
076A	10	1681	defb 00010000		
076B	10	1682	defb 00010000		
076C	70	1683	defb 01110000		
076D	00	1684	defb 00000000		
		1685			
076E	00	1686	defb 00000000	I code 9E	^
076F	10	1687	defb 00010000		
0770	30	1688	defb 00110000		
0771	04	1689	defb 01010100		
0772	10	1690	defb 00010000		
0773	10	1691	defb 00010000		
0774	10	1692	defb 00010000		
0775	00	1693	defb 00000000		
		1694			
0776	00	1695	defb 00000000	I code 9F	_
0777	00	1696	defb 00000000		
0778	00	1697	defb 00000000		
0779	00	1698	defb 00000000		
077A	00	1699	defb 00000000		

0776	00	1700	defb 0000000b		
077C	00	1701	defb 0000000b		
077D	FC	1702	defb 11111100b		
		1703			
077E	00	1704	defb 0000000b	i code 60	round sign
077F	10	1705	defb 0011000b		
0780	24	1706	defb 00100100b		
0781	70	1707	defb 01110000b		
0782	20	1708	defb 00100000b		
0783	20	1709	defb 00100000b		
0784	7C	1710	defb 01111100b		
0785	00	1711	defb 0000000b		
		1712			
0786	00	1713	defb 0000000b	i code 61	a
0787	00	1714	defb 0000000b		
0788	34	1715	defb 00111000b		
0789	04	1716	defb 00000100b		
078A	3C	1717	defb 00111100b		
078B	44	1718	defb 01000100b		
078C	3C	1719	defb 00111100b		
078D	00	1720	defb 0000000b		
		1721			
078E	00	1722	defb 0000000b	i code 62	b
078F	20	1723	defb 00100000b		
0790	20	1724	defb 00100000b		
0791	38	1725	defb 00111000b		
0792	24	1726	defb 00100100b		
0793	24	1727	defb 00100100b		
0794	38	1728	defb 00111000b		
0795	00	1729	defb 0000000b		
		1730			
0796	00	1731	defb 0000000b	i code 63	c
0797	00	1732	defb 0000000b		
0798	1C	1733	defb 00111000b		
0799	20	1734	defb 00100000b		
079A	20	1735	defb 00100000b		
079B	20	1736	defb 00100000b		
079C	1C	1737	defb 00111000b		
079D	00	1738	defb 0000000b		
		1739			
079E	00	1740	defb 0000000b	i code 64	d
079F	08	1741	defb 0001000b		
07A0	08	1742	defb 0001000b		
07A1	38	1743	defb 00111000b		
07A2	48	1744	defb 01001000b		
07A3	48	1745	defb 01001000b		
07A4	38	1746	defb 00111000b		
07A5	00	1747	defb 0000000b		
		1748			
07A6	20	1749	defb 0000000b	i code 65	e
07A7	00	1750	defb 0000000b		
07A8	38	1751	defb 00111000b		
		1752			
07A9	44	1753	defb 01000100b		
07AA	78	1754	defb 01111000b		
07AB	40	1755	defb 01000000b		
07AC	3C	1756	defb 00111000b		
07AD	00	1757	defb 0000000b		
		1758			
07AE	00	1759	defb 0000000b	i code 66	f
07AF	10	1760	defb 0001000b		
07B0	28	1761	defb 00110000b		
07B1	70	1762	defb 00100000b		
07B2	20	1763	defb 00100000b		
07B3	20	1764	defb 00100000b		
07B4	20	1765	defb 0000000b		
07B5	00	1766			
		1767			
07B6	00	1768	defb 0000000b	i code 67	g
07B7	00	1769	defb 0000000b		
07B8	38	1770	defb 00111000b		
07B9	48	1771	defb 01001000b		
07BA	48	1772	defb 00111000b		
07BB	38	1773	defb 0001000b		
07BC	08	1774	defb 00110000b		
07BD	30	1775			
		1776			
07BE	00	1777	defb 0000000b	i code 68	h
07BF	40	1778	defb 01000000b		
07C0	40	1779	defb 01000000b		
07C1	70	1780	defb 01110000b		
07C2	48	1781	defb 01001000b		
07C3	48	1782	defb 01001000b		
07C4	48	1783	defb 0000000b		
07C5	00	1784			
		1785			
07C6	00	1786	defb 0000000b	i code 69	i
07C7	10	1787	defb 0001000b		
07C8	00	1788	defb 0000000b		
07C9	30	1789	defb 00110000b		
07CA	10	1790	defb 0001000b		
07CB	10	1791	defb 0001000b		
07CC	38	1792	defb 00111000b		
07CD	00	1793	defb 0000000b		
		1794			
07CE	00	1795	defb 0000000b	i code 6A	j
07CF	08	1796	defb 0001000b		
07D0	00	1797	defb 0000000b		
07D1	08	1798	defb 0001000b		
07D2	08	1799	defb 0001000b		
07D3	08	1800	defb 0001000b		
07D4	48	1801	defb 01001000b		
07D5	30	1802	defb 00110000b		
		1803			
07D6	00	1803	defb 0000000b	i code 6B	k

0707*	20	1004	defb 00177000		
0708*	20	1005	defb 00178000		
0709*	30	1006	defb 00179000		
070A*	30	1007	defb 0017A000		
070B*	20	1008	defb 0017B000		
070C*	24	1009	defb 0017C000		
070D*	00	1010	defb 00020000		
		1011			
070E*	00	1012	defb 00030000	I code 6C	1
070F*	20	1013	defb 00170000		
0710*	20	1014	defb 001C0000		
0711*	20	1015	defb 001D0000		
0712*	20	1016	defb 001E0000		
0713*	20	1017	defb 001F0000		
0714*	10	1018	defb 00010000		
0715*	00	1019	defb 000E0000		
		1020			
0716*	00	1021	defb 00050000	I code 6D	"
0717*	00	1022	defb 000C0000		
0718*	20	1023	defb 00170000		
0719*	54	1024	defb 0101C000		
071A*	54	1025	defb 0101D000		
071B*	54	1026	defb 0101E000		
071C*	54	1027	defb 0101F000		
071D*	00	1028	defb 00CC0000		
		1029			
071E*	00	1030	defb 000C0000	I code 6E	"
071F*	00	1031	defb 000C0000		
0720*	70	1032	defb 01170000		
0721*	40	1033	defb 01001000		
0722*	40	1034	defb 01002000		
0723*	40	1035	defb 01003000		
0724*	40	1036	defb 01004000		
0725*	00	1037	defb 000C0000		
		1038			
0726*	00	1039	defb 000C0000	I code 6F	0
0727*	00	1040	defb 000C0000		
0728*	30	1041	defb 00111000		
0729*	44	1042	defb 01007000		
072A*	44	1043	defb 01008000		
072B*	44	1044	defb 01009000		
072C*	30	1045	defb 00111000		
072D*	00	1046	defb 00000000		
		1047			
072E*	00	1048	defb 00000000	I code 70	"
072F*	00	1049	defb 000C0000		
0000*	70	1050	defb 01117000		
0001*	40	1051	defb 01001000		
0002*	40	1052	defb 01002000		
0003*	70	1053	defb 01100000		
0004*	40	1054	defb 01000000		
0005*	40	1055	defb 010C0000		
		1056			
0006*	00	1057	defb 00000000	I code 71	"
0007*	00	1058	defb 00000000		
0008*	30	1059	defb 00111000		
0009*	40	1060	defb 01001000		
000A*	40	1061	defb 01002000		
000B*	30	1062	defb 00111000		
000C*	00	1063	defb 00001000		
000D*	0C	1064	defb 00001000		
		1065			
000E*	00	1066	defb 00000000	I code 72	"
000F*	00	1067	defb 00000000		
0010*	10	1068	defb 00011000		
0011*	20	1069	defb 00100000		
0012*	20	1070	defb 00100000		
0013*	20	1071	defb 00100000		
0014*	20	1072	defb 00100000		
0015*	00	1073	defb 00000000		
		1074			
0016*	00	1075	defb 00000000	I code 73	0
0017*	00	1076	defb 00000000		
0018*	30	1077	defb 00111000		
0019*	40	1078	defb 01002000		
001A*	30	1079	defb 00110000		
001B*	00	1080	defb 00001000		
001C*	70	1081	defb 01110000		
001D*	0C	1082	defb 00000000		
		1083			
001E*	00	1084	defb 00000000	I code 74	1
001F*	20	1085	defb 00100000		
0020*	70	1086	defb 01110000		
0021*	20	1087	defb 00100000		
0022*	20	1088	defb 00100000		
0023*	20	1089	defb 00100000		
0024*	10	1090	defb 00011000		
0025*	00	1091	defb 00000000		
		1092			
0026*	00	1093	defb 00000000	I code 75	"
0027*	00	1094	defb 00000000		
0028*	40	1095	defb 01001000		
0029*	40	1096	defb 01001000		
002A*	40	1097	defb 01001000		
002B*	40	1098	defb 01001000		
002C*	30	1099	defb 00110000		
002D*	00	1000	defb 00000000		
		1001			
002E*	00	1002	defb 00000000	I code 76	"
002F*	00	1003	defb 00030000		
0030*	44	1004	defb 0100C000		
0031*	44	1005	defb 0100D000		
0032*	20	1006	defb 00101000		
0033*	20	1007	defb 00101000		

083A	10	1908	defb 00C1C000b		
0833	00	1909	defb 00000000b		
		1910			
0836	00	1911	defb 00000000b	I code 77	u
0837	00	1912	defb 00000000b		
0838	44	1913	defb 01000000b		
0839	54	1914	defb 01000000b		
083A	54	1915	defb 01000000b		
083B	54	1916	defb 01000000b		
083C	28	1917	defb 00100000b		
083D	00	1918	defb 00000000b		
		1919			
083E	00	1920	defb 00000000b	I code 78	x
083F	00	1921	defb 00000000b		
0840	44	1922	defb 01000000b		
0841	28	1923	defb 00100000b		
0842	10	1924	defb 00000000b		
0843	28	1925	defb 00100000b		
0844	44	1926	defb 01000000b		
0845	00	1927	defb 00000000b		
		1928			
0846	00	1929	defb 00000000b	I code 79	y
0847	00	1930	defb 00000000b		
0848	44	1931	defb 01000000b		
0849	44	1932	defb 01000000b		
084A	44	1933	defb 01000000b		
084B	38	1934	defb 00100000b		
084C	04	1935	defb 00000000b		
084D	70	1936	defb 01100000b		
		1937			
084E	00	1938	defb 00000000b	I code 7A	z
084F	00	1939	defb 00000000b		
0850	7C	1940	defb 01110000b		
0851	00	1941	defb 00000000b		
0852	1C	1942	defb 00000000b		
0853	20	1943	defb 00100000b		
0854	7C	1944	defb 01110000b		
0855	00	1945	defb 00000000b		
		1946			
0856	00	1947	defb 00000000b	I code 7B	{
0857	1C	1948	defb 00000000b		
0858	10	1949	defb 00000000b		
0859	60	1950	defb 01100000b		
085A	10	1951	defb 00000000b		
085B	10	1952	defb 00000000b		
085C	1C	1953	defb 00000000b		
085D	00	1954	defb 00000000b		
		1955			
085E	00	1956	defb 00000000b	I code 7C	
085F	10	1957	defb 00000000b		
0860	10	1958	defb 00000000b		
0861	10	1959	defb 00000000b		
		1960			
0862	10	1961	defb 00000000b		
0863	18	1962	defb 00000000b		
0864	10	1963	defb 00000000b		
0865	00	1964			
		1965	defb 00000000b	I code 7D	}
0866	00	1966	defb 01100000b		
0867	70	1967	defb 00000000b		
0868	18	1968	defb 00000000b		
0869	08	1969	defb 00000000b		
086A	10	1970	defb 00000000b		
086B	10	1971	defb 01100000b		
086C	70	1972	defb 00000000b		
086D	00	1973			
		1974	defb 00000000b	I code 7E	~
086E	00	1975	defb 00000000b		
086F	28	1976	defb 01000000b		
0870	50	1977	defb 00000000b		
0871	00	1978	defb 00000000b		
0872	00	1979	defb 00000000b		
0873	00	1980	defb 00000000b		
0874	00	1981	defb 00000000b		
0875	00	1982			
		1983	defb 01110000b	I code 7F	COPYRIGHT
0876	78	1984	defb 10000000b		
0877	84	1985	defb 10000000b		
0878	94	1986	defb 10000000b		
0879	44	1987	defb 10000000b		
087A	44	1988	defb 10000000b		
087B	94	1989	defb 10000000b		
087C	84	1990	defb 10000000b		
087D	78	1991	defb 01110000b		
		1992	defb 00000000b	I code 80	graphics space
087E	00	1993	defb 00000000b		
087F	00	1994	defb 00000000b		
0880	00	1995	defb 00000000b		
0881	00	1996	defb 00000000b		
0882	0C	1997	defb 00000000b		
0883	8C	1998	defb 00000000b		
0884	00	1999			
		2000			
0886	1C	2001	defb 00011000b	I code 81	graphics
0887	1C	2002	defb 00011000b		
0888	1C	2003	defb 00011000b		
0889	1C	2004	defb 00011000b		
088A	00	2005	defb 00000000b		
088B	00	2006	defb 00000000b		
088C	00	2007	defb 00000000b		
088D	00	2008	defb 00000000b		
		2009			
		2010			
088E	8C	2011	defb 11100000b	I code 82	graphics

000F	EO	2012	defb 11100000b		
0090	EO	2013	defb 11100000b		
0091	EO	2014	defb 11100000b		
0092	EO	2015	defb 00000000b		
0093	EO	2016	defb 00000000b		
0094	EO	2017	defb 00000000b		
0095	EO	2018	defb 00000000b		
0096	PC	2019			
0097	PC	2020	defb 11111100b	I code 83	graphics
0098	PC	2021	defb 11111100b		
0099	PC	2022	defb 11111100b		
009A	EO	2023	defb 11111100b		
009B	EO	2024	defb 00000000b		
009C	EO	2025	defb 00000000b		
009D	EO	2026	defb 00000000b		
009E	EO	2027	defb 00000000b		
009F	EO	2028			
00A0	EO	2029			
00A1	EO	2030	defb 00000000b	I code 84	graphics
00A2	EO	2031	defb 00000000b		
00A3	EO	2032	defb 00000000b		
00A4	EO	2033	defb 00000000b		
00A5	EO	2034	defb 00011100b		
00A6	EO	2035	defb 00011100b		
00A7	EO	2036	defb 00011100b		
00A8	EO	2037	defb 00011100b		
00A9	EO	2038	defb 00011100b		
00AA	EO	2039	defb 00011100b	I code 85	graphics
00AB	EO	2040	defb 00011100b		
00AC	EO	2041	defb 00011100b		
00AD	EO	2042	defb 00011100b		
00AE	EO	2043	defb 00011100b		
00AF	EO	2044	defb 00011100b		
00B0	EO	2045	defb 00011100b		
00B1	EO	2046	defb 00011100b		
00B2	EO	2047			
00B3	EO	2048			
00B4	EO	2049	defb 11100000b	I code 86	graphics
00B5	EO	2050	defb 11100000b		
00B6	EO	2051	defb 11100000b		
00B7	EO	2052	defb 11100000b		
00B8	EO	2053	defb 00011100b		
00B9	EO	2054	defb 00011100b		
00BA	EO	2055	defb 00011100b		
00BB	EO	2056	defb 00011100b		
00BC	EO	2057			
00BD	EO	2058	defb 11111100b	I code 87	graphics
00BE	EO	2059	defb 11111100b		
00BF	EO	2060	defb 11111100b		
00C0	EO	2061	defb 11111100b		
00C1	EO	2062	defb 00011100b		
00C2	EO	2063	defb 00011100b		
00C3	EO	2064	defb 00011100b		
00C4	EO	2065	defb 00011100b		
00C5	EO	2066			
00C6	EO	2067	defb 00000000b	I code 88	graphics
00C7	EO	2068	defb 00000000b		
00C8	EO	2069	defb 00000000b		
00C9	EO	2070	defb 00000000b		
00CA	EO	2071	defb 11100000b		
00CB	EO	2072	defb 11100000b		
00CC	EO	2073	defb 11100000b		
00CD	EO	2074	defb 11100000b		
00CE	EO	2075	defb 11100000b		
00CF	EO	2076			
00D0	EO	2077	defb 00011100b	I code 89	graphics
00D1	EO	2078	defb 00011100b		
00D2	EO	2079	defb 00011100b		
00D3	EO	2080	defb 11100000b		
00D4	EO	2081	defb 11100000b		
00D5	EO	2082	defb 11100000b		
00D6	EO	2083	defb 11100000b		
00D7	EO	2084			
00D8	EO	2085	defb 11100000b	I code 8A	graphics
00D9	EO	2086	defb 11100000b		
00DA	EO	2087	defb 11100000b		
00DB	EO	2088	defb 11100000b		
00DC	EO	2089	defb 11100000b		
00DD	EO	2090	defb 11100000b		
00DE	EO	2091	defb 11100000b		
00DF	EO	2092	defb 11100000b		
00E0	EO	2093			
00E1	EO	2094	defb 11111100b	I code 8B	graphics
00E2	EO	2095	defb 11111100b		
00E3	EO	2096	defb 11111100b		
00E4	EO	2097	defb 11111100b		
00E5	EO	2098	defb 11100000b		
00E6	EO	2099	defb 11100000b		
00E7	EO	2100	defb 11100000b		
00E8	EO	2101	defb 11100000b		
00E9	EO	2102			
00EA	EO	2103	defb 00000000b	I code 8C	graphics
00EB	EO	2104	defb 00000000b		
00EC	EO	2105	defb 00000000b		
00ED	EO	2106	defb 00000000b		
00EE	EO	2107	defb 11111100b		
00EF	EO	2108	defb 11111100b		
00F0	EO	2109	defb 11111100b		
00F1	EO	2110	defb 11111100b		
00F2	EO	2111			
00F3	EO	2112	defb 00011100b	I code 8D	graphics
00F4	EO	2113	defb 00011100b		
00F5	EO	2114	defb 00011100b		
00F6	EO	2115	defb 00011100b		

```

08EA* FC 2116 defb 11111100b
08E9* FC 2117 defb 11111100b
08EC* FC 2118 defb 11111100b
08ED* FC 2119 defb 11111100b
2120
08EE* E0 2121 i defb 11100000b icode 8E graphics
08EF* E0 2122 defb 11100000b
08F0* E0 2123 defb 11100000b
08F1* E0 2124 defb 11100000b
08F2* FC 2125 defb 11111100b
08F3* FC 2126 defb 11111100b
08F4* FC 2127 defb 11111100b
08F5* FC 2128 defb 11111100b
2129
08F6* FC 2130 i defb 11111100b icode 8F graphics
08F7* FC 2131 defb 11111100b
08F8* FC 2132 defb 11111100b
08F9* FC 2133 defb 11111100b
08FA* FC 2134 defb 11111100b
08FB* FC 2135 defb 11111100b
08FC* FC 2136 defb 11111100b
08FD* FC 2137 defb 11111100b
2138

```

Additional input after END statement ignored

```

A Reserved ATTTY 0039 ATTYCL 0011 B Reserved BOTLN 0020
C Reserved CALCP0 053C CALCP1 0526 CALCP2 053A CMNGVI 008E
CHRSET 047E CMST 057E CHYBL 002P CLINIT 1000 CLACTL 000C
CLRSB0 0270 CLRSC0 002E IN CLRSC1 027C IN CLRSC0 0292 CLRSC1 02A3
CLRSC2 02AE CLRSC3 0282 CLRSC4 0285 CLRSC5 02C0 CLRSC6 02D9
CLRSC7 02E0 CLRSC8 02E7 COMVPM 04FC COMVPM 04FC CURPOS 0034
D Reserved DEST7 P7C0 DEST7 P7C0 DPAORS 0030 OPBIT 003E
DRMSPT 00F4 DRIVES 0940 E ENBLEX 04D2 ENBLMO 04D9
ERPRET 0164 EXTNRM 04A7 E PIX Reserved PIX 97C0 PIXYBL 1000 GETA00 03C0
GETA70 0010 IN GETATT 03CD IN GETC00 0304 GETCTL 0019 GETCUB 7021 IN
GETCUR 0304 IN GETC1 03EC GETVAL 044C GOODRLE 0142 GRALX 0240
GPHST 077E GPHST 007E GRTBL 0031 GTCM00 032A IN GTCM20 0326
GTCM00 0010 IN GTCM1 033P GTCM01 0031 GTCM22 0349 GTCM23 033P
GTCM3 0344 GTCM2 0363 GTCM02 0360 GTCM4 039A GTCM21 0368 GTCM5 0386
GTCM4 03C4 GTCM31 0367 GTCM03 0360 GTC201 0568 GTC202 057C
GTINX 003A M Reserved HRSPT 00FP HRSPT 00FP INSDL 0201 INSERT 12C0
INVPAR 0147 L Reserved LDPDSM 0530 LDPDSM 0530 LINCL 0007 LINLEN 0033
LN9U 053P L COP 0205 LDP01 0210 LDP01 0210 M Reserved
MARGIN 0030 MASKB 0030 MCVSE1 0940 MCKCTL 0019 NERT1 00C2
NEXT2 00EA NEXT3 010A NOINVE 0117 NOR01 00C9 NOR02 00P1
NOR03 0111 NOSTRG 0106 NOR08 00CF PSM Reserved PARAMS 1CC3
PARPR 04F0 SC04 SC04 SCRLS 002A IN SCRLS 002A IN SCRL00 01CA SCALCT 002E
SCALXY 0237 SCRL0 01E4 SCRGLL 01CE IN SCRPI 0010 SCALCT 002E
SETA70 0012 IN SETATT 02F6 IN SETC00 018A SETCUB 0009 IN SETA00 02F3
SETM00 0310 SETM00 0025 IN SETM01 0446 SETM02 044A SETCUR 018E IN
SETM00 0016 IN SETMSK 0320 IN SET0 04CF SETM00 0340 IN
STBLK0 024C STBLK1 0240 STBLK2 0264 STX7A 0483 TESTAD 01P3 STBLK 0240
STPDSM 0550 STGCT 0030 TVPULQ 0140 TVPUL1 0147 UDC 0000
TSTPR1 04P4 TST01 04C7 TVARS 0C40 VIDM00 0CC2 WRCM0 003P WRCM11 0041
UPDAT 047E UPJDS 0507 VARS 0C40 WRCM19 0084 WRCM2 000A
WRCM0 0042 IN WRCM00 0001 IN WRCMXY 013P WRCM0 003P WRCM11 0041
WRCM12 0049 WRCM13 0040 WRCM14 0003 WRCM19 0084 WRCM2 000A
WRCM3 0095 WRCM5 0098 WRCM6 00AF WRCM7 0124 WRCM20 000A IN
WRCM4 0194 IN WRSTR0 0171 WRSTR1 017C WRCM21 010C WRCM23 019E
WRSTX1 01A7 WRSTX2 0103

```

No errors detected

Cross reference listing (MREF version 4.7)

Symbol	Refs (# = definition # = write <blank> = read)
ATTTY	134# 672# 303 906#
ATTCTL	88# 440 907#
BOTLN	110# 322 844#
CALCP1	1042 1049#
CALCP2	1059 1062#
CALCP0	69# 1023 1031#
CMNGVI	42# 892
CHRSET	39# 126 957
CHRST	39 1120#
CHYBL	126# 170 699 858#
CLINIT	38# 851
CLACTL	84# 555 852#
CLRSB0	87 554#
CLRSC0	507 567 571#
CLRSC1	583# 432
CLRSC2	592#
CLRSC3	595# 441
CLRSC4	597# 426
CLRSC5	604# 420
CLRSC6	617 421#
CLRSC7	591 635#
CLRSC8	30 87#
CLRSCM	27 557#
CLRSXY	630 632#
COMVPM	415 697 811 1010#
COORDS	51# 913#
CURPOS	129# 810 1084# 1089
DATA0	76# 147 864#
DEST7	59# 60
DPAORS	131# 1085# 1090
DPBIT	141# 224 240 269 299 301# 304#
	495 760# 790# 1063# 1107
DRMSPT	46# 973# 979#
DRIVES	56# 57 58
ENBLEX	807 960#
ENBLMO	893 977#
ERPRET	313# 317 334 917
EXTNRM	882 886 916#
PIX	60#
PIXYBL	62#

GETAB0	105	8010					
GETAT0	31	1030					
GETATY	28	9030					
GETC1	814	820	8220				
GETC00	104	8080					
GETCTL	1020	691	8680	8690			
GETCUB	31	1040					
GETCUR	28	8090					
CBTVAL	889	9380					
GOODRE	3120	417	439	968	673	682	876
		898	915				
GBBLK	441	5090					
GRPHST	400	127	859				
GRPET	40	1920					
GRTBL	1270	167	776	8600			
GTC201	1104	11040					
GTC202	1110	11150					
GTC20C	707	732	10980				
GTCM1	7020	779	786				
GTCM2	7040	771					
GTCM22	7090						
GTCM23	720	7230					
GTCM3	716	7240					
GTCM31	7280	743					
GTCM32	753	755	7570				
GTCM4	722	725	742	7640			
GTCM5	775	7800					
GTCM6	781	7890					
GTCMAR	28	6930					
GTCM80	104	6840					
GTCM88	31	1040					
GTCM9P	11110	1114					
GTINDX	1360	7020	718	750	773		
MREKPT	450	463	6660	969	9710	980	9820
INSOBL	4620						
INSERT	574	877					
INVPAR	155	157	316	432	435	437	561
		364	566	655	838	993	
LOPDSM	150	10880					
LINCOL	800	411	8220	8650	8660		
LINLEN	1280	182	445	575	812	8480	960
		578	1010	1031			
		578	1031	10670			
LMBU	448						
LDDP	4650	515					
LDDPO	4670	494					
LDDPI	4740	488					
MARGIN	1400	8500	1051				
MASH0	1320	196	6810	8540			
MOVESZ	580	59	878				
MSKCTL	980	478	8670				
NEXT1	2300	233					
NEXT2	2560	259					
NEXT3	2750	278					
NOINVE	268	2850					
NQRR1	228	2340					
NQRR2	254	2600					
NQRR3	273	2790					
NQSTRG	353	4010					
NQXDR	223	2400					
NXTSC	485	4890					
PARAM0	550	346					
PARERR	9920	997					
PRAHT	530						
RAMTOP	520	883					
RECLN	430	357					
SCIMIT	370	841					
SCRCTL	1110	336	425	8420			
SCRLO	338	438	4410				
SCRLO	30	1140					
SCRLO0	114	4230					
SCRLOT	1230	326	3300	3350	8440		
SCRLOT	498	5010	544				
SCRLL	27	4270					
SCRSI	360	319	442	572	1014	1069	
SET0	942	9610					
SETAB0	97	4670					
SETAT0	38	970					
SETATT	27	4500					
SETC00	83	4090					
SETCUB	30	830					
SETCUR	27	4130					
SETH0	101	6770					
SETH01	836	8710					
SETH02	874	8870					
SETH08	33	1100					
SETH0D	33	8330					
SETH58	30	1010					
SETH5K	27	6800					
SYBL4	453	5170					
SYBL40	5200	540					
SYBL41	5210	534					
SYBL42	531	5350					
SYKND	450	880					
SYH00	110	8290					
SYPDSM	311	1024	10830				
SYRGCT	1380	3950	8550	8560			
SYT6	8990						
TESTAD	4510	500	547				
TST01	940	9580					
TSTPAR	414	690	9890				
TSTPR1	951	9840					
TVPUL1	328	3350					
TVPUL0	141	3150					
UDG	500	163	782				
UPDATE	8970						
UPDDPS	324	416	634	914	10210		
VARI	480	350					
VDMCO	540	453	6680	872	8960		
VDMODE	1070	831					

WRCM0	77	1450	
WRCM1	162	1670	
WRCM2	160	1700	
WRCM3	166	169	1710
WRCM4	183	1880	
WRCM5	187	1890	
WRCM2	1940		
WRCM3	199	2010	
WRCM5	2060	209	
WRCM6	217	2190	
WRCM7	2960		
WRCHAB	27	1490	377
WRCHAB	30	770	
WRCHXT	302	309	3110
WRSTAG	78	3460	
WRSTB1	3510	360	
WRSTB2	355	3610	
WRSTB3	3820	393	
WRSTB8	30	780	
WRSTAG	27	3740	388
WRSTX1	380	3910	
WRSTX2	3980	402	

APPENDIX C-3

40 COLUMN MODE

Name: 40 Column Mode Support

Description:

This component provides support to the application programmer for using the 40-column mode feature of the TS 2066. 40-column mode is implemented by modifying the character width from 8 to 6 pixels. The services include position control, clear screen and scroll screen services and display of characters. For ease of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of PDKE'ing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk 7 (E000H/57344).

Attribute and other display controls such as Inverse are taken from the standard TS2066 System Variables (see Usage Section.)

This component is designed to permit use in normal video mode (Display File 1 only) or, in conjunction with ASC004 - Dual Screen Mode Support, to permit use of Display File 1 and/or Display File 2. The value of the System Variable VIDMOD is used to determine which display file is the target of the requested service.

Application Services

Name: INIT40 (INIT4B from BASIC)

Input: None

Description:

Initializes the internal variables to their default values for 40-column mode (see Usage Section).

Name: CLRSCN (CLRSCB from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)
Starting Line Number (0-23)

From Machine Code: Line Count In Register B
Starting Line In Register C

From BASIC: Starting Line Number in CLSCTL
Line Count in CLSCTL + 1

Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion
BC = 1 invalid parameters
(Line Number + Line Count < 1 or > 24)

Name: SETCUR (SETCUB from BASIC - parameters to LINCOL)

Input: Line Number (0-23)
Column Number (0-39) or (0-41)

From Machine Code: Line Number In Register B
Column Number In Register C

From BASIC: Column Number In LINCOL
Line Number In LINCOL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

Output: BC = 0 for successful completion
BC = 1 for invalid parameters (Line Number > 23,
Column Number > Line Length-1)

Name: MRCMR (MRCMB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed

20H YD 7FH - Std. TS2068 Character Set
80H YD 8FH - Std. Graphics Set
90H YD A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAB

Description:

Displays character at current cursor position, applying current attributes and mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLN (see Usage section) and the scroll count (variable SCRLCT) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRCTL and the new line started at the vacated line.

Note that only the first 6 bits of each byte in the User Defined Graphics area will be transferred to the display file.

Output: BC = 0 for successful completion
BC = 1 invalid character code
BC = 3 for screen full

Name: WRSTRG (WRSTRB from BASIC - String Identifier in PARAMS)

Input: Character Code String

From machine code: Address of string in HL
Count in BC

From BASIC: String Variable Identifier in
System Variable PARAMS - 23747 (SCC3M)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCHR description and Usage Section on Automatic Scrolling). For the Screen Full condition, the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-A4H)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, PDKE the code for the string variable identifier into PARAMS prior to invoking WRSTRB, e.g.

```
0005 LET a$="----string-----"  
0010 PDKE 23747,CODE "a"  
0015 IFUSR(WRSTRB)<>0 THEN ----  
      (continues)
```

Output: BC = 0 Successful
BC = 2 BASIC - String not found
BC = 3 Screen Full - Remaining Count in STRGCT
(HL=Current Address in String)

Name: SCRLL (SCRLB from BASIC - parameters to SCRCTL)

Input: Line Count (1-23)
Starting Line Number (1-23)

From Machine Code: Line Count in B
Starting Line in C
From BASIC: Starting Line in SCRCTL
Line Count in SCRCTL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on "automatic" scrolling.

Output: BC = 0 Successful
BC = 1 Invalid Parameters
(Line Number + Line Count < 1 or > 24)

Name: GTCMAR (GTCMRB from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

From Machine Code: Line Number in B
Column Number in C

*From BASIC: Column Number in GETCTL
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find
BC = 1 invalid parameters
BC = character code (20H-A4H)

Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GTCMAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position. Note that in 40 column mode the 6-pixel character width may cross attribute byte boundaries in the display file (e.g. the character may have 2 pixels in one byte and 4 in the next). The attribute bytes for these two locations may be different. The value of the attribute byte controlling the starting location of the character will be returned.

Output: BC = 1 for invalid parameters
BC = attribute byte:
Bit 7 - FLASH
Bit 6 - BRIGHT
Bit 5
Bit 4 - PAPER
Bit 3 /
Bit 2
Bit 1 - INK
Bit 0 /

Name: GETCUR (GETCUB from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCCL, the current print position (where the next character would be displayed).

Output: B = line number (0-23)
C = Column number (0-39) or (0-41)

BASIC: LINCCL - Column number
LINCCL + 1 - Line number

NOTE: If the last character was printed at Col.39 (41) of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

Usage:

Memory Usage:

This package of machine code routines includes the following internal variables:

<u>Name</u>	<u>Size</u>	<u>Description</u>
SCRCTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BOTLN	1	Bottom Line - Line number (0-23) after which test for scroll will be made.
SCRCLT	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.
CHTBL	2	Character Table (Base Address-100H)
GRTBL	2	Std.Graphics Character Table (Base-100H)
LINLEN	1	Line Length - (40 or 42 when in 40-Col.Mode)
CURPOS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFACDR	2	Current Display File Address
MASKB	1	Working Byte - (P FLAG Shifted Right 1) (bit 0 = OVER) (bit 2 = INVERSE) (bit 4 = INK Complement of PAPER) (bit 6 = PAPER Complement of INK)
ATTBYT	1	Working Byte - (Copy of ATTR P)
GTINDX	1	"Get" Index - Used by GTCCHAR
STRGCT	2	String Count - Contains remaining byte count when EC=3 (Screen Full) is returned from the Write String service WRSTRG (WRSTRB).
MARGIN	1	Margin - Margin Adjust (0/1)
DFBIT	1	Display File Bit - Current Bit Position
ATTMSK	1	Working Byte - (Copy of MASK P)

Initial values set via INIT40 (INIT4E) are as follows:

Variable Name	Value
SCRCTL	1701H
BOTLN	17H
SCRCLCT	1H
CMTBL	(Internal to Module)
GRTBL	(Internal to Module)
LINLEN	28H
CLRPDS	1829H
DFADDR	4000H
GTINDX	80H
STRGCT	0H
MARGIN	1H
DFBIT	7H

The following are the variables used for passing parameters in BASIC and their values as initialized by INIT4B:

Variable Name	Size	Value
DATAB	1	0H
LINCOL	2	0H
CLRCTL	2	1800H
GETCTL	2	0H

In addition, VIDMOD, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Home Bank.

Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the remapping of certain structures when the second display file is open (Dual Screen Mode only). NOTE: Machine code above RAMTOP is not moved.

Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IY Register which must always contain the value 5C3AH for access to the standard system variables.

Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BOTLN=23=24th line). Condition Code 3 (Screen Full) will be returned since SCRCLCT will decrement to zero. If SCRCLCT is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a PCKE or setting the variables BOTLN and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BOTLN be set to line 21 (15H) and SCRCTL+1 be set to 21 (15H) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRCLCT expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the "Screen Full" condition.

By setting the SCRCTL variable and invoking the SCRQLL (SCRQB) routine any portion of the screen may be scrolled at any time.

Margin Control:

In 40-Column Mode, there are actually 42 character positions per line. The variable MARGIN determines the offset of the beginning of the 40 column line from the left side of the screen and has valid offset values of 0 or 1. An offset value of 1 centers the 40 column line on the screen; 0 begins at the extreme left side. The default value is 1. When MARGIN is set to 0, the variable LINLEN can be set to 42 to permit access to the 2 extra print positions. Whenever MARGIN and/or LINLEN are modified, a "Set Cursor" operation should be done to insure the integrity of the print position.

NOTE: Since the different MARGIN values result in different pixel positions for the columns, care must be taken in mixing line length and margin values on the same line.

Attribute Control:

Attribute and masking (Inverse/Dver) control information will be taken from the system variables ATTR P, MASK P and P FLAG as defined below. These variables contain the "permanent" attribute controls set via the BASIC commands PAPER, INK, ERIGHT, FLASH, INVERSE, and DVER, or by directly writing to the specified locations.

In 40-Column Mode, the 6-pixel character width results in characters crossing attribute byte boundaries in the display file. Every four columns across a line are controlled by three attribute bytes. This constraint must be taken into consideration when mixing attributes within a line. Inverse and Dver are applied to individual characters and are therefore not subject to the above limitation.

NAME	ADDRESS	CONTENTS
----	-----	-----
ATTR P	23693	Bit 7 - FLASH 6 - BRIGHT 5 4 - PAPER 3/ 2 1 - INK 0/
MASK P	23694	SAME FORMAT AS ATTR P. USED FOR "TRANSPARENT" DISPLAY: For each bit that is set to 1, the corresponding information will be taken from the current screen position instead of from ATTRP.
P FLAG	23697	BIT 7 PAPER=COMPLEMENT OF INK 5 INK=COMPLEMENT OF PAPER 3 - INVERSE 1 - DVER (New Characters XGR'd with Old)

ADDITIONAL NOTES:

1. All screen operations done by the system ROM (PRINT, LIST Edit line I/O, etc.) work with the standard 8-pixel wide characters.

```

1      NAME ADL - ASC 003 40-COL.MODE SUPPORT
2
3
4
5
6
7
8
9
10
11      *
12      *
13      * APPLICATION DEVELOPMENT LIBRARY *
14      *
15      * NAME: 40-COL.MODE SUPPORT
16      *
17      * ASC NO: 003
18      * VERSION: 001
19      * AUTHOR: C. CONCORAN
20      * DATE: 12/19/83
21      *
22      *
23      SUBTTL DEFINITIONS
24
25
26      *****DEFINITIONS*****
27
28      INTERM W0CHAR, W0STRG, SETCUR, CLASCH, SCROLL
29      INTERM G0CHAR, GETATT, GETCUR
30
31      INTERM W0CM0B, W0STR0, SETC0B, CLRS0B, SCRL0
32      INTERM G0CM0B, GETA0B, GETC0B
33
34      INTERM INIT40,INIT40
35
36
37      SCRSZ      EQU      24      I 24 LINES
38      SCINIT     EQU      1701H   I SCROLL CONTROL INITIALIZATION
39      CLINIT     EQU      1000H   I CLEAR SCREEN CTL. INIT.
40      CHRSET     EQU      ((CHR37)-100H) ICHAR.TABLE-100H
41      GRPHST     EQU      ((GRP37)-100H) I STD.GRAPHICS TABLE
42
43      P0CLEN     EQU      1720H   I ROUTINE IN HOME PGM
44
45      VARS       EQU      9C40H   I SYSTEM VARIABLES
46      UDG        EQU      9C70H   I
47      C00R0S     EQU      9C70H   I
48      ATT0_P     EQU      9C80H   I
49      MASK_P     EQU      9C80H   I
50      P_PLAC     EQU      9C91H   I
51      VIOH0D     EQU      9CC3H   I
52      PARAM0     EQU      9CC3H   I (Location 23747)
53
54      SUBTTL INPUTS AND ENTRIES
55
56
57
58
59      *****PARAMETER INPUTS AND ENTRY POINTS FROM BASIC**
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
126
127
128
129
130
131
132
133
134
135
136
137
138
139
140
141
142
143
144
145
146
147
148
149
150
151
152
153
154
155
156
157
158
159
160
161
162
163
164
165
166
167
168
169
170
171
172
173
174
175
176
177
178
179
180
181
182
183
184
185
186
187
188
189
190
191
192
193
194
195
196
197
198
199
200
201
202
203
204
205
206
207
208
209
210
211
212
213
214
215
216
217
218
219
220
221
222
223
224
225
226
227
228
229
230
231
232
233
234
235
236
237
238
239
240
241
242
243
244
245
246
247
248
249
250
251
252
253
254
255
256
257
258
259
260
261
262
263
264
265
266
267
268
269
270
271
272
273
274
275
276
277
278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
346
347
348
349
350
351
352
353
354
355
356
357
358
359
360
361
362
363
364
365
366
367
368
369
370
371
372
373
374
375
376
377
378
379
380
381
382
383
384
385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420
421
422
423
424
425
426
427
428
429
430
431
432
433
434
435
436
437
438
439
440
441
442
443
444
445
446
447
448
449
450
451
452
453
454
455
456
457
458
459
460
461
462
463
464
465
466
467
468
469
470
471
472
473
474
475
476
477
478
479
480
481
482
483
484
485
486
487
488
489
490
491
492
493
494
495
496
497
498
499
500
501
502
503
504
505
506
507
508
509
510
511
512
513
514
515
516
517
518
519
520
521
522
523
524
525
526
527
528
529
530
531
532
533
534
535
536
537
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580
581
582
583
584
585
586
587
588
589
590
591
592
593
594
595
596
597
598
599
600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681
682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719
720
721
722
723
724
725
726
727
728
729
730
731
732
733
734
735
736
737
738
739
740
741
742
743
744
745
746
747
748
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
769
770
771
772
773
774
775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
836
837
838
839
840
841
842
843
844
845
846
847
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865
866
867
868
869
870
871
872
873
874
875
876
877
878
879
880
881
882
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917
918
919
920
921
922
923
924
925
926
927
928
929
930
931
932
933
934
935
936
937
938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962
963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999
1000
1001
1002
1003
1004
1005
1006
1007
1008
1009
1010
1011
1012
1013
1014
1015
1016
1017
1018
1019
1020
1021
1022
1023
1024
1025
1026
1027
1028
1029
1030
1031
1032
1033
1034
1035
1036
1037
1038
1039
1040
1041
1042
1043
1044
1045
1046
1047
1048
1049
1050
1051
1052
1053
1054
1055
1056
1057
1058
1059
1060
1061
1062
1063
1064
1065
1066
1067
1068
1069
1070
1071
1072
1073
1074
1075
1076
1077
1078
1079
1080
1081
1082
1083
1084
1085
1086
1087
1088
1089
1090
1091
1092
1093
1094
1095
1096
1097
1098
1099
1100
1101
1102
1103
1104
1105
1106
1107
1108
1109
1110
1111
1112
1113
1114
1115
1116
1117
1118
1119
1120
1121
1122
1123
1124
1125
1126
1127
1128
1129
1130
1131
1132
1133
1134
1135
1136
1137
1138
1139
1140
1141
1142
1143
1144
1145
1146
1147
1148
1149
1150
1151
1152
1153
1154
1155
1156
1157
1158
1159
1160
1161
1162
1163
1164
1165
1166
1167
1168
1169
1170
1171
1172
1173
1174
1175
1176
1177
1178
1179
1180
1181
1182
1183
1184
1185
1186
1187
1188
1189
1190
1191
1192
1193
1194
1195
1196
1197
1198
1199
1200
1201
1202
1203
1204
1205
1206
1207
1208
1209
1210
1211
1212
1213
1214
1215
1216
1217
1218
1219
1220
1221
1222
1223
1224
1225
1226
1227
1228
1229
1230
1231
1232
1233
1234
1235
1236
1237
1238
1239
1240
1241
1242
1243
1244
1245
1246
1247
1248
1249
1250
1251
1252
1253
1254
1255
1256
1257
1258
1259
1260
1261
1262
1263
1264
1265
1266
1267
1268
1269
1270
1271
1272
1273
1274
1275
1276
1277
1278
1279
1280
1281
1282
1283
1284
1285
1286
1287
1288
1289
1290
1291
1292
1293
1294
1295
1296
1297
1298
1299
1300
1301
1302
1303
1304
1305
1306
1307
1308
1309
1310
1311
1312
1313
1314
1315
1316
1317
1318
1319
1320
1321
1322
1323
1324
1325
1326
1327
1328
1329
1330
1331
1332
1333
1334
1335
1336
1337
1338
1339
1340
1341
1342
1343
1344
1345
1346
1347
1348
1349
1350
1351
1352
1353
1354
1355
1356
1357
1358
1359
1360
1361
1362
1363
1364
1365
1366
1367
1368
1369
1370
1371
1372
1373
1374
1375
1376
1377
1378
1379
1380
1381
1382
1383
1384
1385
1386
1387
1388
1389
1390
1391
1392
1393
1394
1395
1396
1397
1398
1399
1400
1401
1402
1403
1404
1405
1406
1407
1408
1409
1410
1411
1412
1413
1414
1415
1416
1417
1418
1419
1420
1421
1422
1423
1424
1425
1426
1427
1428
1429
1430
1431
1432
1433
1434
1435
1436
1437
1438
1439
1440
1441
1442
1443
1444
1445
1446
1447
1448
1449
1450
1451
1452
1453
1454
1455
1456
1457
1458
1459
1460
1461
1462
1463
1464
1465
1466
1467
1468
1469
1470
1471
1472
1473
1474
1475
1476
1477
1478
1479
1480
1481
1482
1483
1484
1485
1486
1487
1488
1489
1490
1491
1492
1493
1494
1495
1496
1497
1498
1499
1500
1501
1502
1503
1504
1505
1506
1507
1508
1509
1510
1511
1512
1513
1514
1515
1516
1517
1518
1519
1520
1521
1522
1523
1524
1525
1526
1527
1528
1529
1530
1531
1532
1533
1534
1535
1536
1537
1538
1539
1540
1541
1542
1543
1544
1545
1546
1547
1548
1549
1550
1551
1552
1553
1554
1555
1556
1557
1558
1559
1560
1561
1562
1563
1564
1565
1566
1567
1568
1569
1570
1571
1572
1573
1574
1575
1576
1577
1578
1579
1580
1581
1582
1583
1584
1585
1586
1587
1588
1589
1590
1591
1592
1593
1594
1595
1596
1597
1598
1599
1600
1601
1602
1603
1604
1605
1606
1607
1608
1609
1610
1611
1612
1613
1614
1615
1616
1617
1618
1619
1620
1621
1622
1623
1624
1625
1626
1627
1628
1629
1630
1631
1632
1633
1634
1635
1636
1637
1638
1639
1640
1641
1642
1643
1644
1645
1646
1647
1648
1649
1650
1651
1652
1653
1654
1655
1656
1657
1658
1659
1660
1661
1662
1663
1664
1665
1666
1667
1668
1669
1670
1671
1672
1673
1674
1675
1676
1677
1678
1679
1680
1681
1682
1683
1684
1685
1686
1687
1688
1689
1690
1691
1692
1693
1694
1695
1696
1697
1698
1699
1700
1701
1702
1703
1704
1705
1706
1707
1708
1709
1710
1711
1712
1713
1714
1715
1716
1717
1718
1719
1720
1721
1722
1723
1724
1725
1726
1727
1728
1729
1730
1731
1732
1733
1734
1735
1736
1737
1738
1739
1740
1741
1742
1743
1744
1745
1746
1747
1748
1749
1750
1751
1752
1753
1754
1755
1756
1757
1758
1759
1760
1761
1762
1763
1764
1765
1766
1767
1768
1769
1770
1771
1772
1773
1774
1775
1776
1777
1778
1779
1780
1781
1782
1783
1784
1785
1786
1787
1788
1789
1790
1791
1792
1793
1794
1795
1796
1797
1798
1799
1800
1801
1802
1803
1804
1805
1806
1807
1808
1809
1810
1811
1812
1813
1814
1815
1816
1817
1818
1819
1820
1821
1822
1823
1824
1825
1826
1827
1828
1829
1830
1831
1832
1833
1834
1835
1836
1837
1838
1839
1840
1841
1842
1843
1844
1845
1846
1847
1848
1849
1850
1851
1852
1853
1854
1855
1856
1857
1858
1859
1860
1861
1862
1863
1864
1865
1866
1867
1868
1869
1870
1871
1872
1873
1874
1875
1876
1877
1878
1879
1880
1881
1882
1883
1884
1885
1886
1887
1888
1889
1890
1891
1892
1893
1894
1895
1896
1897
1898
1899
1900
1901
1902
1903
1904
1905
1906
1907
1908
1909
1910
1911
1912
1913
1914
1915
1916
1917
1918
1919
1920
1921
1922
1923
1924
1925
1926
1927
1928
1929
1930
1931
1932
1933
1934
1935
1936
1937
1938
1939
1940
1941
1942
1943
1944
1945
1946
1947
1948
1949
1950
1951
1952
1953
1954
1955
1956
1957
1958
1959
1960
1961
1962
1963
1964
1965
1966
1967
1968
1969
1970
1971
1972
1973
1974
1975
1976
1977
1978
1979
1980
1981
1982
1983
1984
1985
1986
1987
1988
1989
1990
1991
1992
1993
1994
1995
1996
1997
1998
1999
2000
2001
2002
2003
2004
2005
2006
2007
2008
2009
2010
2011
2012
2013
2014
2015
2016
2017
2018
2019
2020
2021
2022
2023
2024
2025
2026
2027
2028
2029
2030
2031
2032
2033
2034
2035
2036
2037
2038
2039
2040
2041
2042
2043
2044
2045
2046
2047
2048
2049
2050
2051
2052
2053
2054
2055
2056
2057
2058
2059
2060
2061
2062
2063
2064
2065
2066
2067
2068
2069
2070
2071
2072
2073
2074
2075
2076
2077
2078
2079
2080
2081
2082
2083
2084
2085
2086
2087
2088
2089
2090
2091
2092
2093
2094
2095
2096
2097
2098
2099
2100
2101
2102
2103
2104
2105
2106
2107
2108
2109
2110
2111
2112
2113
2114
2115
2116
2117
2118
2119
2120
2121
2122
2123
2124
2125
2126
2127
2128
2129
2130
2131
2132
2133
2134
2135
2136
2137
2138
2139
2140
2141
2142
2143
2144
2145
2146
2147
2148
2149
2150
2151
2152
2153
2154
2155
2156
2157
2158
2159
2160
2161
2162
2163
2164
2165
2166
2167
2168
2169
2170
2171
2172
2173
2174
2175
2176
2177
2178
2179
2180
2181
2182
2183
2184
2185
2186
2187
2188
2189
2190
2191
2192
2193
2194
2195
2196
2197
2198
2199
2200
2201
2202
2203
2204
2205
2206
2207
2208
2209
2210
2211
2212
2213
2214
2215
2216
2217
2218
2219
2220
2221
2222
2223
2224
2225
2226
2227
2228
2229
2230
2231
2232
2233
2234
2235
2236
2237
2238
2239
2240
2241
2242
2243
2244
2245
2246
2247
2248
2249
2250
2251
2252
2253
2254
2255
2256
2257
2258
2259
2260
2261
2262
2263
2264
2265
2266
2267
2268
2269
2270
2271
2272
2273
2274
2275
2276
2277
2278
2279
2280
2281
2282
2283
2284
2285
2286
2287
2288
2289
2290
2291
2292
2293
2294
2295
2296
2297
2298
2299
2300
2301
2302
2303
2304
2305
2306
2307
2308
2309
2310
2311
2312
2313
2314
2315
2316
2317
2318
2319
2320
2321
2322
2323
2324
2325
2326
2327
2328
2329
2330
2331
2332
2333
2334
2335
2336
2337
2338
2339
2340
2341
2342
2343
2344
2345
2346
2347
2348
2349
2350
2351
2352
2353
2354
2355
2356
2357
2358
2359
2360
2361
2362
2363
2364
2365
2366
2367
2368
2369
2370
2371
2372
2373
2374
2375
2376
2377
2378
2379
2380
2381
2382
2383
2384
2385
2386
2387
2388
2389
2390
2391
2392
2393
2394
2395
2396
2397
2398
2399
2400
2401
2402
2403
2404
2405
2406
2407
2408
2409
2410
2411
2412
2413
2414
2415
2416
2417
2418
2419
2420
2421
2422
2423
2424
2425
2426
2427
2428
2429
2430
2431
2432
2433
2434
2435
2436
2437
2438
2439
2440
2441
2442
2443
2444
2445
2446
2447
2448
2449
2450
2451
2452
2453
2454
2455
2456
2457
2458
2459
2460
2461
2462
2463
2464
2465
2466
2467
2468
2469
2470
2471
2472
2473
2474
2475
2476
2477
2478
2479
2480
2481
2482
2483
2484
2485
2486
2487
2488
2489
2490
2491
2492
2493
2494
2495
2496
2497
2498
2499
2500
2501
2502
2503
2504
2505
2506
2507
2508
2509
2510
2511
2512
2513
2514
2515
2516
2517
2518
2519
2520
2521
2522
2523
2524
2525
2526
2527
2528
2529
2530
2531
2532
2533
2534
2535
2536
2537
2538
2539
2540
2541
2542
2543
2544
2545
2546
2547
2548
2549
2550
2551
2552
2553
2554
2555
2556
2557
2558
2559
2560
2561
2562
2563
2564
256
```

```

0030" 0000          114      1      CH.CODE IN GTCHAR
0030" 01          115      STRGCT      DEPB      0      I REMAINING COUNT FROM WRSTAG
0030" 07          116      MARGIN      DEPB      1      I WHEN "SCREEN PULL"          57403
0030" 00          117      OPBIT      DEPB      7      I MARGIN ADJUST (0/1)        57405
0030" 00          118      ATYMSK      DEPB      0      I BIT POSN. IN LINR(7,1,3,5) 57406
0030" 00          119      SUBTTL WRITE CHARACTER
0040" 00          121      WRCM0:          I HERE FROM BASIC ENTRY TO DISPLAY THE
0040" 3A 0000"     122      LD A,(DATAB)    I CHARACTER WHOSE CODE IS IN "DATAB"
0043" CD 04PD"     123      WRCM1:          I ENTRY WITH CODE IN A
0043" CD 04PD"     124      CALL L0ATYR     I GET ATTRIBUTE AND MASK CONTROLS
0046" CD 0441"     125      WRCM1: CALL L0PDSM I ENTRY TO USE ATTRIBUTE VALUES IN
0049" PE 20        126      CP 20M          I INTERNAL VARIABLES WITHOUT RELOADING
0049" 0A 013P"    127      JP C,INVPAR    I LOAD REGISTERS FOR CURRENT POSITION
0049" PE A3        128      CP 0A3M        I BC=CURSOR POSITION FROM "CURPOS"
0050" 02 013P"    129      JP NC,INVPAP   I HL=DISPLAY FILE ADDRESS FROM "DPAORS"
0053" C3          130      PUSH BC        I A=CODE FOR CHAR. TO BE DISPLAYED
0054" PE 80        131      CP 80M         I TEST VALID RANGE
0056" 38 13       132      JR C,WRCM12    I < 20M
0058" PE 90        133      CP 90M         I > 0A4M
005A" 38 09       134      JR C,WRCM11    I SAVE CURSOR POSITION
005C" 8C 48 3C78  135      LD BC,(UDG)    I TEST IF ASCII PRINTABLE (20M-7FM)
0060" 05          136      DEC B          I YES
0061" 0A 0C       137      JR WRCM13      I TEST IF STD. GRAPHICS(80-0FM)
0063" 18 0C       138      SUB 70M        I USER -DEFINED GRAPHICS(90-44M)
0065" 8C 48 0031" 139      WRCM11 LD BC,(GR7BL) I ADJUST ADDRESS-100M
0065" 0A 40       140      SUB 40M        I ADJUST FOR INDEX INTO TABLE
0068" 18 04       141      JR WRC-13     I ADDRESS OF GRAPHICS TABLE
0068" ED 48 002P" 142      WRCM12 LD BC,(CHTBL) I ADJUST FOR INDEX INTO TABLE
0071" 0A          143      WRCM13 EI DE,HL  I CHARACTER TABLE
0072" 24 00       144      LD H,0         I DE=POSN. IN DISPLAY FILE
0074" 0F          145      LD L,A         I CODE IS INDEX INTO TABLE
0075" 29          146      ADD HL,HL      I
0076" 29          147      ADD HL,HL      I
0077" 29          148      ADD HL,HL      I
0078" 09          149      ADD HL,BC     I
0079" C1          150      POP BC        I HL=PIXEL PATTERN IN TABLE
007A" 08          151      EI DE,HL     I DE=PIXEL PATTERN IN TABLE/HL=DP
007B" 79          152      LD A,C        I TEST IF END OF LINE
007C" 3C          153      DEC A         I
007D" 3A 0033"    154      LD A,(LINLEN) I
0080" 20 03       155      JR NZ,WRCM14  I
0082" 3C          156      INC A         I START OF NEW LINE
0083" 05          157      DEC B         I BUMP LINE NO.
0084" 4F          158      LD C,A        I LINLEN=1+START OF LINE
0085" 18 01       159      JR WRCM15    I
0087" 3C          160      WRCM14 INC A     I
0088" 09          161      WRCM15 CP C     I TEST IF START OF LINE
0089" 05          162      PUSH 0B      I
008A" CC 0143"    163      CALL Z,VFPULO I TEST IF PAST "BOTTOM" LINE
0090" 01          164      POP DE       I
0098" C9          165      WRCM2 PUSH BC   I HERE TO PUT CHARACTER IN DISPLAY FILE
0098" 89          166      PUSH HL      I CURSOR POSITION
0098" 3A 0030"    167      LD A,(MASKB) I DP ADDRESS
0099" 06 PP       168      LD B,-1      I GET OVER AND INVERSE CONTROLS
0099" 1P          169      BRA B,-1     I
0099" 38 01       170      JR C,WRCM3   I IF RORING NEW INTO CLD
0099" 04          171      INC B        I 00=-1 IF RORING #0 IF NOT
0099" 1P          172      WRCM3 BRA B,-1 I
0099" 1P          173      BRA B,-1     I
0099" 0F          174      SBC A,A      I
0099" 4F          175      LD C,A       I C=-1 FOR INVERSE #0 IF NOT
0099" 3E 00       176      LD A,B       I SCAN COUNT
0099" 08          177      EI AP,AP     I SAVE SCAN COUNT IN ALTERNATE
00A0" C1          178      PUSH BC     I SAVE MASK VALUES
00A1" 03          179      PUSH DE     I SAVE CHAR. TABLE ADDR
00A3" 0C 21 0000 180      LD IX,0      I SAVE PTR. TO STACK
00A4" 0D 39       181      ADD IX,SP    I
00A8" 94          182      LD D,(HL)   I READ SCAN LINE FROM DP
00A9" 85          183      PUSH HL     I SAVE ADDRESS
00AA" 23          184      INC HL      I NEXT BYTE
00AB" 9E          185      LD B,(HL)   I READ SCAN LINE FROM NEXT BYTE
00AC" 88          186      RR DE,HL   I SCANS TO HL. LAST OF PCS. IN DE
00AD" 78          187      LD A,0      I TEST IF "OVER" IS ACTIVE
00AE" A7          188      AND B       I
00AF" 28 1A       189      JR NZ,WRCM0R I YES-RETAIN CLD DATA
00B1" 3A 003E"    190      LD A,(OPBIT) I NO- CLEAR CLD DATA AT CHAR.POSN.
00B4" 06 07       191      SUB 7        I
00B6" ED 44       192      NEG         I
00B8" 01 03PP     193      LD BC,03PPH  I MASK
00B8" 28 00       194      JR I,WRCM0R1 I NO NEED TO ADJUST(CHAR.STARTS AT BIT 7)
00BD" 37          195      SCP         I
00BE" CB 10       196      NEXT1 RR B     I SHIFT MASK "A" TIMES
00CB" CB 10       197      RR C        I (0 0'S START AT BIT 1 OF 0, BIT 3 OF 2,
00C2" 3D          198      DEC A       I OR BIT 5 OF 0)
00C3" 28 P0      199      JR NZ,NEXT1  I SCAN LINE FROM DP
00C3" 7C          200      LD A,"      I
00C4" A0          201      AND B       I
00C7" 67          202      LD H,A      I OBTAIN ADJACENT CHAR. PIXELS
00C8" 7D          203      LD A,L      I CLEAR CURRENT CHAR. PIXELS
00C9" A1          204      AND C       I
00CA" 6P          205      LD L,A      I HL=CLD BIT ROW
00CB" 3A 003E"    206      WRCM0R LD A,(OPBIT) I GET STARTING BIT POSN.
00CC" 06 07       207      SUB 7        I
00DD" FD 44       208      NEG         I
00DD" 63          209      PUSH HL     I SAVE SCANS
00DD" 0D 4F 00    210      LD L,(IX)   I PTR. TO CHAR.SET
00DE" 0C 04 01    211      LD M,(IX+1) I
00E0" 44          212      LD B,(HL)   I SCAN LINE FROM (CHAR.SET TO BC
00EA" 0E 00       213      LD C,0      I (PIXELS IN 0, BITS 7-2)

```

```

000C* P5          225      PUSH  AP          ; SAVE A AND FLAGS
000D* 78          226      LD    A,B          ; PIXELS FROM CHAR.SET
000E* E4 FC       227      AND   BFC=4       ; LIMIT TO 6 PIXELS
000F* 47          228      LD    B,A          ;
0011* P1          229      POP   AP          ; RESTORE A AND FLAGS
0012* E1          230      POP   HL          ; RESTORE SCANS
0013* 20 00       231      JR    Z,NORR2     ; NO SHIFT NEEDED
0019* A7          232      AND   A          ; CLEAR CARRY
001A* C8 18       233      NEXTZ  RR    B    ; SHIFT "A" TIMES
001B* C8 19       234      RR    C          ;
001C* 3C          235      DEC   A          ;
001D* 20 P9       236      JR    NZ,NEXTZ   ;
001E* 7C          237      NORR2  LD    A,P    ; SCAN FROM DP
001F* 48          238      XOR   B          ; INSERT/COMBINE NEW CHAR.
0020* 67          239      LD    M,A        ;
0021* 70          240      LC   A,L        ;
0022* A9          241      XOR   C          ;
0023* 6F          242      LD    L,A        ; DP XOR CM.SET->HL
0024* CC 7E 02    243      LD    A,(K+2)    ; TEST IF INVERSE ACTIVE
0025* A7          244      AND   A          ;
0026* 28 1A       245      JR    Z,NOINVERT ;
0027* 3A 003E*    246      LD    A,(D*BIT) ;
0028* 06 07       247      SUB   7          ;
0029* 80 44       248      NEG   ;
002A* 01 PC00     249      LD    BC,B*COOH  ; MASK TO INVERT
002B* 20 00       250      JR    Z,NORR3   ;
002C* A7          251      AND   A          ;
002D* C8 18       252      NEXT3  RR    B    ; SHIFT MASK "A" TIMES
002E* C8 19       253      RR    C          ;
002F* 3C          254      DEC   A          ;
0030* 20 P9       255      JR    NZ,NEXT3   ;
0031* 7C          256      NORR3  LD    A,P    ; INVERT CHARACTER
0032* 48          257      XOR   B          ;
0033* 67          258      LD    M,A        ;
0034* 70          259      LC   A,L        ;
0035* A9          260      XOR   C          ;
0036* 6F          261      LD    L,A        ;
0037* 80          262      NOINVERT BX DE,HL ; DP ADRS. TO HL-SCAN LINE IN DE
0038* 73          263      LD    (HL),B    ; WRITE SCAN LINE TO DP
0039* E1          264      POP   HL        ; PREVIOUS BYTE
003A* 72          265      LD    (HL),D    ; WRITE SCAN LINE TO DP
003B* 01          266      POP   DE        ; CHAR.SET
003C* C1          267      POP   BC        ; OVER/INVERSE INDICATORS
003D* 08          268      RR   AP,AP*    ; RESTORE SCAN COUNT
003E* 24          269      INC   M        ; NEXT SCAN IN DP
003F* 13          270      INC   DE        ; NEXT BYTE IN CHAR.FILE
0040* 3D          271      DEC   A        ; TEST IF MORE SCANS
0041* C2 009F*    272      JP   NZ,WRCM5   ;
0042* 25          273      WRCM7  DEC   M        ; LAST CHAR.SCAN
0043* C0 04A9*    274      CALL UPDATY    ; UPDATE ATTRIBUTE BYTE
0044* E1          275      POP   HL        ; STARTING ADRS.
0045* C1          276      POP   BC        ; CURSOR POSITION
0046* 0D          277      DEC   C        ; ADJUST CURSOR POSITION
0047* 3A 003E*    278      LD    A,(D*BIT) ; ADJUST BIT POSITION
0048* 06 06       279      SUB   6          ;
0049* 32 003E*    280      LD    (D*BIT),A ; STORE UPDATED POSITION
004A* 30 00       281      JR    NC,WPCM7  ; NEXT CHAR. IN SAME BYTE
004B* C4 00       282      ADD   A,B        ; NEXT CHAR. IN NEXT BYTE
004C* 32 003E*    283      LD    (D*BIT),A ; STORE ADJUSTED VALUE
004D* 23          284      INC   HL        ; ADJUST DP ADRS.
004E* CC 0499*    285      WRCM7  CALL STPDSH  ; STORE NEW POSITION
004F* 0E 00       286      GOODRET LD    C,0  ; RETURN BC=0
0050* 06 00       287      ERRRET LD    B,0  ; ENTER HERE WITH C NON-ZERO
0051* C9          288      RET           ;
0052* 0E 01       289      I
0053* 10 P9       290      INVPAR LD    C,1  ; RETURN BC=1 FOR INVALID PARAMETERS
0054* 292        291      ERRRET
0055* 3E 10       292      I
0056* 90          293      TVPULC LD    A,SCRSE  ; TEST IF NEW LINE IS OFF SCREEN
0057* 57          294      SUB   B          ; A=LINE NO.
0058* 3A 002D*    295      LD    D,A        ; SAVE IN D
0059* 8A          296      LD    A,(BOTLN) ; GET BOTTOM LINE
005A* D2 0492*    297      CP    0          ;
005B* 3A 002E*    298      JP   NC,UPDOSH  ; ON SCREEN - RETURN TO WRITE CHAR.
005C* 3D          299      I
005D* 3D          300      LD    A,(SCRCTL) ; VIA UPDATE AND STORE POSITION
005E* 28 00       301      DEC   A          ; TEST SCROLL COUNT
005F* 3E 01       302      JR    NZ,WVPULL  ; DO AUTOMATIC SCROLL USING SCRCTL
0060* 32 002E*    303      LD    A,I        ; REINITIALIZE TO 1
0061* C1          304      LD    (SCRCTL),A ;
0062* C1          305      POP   BC        ;
0063* 0E 03       306      POP   BC        ; DISCARD RETURN TO WRITE CHAR.
0064* 10 0D       307      LD    C,3        ; RETURN BC=3 FOR SCREEN PULL
0065* 32 002E*    308      JR    ERRRET    ;
0066* ED 40 002B* 309      TVPULL LD    (SCRCTL),A ; UPDATE VARIABLE
0067* C3 018C*    310      LD    BC,(SCRCTL) ; GET SCROLL CONTROLS (STARTING LINE
0068* 311        311      I
0069* 312        312      JP   SCRLB    ; AND LINE COUNT)
006A* 313        313      I
006B* 314        314      I
006C* 315        315      I
006D* 316        316      I
006E* 317        317      I
006F* 318        318      I
0070* C0 040E*    319      WSTRD  CALL GEPSTRG  ; HERE FROM BASIC ENTRY TO
0071* C0          320      RET    I        ; WRITE CHARACTER STRING TO SCREEN.
0072* 321        321      I
0073* 322        322      I
0074* 323        323      I
0075* 324        324      I
0076* 325        325      I
0077* 326        326      I
0078* C0 04PD*    327      WSTRG  CALL LDATY   ; ENTRY FROM MACHINE CCDE WITH
0079* 7E          328      WSTLP  LD    A,(HL)    ; ADDRESS OF CHAR.CCDE "STRING"
007A* E8          329      PUSH  HL        ; IN HL AND LENGTH IN BC
007B* C9          330      PUSH  BC        ; GET ATTRIBUTE AND MASK CONTRCLS
007C* CC 0046*    331      CALL WPC=01   ; GET CCDE
007D* 79          332      LD    A,C      ; SAVE ADRS. AND
007E* 8C          333      OR   B          ; COUNT
007F* 20 09       334      JR    NZ,WSTX1 ; WRITE "A"
0080* C1          335      WSTRP3 POP   BC      ; TEST IF GOOD
0081* 336        336      I
0082* 337        337      I
0083* 338        338      I
0084* 339        339      I
0085* 340        340      I
0086* 341        341      I
0087* 342        342      I
0088* 343        343      I
0089* 344        344      I
008A* 345        345      I
008B* 346        346      I
008C* 347        347      I
008D* 348        348      I
008E* 349        349      I
008F* 350        350      I
0090* 351        351      I
0091* 352        352      I
0092* 353        353      I
0093* 354        354      I
0094* 355        355      I
0095* 356        356      I
0096* 357        357      I
0097* 358        358      I
0098* 359        359      I
0099* 360        360      I
009A* 361        361      I
009B* 362        362      I
009C* 363        363      I
009D* 364        364      I
009E* 365        365      I
009F* 366        366      I
00A0* 367        367      I
00A1* 368        368      I
00A2* 369        369      I
00A3* 370        370      I
00A4* 371        371      I
00A5* 372        372      I
00A6* 373        373      I
00A7* 374        374      I
00A8* 375        375      I
00A9* 376        376      I
00AA* 377        377      I
00AB* 378        378      I
00AC* 379        379      I
00AD* 380        380      I
00AE* 381        381      I
00AF* 382        382      I
00B0* 383        383      I
00B1* 384        384      I
00B2* 385        385      I
00B3* 386        386      I
00B4* 387        387      I
00B5* 388        388      I
00B6* 389        389      I
00B7* 390        390      I
00B8* 391        391      I
00B9* 392        392      I
00BA* 393        393      I
00BB* 394        394      I
00BC* 395        395      I
00BD* 396        396      I
00BE* 397        397      I
00BF* 398        398      I
00C0* 399        399      I
00C1* 400        400      I
00C2* 401        401      I
00C3* 402        402      I
00C4* 403        403      I
00C5* 404        404      I
00C6* 405        405      I
00C7* 406        406      I
00C8* 407        407      I
00C9* 408        408      I
00CA* 409        409      I
00CB* 410        410      I
00CC* 411        411      I
00CD* 412        412      I
00CE* 413        413      I
00CF* 414        414      I
00D0* 415        415      I
00D1* 416        416      I
00D2* 417        417      I
00D3* 418        418      I
00D4* 419        419      I
00D5* 420        420      I
00D6* 421        421      I
00D7* 422        422      I
00D8* 423        423      I
00D9* 424        424      I
00DA* 425        425      I
00DB* 426        426      I
00DC* 427        427      I
00DD* 428        428      I
00DE* 429        429      I
00DF* 430        430      I
00E0* 431        431      I
00E1* 432        432      I
00E2* 433        433      I
00E3* 434        434      I
00E4* 435        435      I
00E5* 436        436      I
00E6* 437        437      I
00E7* 438        438      I
00E8* 439        439      I
00E9* 440        440      I
00EA* 441        441      I
00EB* 442        442      I
00EC* 443        443      I
00ED* 444        444      I
00EE* 445        445      I
00EF* 446        446      I
00F0* 447        447      I
00F1* 448        448      I
00F2* 449        449      I
00F3* 450        450      I
00F4* 451        451      I
00F5* 452        452      I
00F6* 453        453      I
00F7* 454        454      I
00F8* 455        455      I
00F9* 456        456      I
00FA* 457        457      I
00FB* 458        458      I
00FC* 459        459      I
00FD* 460        460      I
00FE* 461        461      I
00FF* 462        462      I

```

```

0170 01 337 POP HL ; ADDRESS
017C 23 338 INC HL ; NEXT CHAR.
017D 08 339 DEC BC ; ADJUST COUNT
017E 78 340 LD A,B ; TEST IF DONE
017F 01 341 DR C ;
0180 20 EE 342 JR NZ,WRSTLP ; WRITE NEXT CHAR.
0182 C9 343 RET ; RETURN BC=0
344 ;
0183 79 345 WRSTX1 LD A,C ; SAVE ERROR
0184 PE 01 346 CP 1 ; TEST IF UNRECOGNIZED CCDE
0186 28 F2 347 JR Z,WRSTR3 ; CONTINUE
0188 01 348 POP HL ; REMAINING COUNT TO HL
0189 22 0038 349 LD (STRGCT),HL ; STORE REMAINING COUNT
019C 01 350 POP HL ; ADDRESS TO HL
019D 0E 03 351 LD C,3 ; RETURN BC=3 FOR SCREEN PULL
019E 04 00 352 LD B,0 ;
019F C9 353 RET ;
354 ;
355 ;
356 ;
357 ;
358 ;
360 SUBTTL POSITION CONTROL
361 ;
362 SETC00: ; HERE FROM BASIC ENTRY. PARAMETERS
363 ; IN LINCOL
364 LD BC,(LINCOL)
365 ;
366 SETCUR: ; ENTRY WITH LINE/COL. IN BC REG.
367 CALL TSTPAR ; TEST PARAMETERS FOR VALIDITY
368 CALL CCNVPM ; CONVERT TO INTERNAL POPMAY
369 CALL UPDPOSM ; CALCULATE AND STORE POSITION
370 ; RETURN BC=0
371 ;
373 SUBTTL SCROLL
374 ;
375 ;
376 SCRL00: ; HERE FROM BASIC ENTRY TO SCROLL
377 ; SCREEN
378 LD BC,(SCRYL) ; GET CONTROL INFO.
379 ;
380 SCROLL: ; HERE WITH CONTROLS IN BC
381 ; B=NO. OF LINES
382 ; C=STARTING LINE NO.
383 ; TEST VALIDITY
384 LD A,B
385 AND A
386 JP Z,INVPAR ; ERROR IF COUNT=0
387 ADD A,C
388 CP 1
389 JP C,INVPAR ; ERROR IF B<C<1
390 CP 25
391 JP NC,INVPAR ; ERROR IF B<C>24
392 CALL SCALD ; DO SCROLL
393 JP GOODRET ; RETURN BC=0
394 ;
395 SCRL0: PUSH BC ; GET STARTING LINE IN INTERNAL PGMAY
396 LD A,SCRSL
397 SUB C
398 LD B,A
399 LD A,(LINLEN)
400 LD C,A ; COL. 0
401 CALL LMBU
402 POP BC
403 PUSH HL ; SAVE STARTING ADRS.
404 PUSH SC ; SAVE ORIG.BC FOR EXIT
405 LD A,L ; TEST IF AT START OF BLCK
406 AND A
407 JR Z,ST9LK ; YES
408 RLCA
409 RLCA
410 RLCA
411 LD C,A ; GET NO. OF LINES THIS BLOCK
412 LD A,B
413 SUB C
414 CP B ; TEST AGAINST TOTAL LINES
415 JR C,GRBLK ; TOTAL GREATER THAN THIS BLOCK
416 INSDBLK LD A,B
417 LD B,0 ; REMAINING COUNT
418 PUSH BC
419 LD B,A ; B=LINES THIS BLOCK
420 LD C,B ; C=SCAN COUNT
421 LD A,B
422 PUSH SC ; SAVE SCAN COUNT
423 RRCA
424 RRCA
425 RRCA ; CALCULATE NO. OF BYTES
426 LD C,A ; BC=32*# OF LINES
427 LD B,0 ;
428 ;
429 LGOPI LA DE,HL ; GET ADRS. OF PREV. LINE
430 ADD HL,-20H
431 EI DE,HL
432 EI DE,HL
433 ; TO DE
434 PUSH HL ; SAVE OP ADRS.
435 LDIR ; DO MOVE
436 POP HL ; GET ADRS.
437 INC H ; NEXT SCAN LINE
438 POP BC ; SCAN COUNT
439 DEC C ;
440 JR NZ,LDDPO ; MORE SCANS
441 POP BC ; RESIDUAL LINE COUNT
442 LD A,B
443 AND A
444 JR Z,SCRL1T ; DONE
445 LD L,0 ; START OF NEXT BLOCK
446 JR TESTAD ; DO REMAINING LINE(S)
447 ; ORIG.BC
448 POP DE ; STARTING ADRS.
449 PUSH BC ; SAVE ORIG.BC
450 LD L,B ; NO. OF LINES SCROLLED
451 LD H,0 ;
452 ADD HL,HL ; 32*NO. OF LINES=
453 ADD HL,HL ; NO JP ATTRIBUTE BYTES
454 ADD HL,HL ; TO BE SCROLLED
455 ;

```

0105	29	454	ADD	HL,HL	
0106	44	455	LD	B,M	
0107	40	456	LD	C,L	
0108	42	457	LD	M,D	
0109	48	458	LD	L,E	
0110	CO 0420	459	CALL	CALCATT	
0111	88	460	EX	DE,HL	
0112	21 PFE8	461	LD	HL,-720H	
0113	19	462	ADD	HL,DE	
0114	88	463	EX	DE,HL	
0115	EC 06	464	LDIR		
0116	C1	465	PDP	BC	
0117	77	466	LD	A,C	
0118	80	467	ADD	A,B	
0119	3D	468	DEC	A	
0120	4P	469	LD	C,A	
0121	06 01	470	LD	B,1	
0122	18 45	471	JR	CLRSCO	
		472			
0123	4P	473	ORBLK	LD	C,A
0124	78	474		LD	A,B
0125	91	475	SUB	C	
0126	47	476	LD	B,A	
0127	C9	477	PUSH	BC	
0128	79	478	LD	A,C	
0129	18 82	479	JR	LEDP	
		480			
0130	09	481	STBLE	DEC	B
0131	C9	482		PUSH	BC
0132	08 08	483		LD	C,0
0133	C9	484	STBLEK	PUSH	BC
0134	88	485	STBLE1	EX	DE,HL
0135	21 P888	486		LD	HL,-720H
0136	19	487		ADD	HL,DE
0137	80	488		EX	DE,HL
0138	01 0020	489		LD	BC,32
0139	89	490		PUSH	HL
0140	80 80	491		LDIR	
0141	81	492		PDP	HL
0142	24	493		INC	H
0143	71	494		PDP	BC
0144	0C	495		DEC	C
0145	20 80	496		JR	NZ,STBLEK
0146	C1	497		PDP	BC
0147	78	498		LD	A,0
0148	47	499		AND	A
0149	20 89	500		JR	Z,SCRLET
0150	11 P820	501		LD	DE,-720H
0151	19	502		ADD	HL,DE
0152	C3 01CC	503		JP	TESTAD
		504			
		505			
		506			
		507		SUBTYL	CLEAR SCREEN
		508			
		509			
0153	80 48 000C	510	CLRS001		
0154		511		LD	BC,(CCLUCTL)
		512			
0155		513	CLRS01		
		514			
0156	78	515		LD	A,0
0157	47	516		AND	A
0158	C4 013P	517		JP	Z,INVPAR
0159	81	518		ADD	A,C
0160	P8 01	519		CP	1
0161	8A 013P	520		JP	C,INVPAR
0162	P8 19	521		CP	25
0163	D2 013P	522		JP	NC,INVPAR
0164	C0 026P	523		CALL	CLRS00
0165	C3 013P	524		JP	0BCDRET
		525			
		526			
0166	C0 04PB	527	CLRS00	CALL	LDATP
0167	C3	528		PUSH	BC
0168	38 18	529		LD	A,SCRZI
0169	91	530		SUB	C
0170	47	531		LD	B,A
0171	2A 0033	532		LD	A,(LINLEN)
0172	3C	533		INC	A
0173	4P	534		LD	C,A
0174	C0 047P	535		CALL	LD0U
0175	58	536		LD	B,0
0176	59	537		LD	H,C
0177	C1	538		PDP	BC
0178	85	539		PUSH	DE
0179	78	540	CLRS01	LD	A,L
0180	87	541		RLCA	
0181	87	542		RLCA	
0182	87	543		RLCA	
0183	4P	544		LD	C,0
0184	38 88	545		LD	A,0
0185	91	546		SUB	C
0186	88	547		CP	0
0187	38 47	548		JR	C,CLRS07
0188	78	549	CLRS02	LD	A,0
0189	08 88	550		LD	B,0
0190	C1	551		PUSH	BC
0191	47	552	CLRS03	LD	B,A
0192	08 88	553		LD	C,0
0193	78	554	CLRS04	LD	A,0
0194	C9	555		PUSH	BC
0195	04 87	556		AND	7
0196	8P	557		RRCA	
0197	8P	558		RRCA	
0198	8P	559		RRCA	
0199	4P	560		LD	C,A
0200	06 88	561		LD	B,0
0201	88	562		DEC	C
0202	88	563	CLRS05	PUSH	HL


```

0298 54 364 LO D,M
029C 5C 365 LO E,L
029D 36 00 366 LO (CML),00 ; CLEAR DP
029F 13 367 INC DE
02A0 ED 00 368 LOIR
02A2 C1 369 POP HL
02A3 24 370 INC H ; NEXT SCAN ROW
02A4 C1 371 POP BC ; POP LINE/SCAN COUNT
02A5 0C 372 DEC C ; DECR. SCAN COUNT
02A6 20 E7 373 JR NZ,CLRSC4 ; NEXT SCAN
02A8 E5 374 PUSH HL ; SAVE NEXT DP ADRS.
02A9 25 375 DEC H ; LAST SCAN DP CHAR.
02AA CD 04ED 376 CALL CALCATY ; GET ADRS. IN ATTRIBUTE FILE
02AD 78 377 LD A,B ; NO. OF LINES THIS BLOCK
02AE E6 07 378 AND 7
02B0 0F 379 RRCA
02B1 0F 380 RRCA
02B2 0F 381 RRCA
02B3 4F 382 LD C,A ; BC=320ND. OF LINES
02B4 06 00 383 LD B,0
02B6 0C 384 DEC C ; ADJUST
02B7 54 385 LD D,M
02B8 50 386 LD E,L
02B9 3A 003A 387 LD A,(ATBTBYT) ; ATTRIBUTE VALUE
02BC 77 388 LD (CML),A ; UPDATE ATTRIBUTE FILE
02BD 13 389 INC DE ;
02BE ED 00 390 LOIR ;
02C0 E1 391 POP HL ; DP ADRS.(LAST SCAN+100M)
02C1 C1 392 POP BC ; TOTAL LINE COUNT
02C2 78 393 LD A,B
02C3 A7 394 AND A
02C4 28 05 395 JR Z,CLRSXT
02C6 2E 00 396 LD E,0 ; ML=START DP NEXT BLOCK
02C8 C3 0270 397 JP CLASC1 ; B=REMAINING LINE COUNT
02CB C1 398 CLRST POP BC ; CURSOR POSITION
02CC C3 0452 399 JP UPDPDSH ; RETURN VIA UPDATE AND STORE POSITION
02CF 4F 400 CLRSC7 LD C,A ; SAVE NO. LINES THIS BLOCK
02D0 76 401 LD A,B
02D1 91 402 SUB C ; ADJUST TOTAL LINE COUNT
02D2 47 403 LD B,A
02D3 C5 404 PUSH BC ; REMAINING LINE COUNT
02D4 79 405 LD A,C ; LINES THIS BLOCK
02D5 10 05 406 JR CLRSC3 ;
407 ;
408 ; SUBTTL "GET" ROUTINES
409 ;
410 ;
411 ;
02D7 412 ; GTCH00: ; HERE FROM BASIC ENTRY TO GET CHARACTER
02D8 413 ; ; (RETURNS CODE FOR CHARACTER AT
02D9 414 ; ; POSITION SPECIFIED IN GETCTL)
02DA 415 ; LD BC,(GETCTL) ; GET CONTROL INFO.
02DB 416 ;
02DC 417 ; GTCHAR: ; ENTRY WITH POSITION IN BC
02DD 418 ; CALL TSTPAR ; TEST PARAMETERS
02DE 419 ; LD A,(CPBIT) ; SAVE CURRENT BIT POSITION
02DF 420 ; PUSH AF
02E0 421 ; CALL CONVPM ; CONVERT TO INTERNAL FORMAT
02E1 422 ; CALL CALCPDS ; GET DP ADDRESS AND BIT POS.
02E2 423 ; LD DE,(CHTBL) ; CHAR.TABLE
02E3 424 ; LD B,96 ; NO. OF PRINTABLE CHARACTERS
02E4 425 ; LD A,BCHM ; SET ADJUSTMENT INDEX
02E5 426 ; GTCH1 LD (GTINDX),A
02E6 427 ; INC D ; ADJUST TO START DP TABLE
02E7 428 ; GTCH2 PUSH BC ; SAVE COUNT
02E8 429 ; PUSH HL ; SAVE ADRS. IN DP
02E9 430 ; PUSH DE ; SAVE ADRS. IN CHAR.TABLE
02EA 431 ; CALL GTC2BC ; GET SCAN LINE FROM DP TO REG.B
02EB 432 ;
02EC 433 ; GTCH22 LD A,B ;
02ED 434 ; AND OPCM ; & BITS TO A
02EE 435 ; LD B,A ; DP CHAR.SCAN ROW IN B
02EF 436 ; EX DE,HL ; CHAR.SET PTR. TO HL
02F0 437 ; LD A,(CML) ; PIXEL ROW FROM CHAR.SET
02F1 438 ; AND OPCM ; MASK TO 6 BITS
02F2 439 ; XOR B ; TEST AGAIN DP IMAGE
02F3 440 ; JR Z,GTCH3 ; MATCH
02F4 441 ; PUSH AF
02F5 442 ; LD A,(GTINDX) ; TEST IF LOOKING AT STD.GRAPHICS
02F6 443 ; CP 90H
02F7 444 ; JR NZ,GTCH23
02F8 445 ; POP AF
02F9 446 ;
02FA 447 ; GTCH23 JR GTCH4 ; DO NOT TEST INVERSE IF STD.GR.
02FB 448 ; POP AF
02FC 449 ; CP OPCM ; TEST IF INVERSE
02FD 450 ; JR NZ,GTCH4 ; DOES NOT MATCH
02FE 451 ; LD C,A ; C=0 FOR MATCH -4 FOR INVERSE
02FF 452 ; GTCH3 LD B,7 ; SCAN COUNT
0300 453 ; EX DE,HL ; DP ADRS. TO HL
0301 454 ; PUSH BC ; SAVE MATCH AND SCAN COUNT
0302 455 ; INC H ; NEXT SCAN IN DP
0303 456 ; INC DE ; NEXT ROW IN CHAR.SET
0304 457 ; CALL GTC2BC ; NEXT SCAN ROW TO BC
0305 458 ; LD A,B
0306 459 ; AND OPCM
0307 460 ; EX DE,HL ; CHAR.SET PTR. TO HL
0308 461 ; LD A,(CML) ; PIXEL ROW TO A
0309 462 ; AND OPCM
0310 463 ; XOR B ; TEST MATCH
0311 464 ; POP BC ; GET PREV.MATCH AND SCAN COUNT
0312 465 ; XOR C ;
0313 466 ; JR NZ,GTCH4 ; NO MATCH-START AGAIN AT NEXT
0314 467 ; DJNZ GTCH31 ; DO NEXT SCAN
0315 468 ; LD A,C ;
0316 469 ; ADD A,3 ; A=-4 IF MATCH ON INVERSE
0317 470 ; POP BC ; SET TC -1 IF MATCH ON INVERSE
0318 471 ; POP BC ; HERE IF MATCH=(SP)=72,TC CHAR.
0319 472 ; POP BC ; IN TABLE(CSP+2)=CHAR.IN CP;
0320 473 ; LD C,A ; (CSP+4)=CHAR.COUNT
0321 474 ; LD C,A ; B=COUNT C-1 IF MATCH ON INVERSE
0322 475 ; LD A,(GTINDX) ; CONVERT MATCH TO CHAR.CODE
0323 476 ; SUB B ;
0324 477 ; CP 20H ; TEST IF MATCH ON SPACE
0325 478 ; JR NZ,GTCH32 ; NO
0326 479 ; INC C ; TEST IF MATCH ON INVERSE
0327 480 ; JR NZ,GTCH32

```

```

0340* 3E 0F          480          LD      A,0FH          I LOAD CODE FOR GRAPHICS BLOCK
0342* 4F          481          LD      C,A          I RETURN IN REG. C OF BC PAIR
0343* 04 00          482          LD      B,0          I AND IN REG. A
0345* 01          483          POP     AP          I RESTORE DPBIT
0346* 32 003E*      484          LD      (DPBIT),A   I RESTORE DPBIT
0349* 79          485          LD      A,C          I CHAR.CODE TO A
034A* C9          486          RET
;
034B* 21          487          I
034C* 11 0000       488          GTCM4  POP     HL          I PTR. TO CNR. SET
034F* 19          489          LD      DE,B          I MOVE CN TO NEXT CHARACTER
0350* 5D          490          ADD     HL,DE
0351* 54          491          LD      B,L
0352* 21          492          LD      D,M
0353* C1          493          POP     HL          I DP ADDRESS
0354* 10 9E        494          POP     BC          I CHAR. COUNT
0355*          495          OJNZ  GTCM2        I DECREMENT CHAR.COUNT AND LOOP AGAIN
;
0356*          496          I
0357* 3A 003A*      497          LD      A,(GTINDX)   I TEST IF DONE
0358* 0E 80        498          CP      80H          I TEST IF STD.CHG.SET
0359* 20 0A        499          JR      NZ,GTCM5     I TRY GRAPHICS
035A* 0C 50 0031*  500          LD      DE,(GRTBL)   I GRAPHICS TABLE
035B* 04 10        501          LD      B,16          I NO. OF ENTRIES
035C* 3E 90        502          LD      A,90H
035D* 10 09        503          JR      GTCM1
035E* 0E 90        504          GTCM5  CP      90H          I TEST IF STD.GRAPHICS
035F* 20 0C        505          JR      NZ,GTCM6     I
0360* 0C 50 5C7B   506          LD      DE,(UDG)     I TRY USER-DEFINED GRAPHICS
0361* 15          507          DEC     D            I ADJUST ADDRESS-100H
0362* 04 19        508          LD      B,21          I NO. OF ENTRIES
0363* 1E 45        509          LD      A,BASH       I INDEX ADJUST
0364* C3 02P0*     510          JP      GTCM1
;
0365*          511          I
0366*          512          I
0367*          513          GTCM6  POP     AP          I RESTORE ORIG. DPBIT
0368* 32 003E*     514          LD      (DPBIT),A   I
0369* 00          515          LD      C,B          I BC=B
0370* 4F          516          XOR     A            I AND
0371* C9          517          RET
;
0372*          518          I
0373*          519          I GET ATTRIBUTE BYTE
;
0374*          520          I
0375*          521          I HERE FROM BASIC ENTRY
0376* 0C 40 0019*  522          GETAB0: LD     BC,(GETCTL) I HERE WITH LINE/COL. IN BC
0377*          523          I
0378*          524          GETATT:
0379* 00 0416*      525          CALL   YSTPAR        I TEST PARAMETERS
0380* 3A 003E*     526          LD      A,(DPBIT)   I SAVE CURRENT BIT POSITION
0381* 05          527          PUSH   AP
0382* 00 0447*     528          CALL   CONVPM        I CONVERT TO INTERNAL FORMAT
0383* 00 0457*     529          CALL   CALCPDS       I CALCULATE DP POSITION
0384* 00 0460*     530          CALL   CALCATT       I CALCULATE ATTRIBUTE POSITION
0385* 7E          531          LD      A,(HL)       I ATTRIBUTE BYTE TO A
0386* 4F          532          LD      C,A          I PASS BACK IN BC
0387* 04 00        533          LD      B,0
0388* 01          534          POP     AP          I RESTORE (DPBIT)
0389* 32 003E*     535          LD      (DPBIT),A   I
0390* 79          536          LD      A,C          I ATTRIBUTE ALSO IN A
0391* C9          537          RET
;
0392*          538          I
0393*          539          I
0394*          540          I GET CURSOR POSITION
;
0395*          541          I HERE FROM BASIC ENTRY
0396*          542          GETCUR:
0397* 00 00 003A*   543          LD      BC,(CURPOS) I GET INTERNAL POSITION
0398* 00 0447*     544          CALL   CONVPM        I CONVERT TO USER FORMAT
0399* 3A 0033*     545          LD      A,(LINLEN)  I TEST IF END OF LINE
0400* 09          546          CP      C            I IF =40 OR 42
0401* 20 00        547          JR      NZ,GTC1     I NO
0402* 9E 00        548          LD      C,0          I NEXT POSN.START OF NEXT LINE
0403* 7E          549          LD      A,0
0404* 3C          550          INC     A            I BUMP TO NEXT LINE
0405* 47          551          LD      B,A
0406* 0E 10        552          CP      24          I YES? IF OFF SCREEN
0407* 30 02        553          JR      C,GTC1     I NO
0408* 04 17        554          LD      B,23          I POSN. IS AT BOTTOM LINE
0409* 0C 43 0007*  555          GTC1  LD      (LINCCL),BC I STORE FOR BASIC PROGRAM
0410* C9          556          RET          I VALUES ALSO IN BC
;
0411*          557          I
0412*          558          SUBTTL INITIALIZE 40-COL.MODE
;
0413*          559          I
0414*          560          I
0415*          561          I
0416*          562          I
0417*          563          INIT40:
0418*          564          LD      HL,SCINIT   I INITIALIZE VARIABLES
0419* 21 1701       565          LD      (SCRCCTL),HL I SCROLL CONTROL
0420* 22 0020*     566          LD      A,23          I
0421* 3E 17        567          LD      (BOTLM),A   I BOTTOM LINE
0422* 32 002D*     568          LD      A,1          I
0423* 3E 01        569          LD      (SCROLLCT),A I SCROLL COUNT
0424* 32 002E*     570          LD      A,40          I
0425* 3E 28        571          LD      (LINLEN),A  I LINE LENGTH
0426* 32 0033*     572          LD      A,1          I
0427* 3E 01        573          LD      (MARGIN),A  I MARGIN
0428* 32 0030*     574          LD      (CLRCTL),HL I CLEAR SCREEN CONTROL
0429* 21 1800       575          LD      HL,CLRCTL   I
0430* 22 000C*     576          XOR     A            I
0431* 4F          577          LD      (STRCTT),A   I STRING COUNT
0432* 32 0038*     578          LD      (STRCTT+1),A I
0433* 32 003C*     579          LD      HL,CHRSET    I STD. 40-COL.CHAR.SET
0434* 21 0423*     580          LD      (CHTSL),HL  I
0435* 22 002F*     581          LD      HL,GRPHST   I STD. 40-COL.GRAPHICS SET
0436* 21 0729*     582          LD      (GRTBL),HL  I
0437* 22 0031*     583          LD      (GRTBL),HL  I INITIALIZE BASIC INPUTS
;
0438*          584          I
0439*          585          LD      (DATAS),A   I DATA BYTE
0440* 32 0007*     586          LD      (LINCCL),A  I COLUMN
0441* 32 0008*     587          LD      (LINCCL+1),A I LINE
0442* 32 0019*     588          LD      (GETCTL),A  I "GET" COLUMN
0443* 32 001A*     589          LD      (GETCTL+1),A I "GET" LINE
;
0444*          590          I

```

```

03PB 21 0000
03PE 22 5C70
0401 3A 0033
0404 3C
0405 4P
0406 06 10
0408 CC 0452
0408 C3 013A

791 LD HL,0
792 LD (COORDS),HL
793 LD A,(CLINLEN)
794 INC A
795 LD C,A
796 LD B,SCRSZ
797 CALL UPDPOSN
798 JP GOODRET
799
800
801
802
803 SUBTYL INTERNAL SUB-RTMS.
804
805
806 GETSTRG LD A,(PARAM0)
807 AND 1FH
808 OR 40H
809 LD D,A
810 LD HL,(VARS)
811 GTSTR1 LD A,(HL)
812 AND 07FH
813 JR Z,NOSTRG
814 CP 0
815 JR Z,GTSTR2
816 PUSH DE
817 CALL RECLEN
818 EX DE,HL
819 POP DE
820 JR GTSTR1
821 GTSTR2 INC HL
822 LD C,(HL)
823 INC HL
824 LD B,(HL)
825 INC HL
826 LD A,B
827 OR C
828 RET
829
830 NOSTRG LD C,2
831 LD B,0
832 RET
833
834
835
836
837
838
839
840
841 TSTPAR LD A,23
842 CP B
843 JR NC,TSTPR1
844 PARERR POP AF
845 JP INVPAR
846 TSTPR1 LD A,(CLINLEN)
847 DEC A
848 CP C
849 JR C,PARERR
850 RET
851
852
853
854
855
856
857
858
859
860
861
862 CONVPM LD A,(CLINLEN)
863 INC A
864 SUB C
865 LD C,A
866 LD A,SCRSZ
867 SUB B
868 LD B,A
869 RET
870
871
872
873 UPDPOSN:
874
875 CALL CALCPDS
876 JR STPCSN
877
878
879
880
881
882
883 CALCPDS CALL LMBU
884 LD A,(CLINLEN)
885 INC A
886 SUB C
887 PUSH AF
888 LD E,A
889 ADD A,A
890 ADD A,E
891 SRL A
892 SRL A
893 LD E,A
894 LD A,(MARGIN)
895 ADD A,E
896 LD B,A
897 LD D,0
898 ADD HL,DE
899 LD E,7
900 POP AF
901 AND 3
902 JR Z,CALCP2
903 LD E,A
904 DEC A
905 CALCP2 ADD A,E
906 LD (CPBIT),A

```

```

0470* C9          907      RET          ; BC=POS. ML=CP ADDRESS
                908      |
                909      |
0471*          910      LDBUI         ; GET DISPLAY FILE ADDRESS
                911      |          ; FOR START CP LINE IN B
0472* 38 10      912      LD  A,SCN5Z
0480* 90        913      SUB  0
0481* 97        914      LD  0,A
0482* 0P        915      RRCA
0483* 0P        916      RRCA
0484* 0P        917      RRCA
0485* 84 10      918      AND  000H
0487* 6P        919      LD  L,A
0488* 7A        920      LD  A,0
0489* 84 10      921      AND  10H
048A* P6 40      922      OR  40H
048B* 67        923      LD  M,A
048C* 3A 5CC2     924      LD  A,(VIEW00) ; TEST IF USING DP1
0491* C8 47      925      BIT  0,A
0493* C8        926      RET  I
0494* 38 20      927      LD  A,20H ; USING DP2
0496* 84        928      OR  H
0497* 67        929      LD  M,A
0498* C9        930      RET
                931      |
0499*          932      STP0SI:      ; STORE CURSOR POSITION
0499* 60 43 0034* 933      LD  (CURPOS),BC
049D* 22 0036*   934      LD  (CPADR5),ML
04A0* C9        935      RET
                936      |
04A1*          937      LD0SI:      ; LOAD CURSOR POSITION
04A1* ED 40 0034* 938      LD  BC,(CURPOS)
04A3* 2A 0036*   939      LD  ML,(CPADR5)
04A8* C9        940      RET
                941      |
                942      |
04A9*          943      UPDAT1:      ; UPDATE ATTRIBUTE BYTE FOR CHARACTER
                944      |          ; JUST PRINTED
                945      |          ; ML = ANY SCAN OP CHAR. IN OP
                946      |          ;
                947      |          ; IF CHARACTER SPANS ATTRIBUTE BYTE
                948      |          ; BOUNDARY, BOTH ATTRIBUTE BYTES WILL
                949      |          ; BE ADJUSTED
                950      |
                951      |
04A9* C0 04E0*   952      CALL  CALCATY ; ADRS. OP ATTRIBUTE BYTE TO ML
04AC* 3A 003F*   953      LD  A,(CPBIT) ; SAVE BIT POSITION
04AD* P3        954      PUSH  AP ;
04B0* 3A 0039*   955      LD  A,(ATTBYT) ; CURRENT ATTRIBUTE BYTE VALUE
04B3* 9F        956      LD  0,A ; TO B
04B5* 3A 003F*   957      LD  A,(ATTMSK) ; MASK TO D
04B7* 37        958      LD  0,A
04B8* 3A 0030*   959      LD  A,(MASK0) ; FLAGS
04B9* 67        960      LD  0,A ; TO B
04BC* 7E        961      UPDAT0  LD  A,(ML) ; BYTE FROM ATTRIBUTE FILE
04BD* A0        962      XOR  B
04BE* A2        963      AND  0
04BF* A0        964      XOR  B ; NEW ATTR. FOR 0; OLD FOR 1
04C0* C0 60      965      AND  4,0 ; SET INK COMPLEMENT OF PAPER?
04C2* 28 00      966      JR  Z,UPDAT1 ;
04C4* 86 P0      967      AND  0P0H ; SET INK TO BLACK
04C6* C0 6P      968      BIT  0,A ; IS PAPER 4-7
04C8* 20 02      969      JR  NZ,UPDAT1 ; YES - USE BLACK INK
04CA* P6 07      970      OR  7 ; NO - USE WHITE INK
04CC* C0 70      971      UPDAT1  BIT  6,P ; SET PAPER COMPLEMENT OF INK?
04CD* 28 00      972      JR  Z,UPDAT2 ;
04CE* 86 C7      973      AND  0C7H ; SET PAPER TO BLACK
04CF* C0 57      974      BIT  2,A ; IS INK 4-7
04D0* 20 02      975      JR  NZ,UPDAT2 ; YES - USE BLACK PAPER
04D2* P6 30      976      OR  30H ; NO - USE WHITE PAPER
04D4* 77        977      UPDAT2  LD  (ML),A ; TO ATTRIBUTE FILE
04D5* 3A 003E*   978      LD  A,(CPBIT) ; TEST BIT POSITION
04D6* P6 05      979      CP  5
04D8* 30 00      980      JR  NC,UPDAT3 ;
04D9* 23        981      |
04E1* 38 07      982      INC  ML ; CHAR. IN SINGLE BYTE IF STARTS
04E3* 32 003E*   983      LD  A,7 ; IN BIT 7 OR 5
04E5* 10 04      984      LD  (CPBIT),A ; CHAR. SPANS 2 BYTES IF STARTS
                985      JR  UPDAT0 ; IN BIT 3 OR 1
                986      |          ; TEMPORARILY SET TO 7
                987      |          ; UPDATE NEXT BYTE
                988      |          ; (CPBIT) WILL FORCE EXIT AND
                989      |          ; ORIGINAL VALUE OF CPBIT WILL
                990      |          ; BE RESTORED)
                991      |
04E9* P1        992      UPDAT3  POP  AP ; ORIGINAL CPBIT
04EB* 32 003E*   993      LD  (CPBIT),A ; RESTORE
04EC* C9        994      RET
                995      |
                996      |          ; SUB-RTN. TO CALCULATE ADRS. OP
                997      |          ; ATTRIBUTE BYTE FROM ADRS. OP
                998      |          ; ANY SCAN ROW IN OP
                999      |          ;
                1000      |          ; USED BY UPDAT1 AND GETATT
                1001      |
04ED* 7C        1001      CALCATY  LD  A,H ; UPPER BYTE OP ADDRESS
04EE* 0P        1002      RRCA
04EF* 0P        1003      RRCA
04F0* 0P        1004      RRCA
04F1* 86 03      1005      AND  03H
04F3* P6 50      1006      OR  50H ; ADRS. OP ATTRIBUTE BYTE
04F5* C0 4C      1007      BIT  0,H ; TEST WHICH OP
04F7* 20 02      1008      JR  Z,CALCAL ; DP1
04F9* P6 20      1009      OR  20H ; DP2
04FB* 67        1010      CALCAL  LD  M,A ; ADRS. OP ATTRIBUTE
04FC* C9        1011      RET
                1012      |
04FD*          1013      LDATT1:      ; LOAD INTERNAL ATTRIBUTE VARIABLES
                1014      |          ; FROM SYSTEM VARIABLES
                1015      |
                1016      |          ;
                1017      |          ; SAVE A
                1018      |          ;
                1019      |          ;
                1020      |          ;
                1021      |          ;
04FD* P9        1016      PUSH  AP
04FE* 3A 5C6D     1017      LD  A,(ATTR_P)
0501* 32 0039*   1018      LD  (ATTBYT),A
0502* 3A 5C8E     1019      LD  A,(MASK_P)
0503* 32 003F*   1020      LD  (ATTMSK),A
0504* 3A 5C91     1021      LD  A,(CP_FLAG)
0509* 0P        1022      RRCA ; SHIFT ODD BITS TO EVEN

```

```

0508' 32 0038'      1023      LD (MASKB),A      1
0511' #1            1024      POP AP
0512' C9            1025      RET
1026      1
1027      1
1028      1          ; RTN. USED BY GTCM4R TO PUT
1029      1          ; 4 PIXELS FROM OP IN B REG. BITS 7-2
1030      1          ; FOR WATCHING AGAINST CHAR.SET
0513' P5            1031      GTC28C  PUSH  ML      ; SAVE OP ADDR.
0514' 44            1032      LD      B,(HL)      ; SCAN LINE FROM OP
0515' 23            1033      INC     ML          ; NEXT BYTE
0516' 44            1034      GTC281  LD      C,(HL)      ; BC CONTAINS CHAR.AT DPBIT
0517' 3A 0038'     1039      LD      A,(DPBIT)
0518' D6 07        1036      SUB     7
0519' EC 44        1037      NEG
1038      JR      Z,GTC282      ; A=NO.OP BITS OFFSET IN BC
0520' C8 11        1039      GTC5MPT RL  C          ; STARTS AT BIT 7
0521' C8 10        1040      RL      B
0522' 30           1041      DEC     A          ; SNIPT 4 PIXELS FOR CHARACTER
0523' 20 #9        1042      JR      NZ,GTC5MPT ; 1 TO B. BITS 7-2
0524' E1           1043      GTC282  POP  ML
0525' C9           1044      RET
1045      1
1046      1
1047      1          INCLUDE CMST80
1048      1          SUBTTL CHAR.SET FOR 40/80 COL.MODES
1049      1
1050      1
1051      1          ; Det patterns for characters on the TV screen!
1052      1          ; character with code = 16 at offsets 5x (top line) to 8x+7 (bottom line)
1053      1          ; 0 = white, 1 = black, no margins between character spaces either
1054      1          ; vertically or horizontally
1055      1
0526' 00           1056      CHRST  defb 00000000b      ; code 10      space
0527' 00           1057      defb 00000000b
0528' 00           1058      defb 00000000b
0529' 00           1059      defb 00000000b
0530' 00           1060      defb 00000000b
0531' 00           1061      defb 00000000b
0532' 00           1062      defb 00000000b

```

See APPENDIX C-2 for Character Set

ADL - ASC 003 40-COL.MODE SUPPORT CR180/11 version 10.36.14 14-Mar-84 12143123
 CHAR.SET FOR 40/80 COL.MODES CMST80.SRC

A	Reserved	ATTSY	0039	ATTMSK	003F	ATTRBP	0C8D	B	Reserved
BOTLN	002D	C	Reserved	CALCAT	04E0	CALCA1	04FB	CALCP0	0457
CALCP2	0479	CHRSET	0429	CHRST	0529	CHYBL	002F	CLINIT	1900
CLRCYL	000C	CLRSB0	024F	CLRSB	050E IN	CLRSCN	0253 IN	CLRSCD	0269
CLRSC1	027D	CLRSC2	0288	CLRSC3	028C	CLRSC4	028F	CLRSC5	029A
CLRSC7	02CF	CLRSXT	02CB	CONVPM	0447	COORDS	5C7D	CURPOS	0034
D	Reserved	DATAB	0000	DPADRS	0036	DPBIT	003E	E	Reserved
ERRRET	013C	GETAB0	037E	GETATB	001E IN	GETATY	0382 IN	GETC80	039C
GETCTL	0019	GETCUS	0021 IN	GETCUR	039C IN	GETC1	0384	GETSTR	0408
GDDDR8	013A	GRBLK	0274	GRPST	0729	GRPST	0029	GRYBL	0031
GTCM4R	020B IN	GTCM80	0207	GTCM8B	001B IN	GTCM1	02F0	GTCM2	02F4
GTCM22	02FA	GTCM23	0310	GTCM3	0315	GTCM31	0318	GTCM32	0342
GTCM4	034B	GTCM5	0367	GTCM6	0377	GTCM4	0520	GTC28C	0513
GTC281	0516	GTC282	0527	GTINDX	003A	GTSMP	0419	GTSYR2	0429
H	Reserved	INITAB	0025 IN	INIT40	0389 IN	INSDBL	010A	INYPAR	713F
L	Reserved	LDATTR	04FD	LDPDSM	04A1	LINCOL	0007	LINLEN	0035
LNBU	047E	LOOP	01DE	LODP0	01E1	LQDP1	01E9	M	Reserved
MARGIN	003D	MASKB	0038	MASKBP	0C8E	NEXT1	008E	NEXT2	00E6
NEXT3	0176	NOINVE	0113	NORR1	0CC5	NORR2	00E0	NORR3	0100
NOSTRG	0431	NOXJR	00C8	PARAMS	5CC3	PARERR	0438	PSW	Reserved
PPPLAG	5C91	RECLN	1720	SCINIT	1701	SCRCTL	002B	SCRBL	002A IN
SCRLS0	01A2	SCRCLY	002E	SCRCLY	0201	SCRLO	019C	SCRCLL	01A6 IN
STBLK	022C	SETC80	0192	SETC8U	0009 IN	SETCUR	0196 IN	SP	Reserved
TESTAD	01CC	SPARE1	0013	SPARE2	0015	SPARE3	0017	SPARE4	0024
UDG	5C7B	STBLK0	0230	STBLK1	0231	STPDSM	0499	STRCT	003B
UPDAT3	04E8	TSTPAR	0434	YSTPR1	043F	TVPULQ	0143	TVPUL1	015F
WRCHR8	0001 IN	UPDATY	04A9	UPDAT0	C4BC	UPDAT1	04CC	UPDAT2	04D0
WRCH12	006D	UPDOS	0452	VARS	5C48	VIDMOD	5CC2	WRCH4R	0043 IN
WRCH3	0099	WRCHXT	0137	WRCH0	004D	WRCH01	0046	WRCH11	0065
WRSTR8	0004 IN	WRCH13	0071	WRCH14	0087	WRCH15	0088	WRCH2	008E
		WRCH5	009F	WRCH6	00AB	WRCH7	0120	WRSTLP	0170
		WRSTRG	014D IN	WRSTR0	0369	WRSTR3	017A	WRSTX1	0183

No errors detected

Cross reference listing (MREF version 4.7)

Symbol	Refs (# = definition 0 = write <blank> = read)
ATTSY	1110 587 955 10180
ATTMSK	1190 957 10206
ATTR_P	480 1017
BOTLN	950 296 7678
CALCA1	1008 10100
CALCAT	489 574 730 932 10010
CALCP2	902 9050
CALCP0	622 729 875 8830
CHRSET	400 103 779
CHRST	40 10560
CHYBL	1030 152 623 7800
CLINIT	390 774
CLRCYL	730 511 7756
CLRSB0	70 5100
CLRSC0	471 523 5270
CLRSC1	5400 597
CLRSC2	5490
CLRSC3	5520 606
CLRSC4	5540 573
CLRSC5	5630
CLRSC7	540 6000

CLRSCB	31	760						
CLRSCW	20	9130						
CLRSX7	999	9900						
CONVPM	300	621	720	744	0020			
COORNS	470	7020						
CURPDS	1000	743	9330	930				
DATAB	600	125	7050					
DPADDS	1000	9340	930					
DPBIT	1100	201	117	240	270	2000	2030	
		610	0040	7140	720	7350	0000	
		953	970	0040	9010	1030		
		291	500					
ERRRET	2070							
GETAB0	04	7220						
GETAT0	32	840						
GETATT	20	7240						
GETC1	747	753	7550					
GETC00	05	7410						
GETCYL	010	615	712	7000	7000			
GETCUB	32	050						
GETCUR	29	7420						
GETSTR	319	0000						
GOODRE	2000	370	392	520	700			
GRBLK	415	4730						
GRPHST	410	104	701					
GRPST	01	19200						
GRFBL	1040	149	700	7020				
GTC201	10300							
GTC202	1030	10630						
GTC20C	031	454	10310					
GTCM1	6200	703	710					
GTCM2	6200	095						
GTCM02	0330							
GTCM03	040	6670						
GTCM3	040	4500						
GTCM31	0520	467						
GTCM32	077	079	0010					
GTCM4	040	040	000	0000				
GTCM5	090	7040						
GTCM6	705	7130						
GTCM0K	29	0170						
GTCM00	03	0120						
GTCM00	32	030						
GTCM0P	10300	1042						
GTIMEK	1130	0260	042	074	097			
GTSTR1	0110	920						
GTSTR2	015	0210						
INIT40	34	07	7030					
INIT40	34	070						
INSOBL	4100							
INVPAR	137	139	2900	309	300	390	917	
		520	522	045				
		327	527	10130				
LDAYTR	120							
LDPDSN	132	9370						
LINCOL	090	304	7550	7000	7070			
LINLEN	1050	104	390	532	745	7710	703	
		040	062	004				
LNSU	001	535	003	9100				
LOOP	4100	479						
LOOP0	4210	430						
LOOP1	4200							
MARGIN	1170	7730	294					
MASK0	1000	170	959	10230				
MASK_P	090	1019						
NEXT1	2070	210						
NEXT2	2330	230						
NEXT3	2520	255						
NOINVE	245	2620						
NORR1	205	2110						
NORR2	231	2370						
NORR3	250	2560						
NOSTRG	013	0300						
NOXDR	200	2170						
PARAM0	520	000						
PARERR	0440	049						
P_FLAG	500	1021						
RECLEM	430	017						
SCRINIT	300	704						
SCRCTL	000	310	370	7050				
SCALO	312	391	3940					
SCAL0	31	910						
SCAL00	91	3760						
SCALCT	1000	300	3040	3090	7090			
SCALTY	442	4450	500					
SCRQLL	20	3000						
SCRSL	370	293	309	520	700	000	912	
SETC00	72	3620						
SETCUB	31	720						
SETCUR	20	3600						
SPARE0	770							
SPARE1	700							
SPARE2	790							
SPARE3	000							
SPARE4	000							
SYBLK	407	4010						
SYBLK0	0044	490						
SYBLK1	0050							
SYDISH	205	070	320					
STRGCT	1150	3490	7770	7700				
TESTAD	4050	444	503					

YSTPAR	367	418	725	8418
YSTPRI	843	8468		
YVPUL1	302	3098		
YVPULQ	173	2938		
UDG	468	145	706	
UPDAT0	9618	985		
UPDAT1	966	969	9718	
UPDAT2	972	975	9778	
UPDAT3	980	9908		
UPDAT7	274	9438		
UPDP05	298	369	599	797
UPDP05	458	817		8738
VIDM00	518	924		
WRCH0	67	1238		
WRCH01	1328	331		
WRCH11	144	1498		
WRCH12	142	1528		
WRCH13	148	151	1538	
WRCH14	165	1708		
WRCH15	169	1718		
WRCH2	1768			
WRCH3	181	1838		
WRCH5	1888	272		
WRCH6	1968			
WRCH7	2738			
WRCHAR	28	1278		
WRCHRB	31	678		
WRCHXT	281	2858		
WRSTLP	3288	342		
WRSTR0	68	3198		
WRSTR3	3368	347		
WRSTR8	31	688		
WRSTRG	28	3278		
WRSTX1	324	3498		

APPENDIX C-4

DUAL SCREEN MODE

Name: Dual Screen Mode Support

Description:

This component provides support to the application programmer for using the dual screen capability of the TS 2068. The services include opening/closing the second display file (moving the machine stack, OS RAM routines and BASIC structures), position control, clear screen and scroll screen services, and display of characters. In addition, services are provided to control which display file is active at the screen and which is the target of the current screen operation, as well as a Copy Service and an Exchange Service to transfer screen data/attributes within or between the two display files. "Get" services are provided to return the current display position, the character code for a specified display position, or the attribute byte for a specified display position.

For ease of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of PKCing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk 7 (E000H/3734H).

Attribute and other display controls such as Inverse are taken from the standard TS2063 System Variables (see Usage Section.) The system variable VIDM00 is used to determine which display file is the target of the requested service.

DECLARATIONS SERVICES

Name: SETMODE (SETM08 from BASIC - parameter to VIDM00)

Input:

M00E	0	=	Normal (Display File 1 only)
	128(B0H)	=	DF1 Active (DF2 Open)
	1	=	DF2 Active
	4	=	Screen Operations to DF1
	5	=	Screen Operations to DF2

From Machine Code: Mode Parameter in A

From BASIC: Mode Parameter in VIDM00

Description:

Mode values of 0, 128 or 1 will cause an update of the video mode hardware and the opening or closing of the second display file as needed. This involves determining if there is enough free RAM to open the second display file and if so, moving the BASIC structures and machine language variables area up and the UDG (User-Defined Graphics) area down to make space for the machine stack and OS RAM routines at the top of memory. The second display file is cleared to zeroes. The affected system and internal variables are updated or initialized (see Usage Section). When returning to Mode 0 the structures are returned to their normal locations. In these modes the designated Display File is active at the screen and is the target of all screen operations.

The mode values of 4 and 5 do not affect the hardware, but are used to redirect screen operations to the desired display file. This permits building of a display file prior to activating it at the screen.

The screen position is set to "home" (Line 0/Col.0) whenever SETMDGE (SETMD3) is executed.

NOTE: All screen operations of the TS 2058 System BASIC Interpreter including program entry, system messages, LIST, PRINT, PLDT, DRAW, etc. work only in Display File 1.

Name: CLRSCN (CLRSCB from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)
Starting Line Number (0-23)

From Machine Code: Line Count In Register B
Starting Line In Register C

From BASIC: Starting Line Number in CLSCTL
Line Count in CLSCTL + 1

Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion
BC = 1 invalid parameters
(Line Number + Line Count < 1 or > 24)

Name: SETCUR (SETCUE from BASIC - parameters to LINCCL)

Input: Line Number (0-23)
Column Number (0-31)

From Machine Code: Line Number In Register B
Column Number In Register C

From BASIC: Column Number In LINCCL
Line Number In LINCCL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

NOTE: The screen position used and maintained by these service routines is independent of that used by the System ROM (S POSN).

Output: BC = 0 for successful completion
BC = 1 for invalid parameters (Line Number > 23,
Column Number > Line Length-1)

Name: WRCHR (WRCHB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed

20H TD 7FH - Std. TS2060 Character Set
80H TD 8FH - Std. Graphics Set
90H TD A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAB

Description:

Displays character at current cursor position, applying current attributes and mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLM (see Usage section) and the scroll count (variable SCRCLCT) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRCTL and the new line started at the vacated line.

Output: BC = 0 for successful completion
BC = 1 invalid character code
BC = 3 for screen full

Name: WRSTRG (WRSTRB from BASIC - String Identifier in PARAMS)

Input: Character Code String

From machine code: Address of string in HL
Count in BC

From BASIC: String Variable Identifier in
System Variable PARAMS - 23747 (5CC3H)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCHR description and Usage Section on Automatic Scrolling). For the Screen Full condition, the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-A4H)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, PDKE the code for the string variable identifier into PARAMS prior to invoking WRSTRG, e.g.

```
0005 LET #="-----string-----"
0010 PDKE 23747, CODE "a"
0015 IFUSR (WRSTRB) <> 0 THEN -----
      (continue)
```

Output: BC = 0 Successful
BC = 2 BASIC - String not found
BC = 3 Screen Full - Remaining Count in STRGCT
(HL=Current Address in String)

Name: SCROLL (SCRLLB from BASIC - parameters to SCRCTL)

Input: Line Count (1-23)
Starting Line Number (1-23)

From Machine Code: Line Count in B
Starting Line in C
From BASIC: Starting Line in SCRCTL
Line Count in SCRCTL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on 'automatic' scrolling.

Output: BC = 0 Successful
BC = 1 Invalid Parameters
(Line Number + Line Count < 1 or > 24)

Name: GTCMAR (GTCMRB from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

From Machine Code: Line Number in B
Column Number in C

From BASIC: Column Number in GETCTL
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find
BC = 1 invalid parameters
BC = character code (20h-A6h)

Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GTCMAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position.

Output: BC = 1 for invalid parameters
BC = attribute byte

- Bit 7 - PLASH
- Bit 6 - BRIGHT
- Bit 5
- Bit 4 - PAPER
- Bit 3 /
- Bit 2
- Bit 1 - INK
- Bit 0 /

Name: GETCUR (GETCUB from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCOL, the current print position (where the next character would be displayed).

Output: B = Line number (0-23)
C = Column number (0-31)

BASIC: LINCOL - Column number
LINCOL + 1 - Line number

NOTE: If the last character was printed at Col.31 of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

Name: CDPYSC (CDPYSB from BASIC - string identifier in PARAMS)

Input:

Source Line (0-23)
Destination Line (0-23)
Source Display File (1 or 2)
Destination Display File (1 or 2)
Line Count (1-24)

From Machine Code: Source Line in B
Destination Line in C
Source DF in D
Dest.DF in E
Line Count in A

From BASIC: Parameters in string variable, separated by commas.
String Identifier in System Variable PARAMS - 23747
(SCC3M)

Description:

Copies the designated portion of the source display file to the designated portion of the destination display file. The source and destination may be in the same display file. During a multi-line copy, source and destination operands are accessed in a top to bottom fashion, therefore, in certain cases of operand overlap the operation may be destructive, i.e. the source data may be modified before it is copied. To get the desired result, it may be necessary to copy individual lines.

From BASIC, the following example would copy lines 8 thru 20 from DF1 to lines 0 thru 12 in DF2:

```
LET as="0,0,1,2,13"      0=Starting Line  
                        0=Destination Line  
                        1=Source DF  
                        2=Dest.DF  
                        13=Line Count  
  
POKE 23747,CCDE "a"    (String Identifier to PARAMS)  
LET cc=USR (CDPYSC)  
IF cc<>0 THEN.....
```

Output: BC=0 Successful
BC=1 Invalid Parameters (DF<>1 or 2; Line No.<>0-23;
Line+Count<>1-24)
BC=2 BASIC - String Not Found

Name: EXCHSC (EXCHSB from BASIC)

Input: Source "1" Display File (1 or 2)
Source "1" Line (0-23)
Source "2" Display File (1 or 2)
Source "2" Line (0-23)
Line Count (1-24)

From Machine Code: Source "1" DF in B
Source "1" Line in C
Source "2" DF in D
Source "2" Line in E
Line Count in A

From BASIC: Parameters in string variable, separated
by commas, in the order Source "1" Line,
Source "2" Line, Source "1" DF, Source "2"
DF, Line Count. String Identifier in
System Variable PARAMS - 23747 (SCC3M)

Description:

Exchanges the specified number of lines between Source "1" and Source "2". Both Sources may be in the same Display File. The exchange is done by XOR'ing the two sources together three times. The operation is done a line at a time, accessing the two sources in a top to bottom fashion, e.g. to exchange lines 8-12 in DF1 with lines 16-20 in DF2 would first exchange line 8 (DF1) with line 16 (DF2), then line 9 with line 17 and proceed in this fashion through exchange of line 12 with line 20.

From BASIC, the following example would exchange lines 0 thru 4 in DF1 with lines 8-12 in DF2:

```
LET as="0,8,1,2,5"      0=Source "1" Line  
                        8=Source "2" Line  
                        1=Source "1" DF  
                        2=Source "2" DF  
                        5=Line Count  
  
POKE 23747,CCDE "a"    (String Identifier to PARAMS)  
LET cc=USR (EXCHSB)  
IF cc<>0 THEN.....
```

Output: BC=0 Successful
BC=1 Invalid Parameters (DF<>1 or 2; Line No.<>0-23;
Line+Count <>1-24)
BC=2 BASIC - String Not Found

UASCSI

Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
SCRCTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BOTLN	1	Bottom Line - Line number (0-23) after which test for scroll will be made.
SCRCLCT	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.
CHTBL	2	Character Table (Base Address-100H)
GRTBL	2	Std.Graphics Character Table (Base-100H)
LINLEN	1	Line length
CURPDS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFADDR	2	Current Display File Address
MASKB	1	Working Byte - (P FLAG Shifted Right 1) (bit 0 = OVER) (bit 2 = INVERSE) (bit 4 = INK Complement of PAPER (bit 6 = PAPER Complement of INK
ATTBYT	1	Working Byte - (Copy of ATTR P)
GTINOX	1	'Get' Index - Used by GETCHAR
STRGCT	2	String Count - Contains remaining byte count when EC=3 (Screen Full) is returned from the Write String service WRSTRG (WRSTRB).
ATTMSK	1	Working Byte - (Copy of MASK P)

Initial values set via SETMCDE (SETHOB) when the second display file is first opened are as follows:

Variable Name	Value
SCRCTL	1701H
BOTLN	17H
SCRCLCT	1H
CHTBL	3000H
GRTBL	(Internal to Module)
LINLEN	20H
CURPDS	1821H
DFADDR	4000H/6000H
GTINCX	80H
STRGCT	0H

The following are the variables used for passing parameters in BASIC. The * indicates those initialized by SEHADS when the second display file is first opened:

Variable Name	Size	Value
* DATAB	1	0H
* LINCLL	2	0H
* CLRCTL	2	1800H
* GETCTL	2	0H
VIMODE	1	

In addition, VIDMOD, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Home Bank.

Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the rewrapping of certain structures when the second display file is open

NOTE: Machine code above RAMTOP is not moved.

Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IV Register which must always contain the value SC3AM for access to the standard system variables.

Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BD7LN=23=24th line). Condition Code 3 (Screen Full) will be returned since SCRLCT will decrement to zero. If SCRLCT is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a PAGE or setting the variables BD7LN and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BD7LN be set to line 21 (15M) and SCRCTL+1 be set to 21 (15M) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRLCT expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the "Screen Full" condition.

By setting the SCRCTL variable and invoking the SCROLL (SCRL0) routine any portion of the screen may be scrolled at any time.

Attribute Control:

Attribute and masking (Inverse/Over) control information will be taken from the system variables ATTR_P, MASK_P and P_FLAG as defined below. These variables contain the "permanent" attribute controls set via the BASIC commands PAPER, INK, BRIGHT, FLASH, INVERSE, and OVER, or by directly writing to the specified locations.

NAME	ADDRESS	CONTENTS
----	-----	-----
ATTR_P	23693	BIT 7 - FLASH 6 - BRIGHT 5 4 - PAPER 3/ 2 1 - INK 0/
MASK_P	23694	SAME FORMAT AS ATTR_P. USED FOR "TRANSPARENT" DISPLAY: For each bit that is set to 1, the corresponding information will be taken from the current screen position instead of from ATTR_P.
P_FLAG	23697	BIT 7 PAPER=COMPLEMENT OF INK 5 INK=COMPLEMENT OF PAPER 3 - INVERSE 1 - OVER (New Characters XOR'd with Old)

Using System ROM Screen Services:

The following technique can be used to build screens using the BASIC commands such as PRINT, PLOT, CIRCLE, and DRAW or other System ROM routines and get them into the second display file:

1. Set Mode B0 (opens 2nd DP)
2. Build Screen in DP1 using System or ADL Services
3. Copy CP1 to DP2 using COPYSC (COPYSB)
4. Set Mode 1 (DP2 to Screen)
5. Set Mode 4 (DP2 at Screen/Operations to DP1)
6. Build Screen in DP1 using System or ADL Services
7. Set Mode B0 (DP1 to Screen)
8. Go to Step 3

The switching of the screen from one display file to the other is transparent to the viewer where the files contain the same data.

M A R K I N G

When the second display file is active at the screen, any system messages such as SCROLL?, error reports, or use of the INPUT command will not be visible and the system may appear to be "hung". Indiscriminately pressing keys may be filling the "invisible" Edit Line with "garbage" thus further aggravating the situation. The DM ERR facility can be used to intercept some of these situations and set the video mode to use Display File 1 at the screen in order that the message can be seen and responded to. Otherwise, it is necessary to key in and execute from the Edit Line without being able to see it.

ADL - ASC 004 DUAL SCREEN MODE SUPPORT CR180/11 version 10.30.14 16-Mar-84 12147130
 VERSION LEVEL CONTROL ZDS.SRC

```

1      NAME ADL - ASC 004 DUAL SCREEN MODE SUPPORT
2      |
3      |
4      |
5      |
6      |
7      |
8      |
9      |
10     |
11     |
12     |
13     |
14     |
15     |
16     |
17     |
18     |
19     |
20     |
21     |
22     |
23     |
24     |
25     |
26     |
27     |
28     |
29     |
30     |
31     |
32     |
33     |
34     |
35     |
36     |
37     |
38     |
39     |
40     |
41     |
42     |
43     |
44     |
45     |
46     |
47     |
48     |
49     |
50     |
51     |
52     |
53     |
54     |
55     |
56     |
57     |
58     |
59     |
60     |
61     |
62     |
63     |
64     |
65     |
66     |
67     |
68     |
69     |
70     |
71     |
72     |
73     |
74     |
75     |
76     |
77     |
78     |
79     |
80     |
    
```

```

*****
*
*          TIMEX
* APPLICATION DEVELOPMENT LIBRARY
* NAME: DUAL SCREEN MODE SUPPORT
*
* ASC NO: 004
* VERSION: 001
* AUTHOR: C. CORCORAN
* DATE: 1/06/84
*****
SUBTTL VERSION LEVEL CONTROL
-----
VERSION      DATE      COMMENTS
-----
001          1/0/84      ORIGINAL
-----
SUBTTL DEFINITIONS
*****DEFINITIONS*****
INTERM WRCHAR,WRSTRG,SETCUR,CLRSCN,SCROLL
INTERM GTCHAR,GETATT,GETCUR
INTERM WRCHRB,WRSTRB,SETCUR,CLRSCB,SCRLB
INTERM GTCHRB,GETATB,GETCUB
INTERM SETMCDE,SETHDB
INTERM COPYSC,COPYSB
INTERM EXCMSC,EXCMSB
-----
=0010 SCRSZ EQU 24 ; 24 LINES
=1701 SCINIT EQU 1701M ; SCROLL CONTROL INITIALIZATION
=1000 CLINIT EQU 1000M ; CLEAR SCREEN CYL. INIT.
=3C00 CHRSET EQU 3C00M ; ROM CHAR.TABLE-100M
=05E0 GRPHST EQU ((GRPST)-100M) ;STD.GRAPHICS CHARS.
-----
=0E0E CHNGVID EQU 0E0EM ; ROUTINE IN ROM EXTENSION
=1720 RECLEN EQU 1720M ; ROUTINE IN ROM ROM
=30F9 ININT EQU 30F9M ; ROUTINE IN ROM ROM
=3193 PP2A EQU 3193M ; ROUTINE IN ROM ROM
-----
=00FF HRRPT EQU 0FFM ; ROM ROM EXT.SELECT PORT (BIT 7)
=00F4 DMHPT EQU 0F4M ; DOCK HORIZONTAL SELECT PORT
-----
=5C48 VARS EQU 5C48M ; SYSTEM VARIABLES
=5C50 CH_ADD EQU 5C50M ;
=5C65 STREND EQU 5C65M ;
=5C75 UDS EQU 5C75M ;
=5C7D COORDS EQU 5C7DM ;
=5C8D ATTR_P EQU 5C8DM ;
=5C8E MASH_P EQU 5C8EM ;
=5C91 P_FLAG EQU 5C91M ;
=5C82 RAMTOP EQU 5C82M ;
=5C84 PRANT EQU 5C84M ;
=5CC2 VIDMOD EQU 5CC2M ;
=5CC3 PARAM9 EQU 5CC3M ; (LOCATION 23747)
=6840 DRIVES EQU 6840M ;
=12C0 INSERTS EQU 7800M-DRIVES
=0840 MOVESI EQU DRIVES-6000M
=07C0 DEST EQU 0FFFM-MOVESI+1
=07C0 PIX EQU DEST-6000M
-----
=1000 PIXBL EQU 1000M
    
```



```

0046* 03          195      DEC      0          ; ADJUST ADDRESS-100M
0047* 04 70      196      SUB      70M         ; ADJUST FOR INDEX INTO TABLE
0049* 10 0C      197      JR        WRCM13        ;
0049* 0C 48 0031* 198      WRCM11 LD      0C,(CRTBL) ; ADDRESS OF GRAPHICS TABLE
0049* 04 40      199      SUB      40M         ; ADJUST FOR INDEX INTO TABLE
0071* 10 04      200      JR        WRCM13        ;
0073* 0C 48 002F* 201      WRCM12 LD      0C,(CHTBL) ; CHARACTER TABLE
0077* 00          202      WRCM13 EX      0E,ML    ; DE-PSM IN DISPLAY FILE
0078* 24 00      203      LD        M,0          ;
007A* 0F          204      LD        L,A          ; CODE IS INDEX INTO TABLE
0078* 29          205      ADD      ML,ML    ;
007C* 29          206      ADD      ML,ML    ;
007D* 29          207      ADD      ML,ML    ;
007E* 09          208      ADD      ML,BC    ; ML PIXEL PATTERN IN TABLE
007F* C1          209      POP      BC          ;
0080* 08          210      EX      0E,ML    ;
0081* 79          211      LD        A,C          ; TEST IF END OF LINE
0082* 30          212      DEC      A          ;
0083* 3A 0033*   213      LD      A,(LINLEN) ;
0084* 20 05      214      JR        NZ,WRCM14 ;
0088* 3C          215      INC      A          ; START OF NEW LINE
0089* 05          216      DEC      B          ; BUMP LINE NO.
008A* 4F          217      LD      C,A          ; LINLEN+1=START OF LINE
008B* 10 01      218      JR        WRCM15        ;
008D* 3C          219      WRCM14 INC      A          ;
008E* 09          220      WRCM15 CP      C          ; TEST IF START OF LINE
008F* 05          221      PUSH     DE          ;
0090* 0C 00C8*  222      CALL   I,TVPULG ; TEST IF OFF SCREEN
0093* 01          223      POP      DE          ;
0094* C5          224          ; HERE TO PUT CHARACTER IN DISPLAY FILE
0095* E5          225      WRCM2  PUSH   BC          ; CURSOR POSITION
0096* 3A 0038*  226      PUSH   ML          ; DISPLAY FILE ADRS.
0097* 04 FF      227      LD      A,(MASKB) ;
0098* 1F          228      LD      B,-1        ;
009C* 38 01      229      RRA          ;
009E* 04          230      JR        C,WRCM3 ; IF XORING NEW INTO OLD
009F* 1F          231      INC      B          ; B=-1 IF XORING =0 IF NOT
00A0* 1F          232      WRCM3  RRA          ;
00A1* 9F          233      RRA          ;
00A2* 4F          234      SBC      A,A          ; A=A
00A3* 3E 08      235      LD      C,A          ; C=A
00A5* A7          236      LD      A,B          ; A=B
00A6* EB          237      AND      A          ;
00A7* 00          238      WRCM5  EX      0E,ML    ; DE,ML
00A8* 1A          239      EX      AP,AP*   ; AP,AP*
00A9* A0          240      LD      A,(DE)    ; A,(DE)
00AA* AE          241      AND      B          ; B
00AB* A9          242      XOR      (ML)     ; (ML)
00AC* 12          243      XOR      C          ; C
00AD* 00          244      LD      (OE),A    ; (OE),A
00AE* 14          245      EX      AP,AP*   ; AP,AP*
00AF* 23          246      INC      D          ; D
00B0* 3C          247      INC      ML         ; ML
00B1* 20 FA      248      DEC      A          ; A
00B3* 15          249      WRCM7  JR        NZ,WRCM5 ; NZ,WRCM5
00B4* EB          250      DEC      D          ; D
00B5* CD 0465*   251      EX      DE,ML    ; DE,ML
00B6* E1          252      CALL   UPDATY   ; UP. ADRS. TO ML
00B7* C1          253      POP      ML         ; UPDATE ATTRIBUTE BYTE
00B8* 0C          254      POP      BC         ; STARTING ADDRESS
00B9* 23          255      DEC      C          ; CURSOR POSITION
00BA* 02          256      INC      ML         ; ADJUST CURSOR POSITION
00BB* 23          257      INC      ML         ; ADJUST OF ADDRESS
00BC* CD 0655*   258      WRCM8T CALL  STPOSH ; STORE NEW POSITION
00BD* 0E 00      259      GCJOREY LD      C,0   ; RETURN BC=0
00BE* 04 00      260      ERRRET LD      B,0   ; ENTER HERE WITH C NON-ZERO
00BF* 06 00      261          ;
00C4* 0E 01      262      INVPAR LD      C,1   ; RETURN BC=1 FOR INVALID PARAMETERS
00C6* 10 FF      263      JR        ERRRET ;
00C8* 3E 10      264          ;
00CA* 90          265      TVPULG LD      A,SCRSE ; TEST IF NEW LINE IS CPP SCREEN
00CB* 57          266      SUB      B          ; A=LINE NO.
00CC* 3A 0020*  267      LD      D,A          ; D=A
00CD* 0A          268      LD      A,(BOTLN) ; GET BOTTOM LINE
00CE* 0A          269      CP      D          ;
00D0* 02 0628*  270      JP        NC,UPDP0M ; ON SCREEN - RETURN TO WRITE CHAR.
00D3* 3A 002E*  271      LD      A,(SCRCLCT) ; VIA UPDATE AND STORE POSITION
00D4* 30          272          ; TEST SCROLL COUNT
00D7* 20 08      273      DEC      A          ;
00D9* 3E 01      274      JR        NZ,TVPUL1 ; DO AUTOMATIC SCROLL USING SCRCLT
00DB* 32 002E*  275      LD      A,1          ; RINITIALIZE TO 1
00DE* C1          276      LD      (SCRCLCT),A ;
00DF* C1          277      POP      BC         ;
00E0* 0E 03      278      POP      BC         ; DISCARD RETURN TO WRITE CHAR.
00E2* 10 00      279      LD      C,3          ; RETURN BC=3 FOR SCREEN FULL
00E4* 32 002E*  280      JR        ERRRET ;
00E7* 0C 48 0028* 281      TVPUL1 LD      (SCRCLCT),A ; UPDATE VARIABLE
00E8* C3 0141*   282      LD      BC,(SCRCLT) ; GET SCRCLL CONTROLS (STARTING LINE
00E8* 03 0141*   283          ; AND LINE COUNT)
00E8* 03 0141*   284      JP        SCRL0   ; RETURN TO WRITE CHAR. VIA SCROLL
00E8* 03 0141*   285          ;
00E8* 03 0141*   286          ;
00E8* 03 0141*   287      SUBTTL WRITE STRING
00E8* 03 0141*   288          ;
00E8* 03 0141*   289          ;
00E8* 03 0141*   290          ;
00E8* 03 0141*   291          ;
00E8* 03 0141*   292      WRCM8T CALL  GETSTAG ; HERE FROM BASIC ENTRY TO
00F1* C0          293      RET      I          ; WRITE CHARACTER STRING TO SCREEN.
00E8* 03 0141*   294          ; STRING IDENTIFIER (CM.CODE) IN
00E8* 03 0141*   295          ; SYSTEM VARIABLE PARAMS AT SCC3M
00E8* 03 0141*   296          ; GET STRING VAR. ADRS. TO ML
00E8* 03 0141*   297          ; STRING NOT FOUND OR NULL STRING
00E8* 03 0141*   298          ; (BC CONTAINS STATUS)
00E8* 03 0141*   299          ;
00E8* 03 0141*   300          ;
00E8* 03 0141*   301          ;
00E8* 03 0141*   302          ;
00E8* 03 0141*   303          ;
00E8* 03 0141*   304          ;
00E8* 03 0141*   305          ;
00E8* 03 0141*   306          ;
00E8* 03 0141*   307          ;
00E8* 03 0141*   308          ;
00E8* 03 0141*   309      WRCM8T CALL  LDATTR ; GET ATTRIBUTE AND MASK CONTROLS
00F2* CD 06A2*   300      WRCM8T LD      A,(ML) ; GET CODE
00F5* 7E          301      WRCM8T LD      A,(ML) ; SAVE ADDRESS
00F6* 85          302      PUSH   BC         ; AND COUNT
00F7* C5          303      PUSH   BC         ;
00F8* CD 004C*   304      CALL   WRCM01 ; WRITE "A"
00F8* 79          305      LD      A,C          ; TEST IF GOOD
00FC* 80          306      OR      B          ;
00FD* 20 09      307      JR        NZ,WRCM8T ; EXIT IF INVALID CODE OR
00FF* C1          308          ; IF SCREEN FULL
00FF* C1          309      WRCM8T POP      BC ; COUNT

```



```

0100* 01
0101* 23
0102* 00
0103* 70
0104* 01
0109* 20 00
0107* C9

0100* 79
0109* PE 01
0108* 20 PZ
0100* 01
010E* 22 0030*
0111* 01
0112* 00 03
0114* 06 00
0116* C9

POSITION CONTROL  IODS.SRC

310      POP  ML
311      INC  ML
312      DEC  BC
313      LD   A,B
314      CR   C
315      JR   NZ,WRSTLP
316      RET
317
318      WRSTX1 LD  A,C
319      CP   1
320      JR   Z,WRSTR3
321      POP  ML
322      LD   (STRGCT),ML
323      POP  ML
324      LD   C,3
325      WRSTX2 LD  B,0
326      RET
327

329      SUBTTL POSITION CONTROL
330
331      SETC00:
332
333      LD   BC,(LINCGL)
334
335      SETC01:
336      CALL TSTPAP
337      CALL CONVFM
338      CALL UPDPOSN
339      JP   GOODRET
340
341      SUBTTL SCROLL
342
343
344
345
346      SCRL00:
347
348      LD   BC,(SCREVL)
349
350      SCRL01:
351
352
353      LD   A,B
354      AND  A
355      JP   Z,INVPAR
356      ADD  A,C
357      CP   1
358      JP   C,INVPAR
359      CP   23
360      JP   NC,INVPAR
361      CALL SCRL0
362      JP   GOODRET
363
364      SCRL0  PUSH BC
365      LD   A,SCR2
366      SUB  C
367      LD   B,A
368      LD   A,(LINLEN)
369      INC  A
370      LD   C,A
371      CALL LNBW
372      POP  BC
373      PUSH ML
374      PUSH BC
375      TESTAD LD  A,L
376      AND  A
377      JR   Z,ST0LK
378      RLCA
379      RLCA
380      RLCA
381      LD   C,A
382      LD   A,B
383      SUB  C
384      CP   B
385      JR   C,GRBLK
386      LD   A,B
387      LD   B,0
388      PUSH BC
389      LD   B,A
390      LD   C,B
391      LD   A,B
392      PUSH BC
393      RRCA
394      RRCA
395      RRCA
396      LD   C,A
397      LD   B,0
398      EX  DE,HL
399      LD   HL,-Z0H
400      ADD  HL,DE
401      EX  DE,HL
402      PUSH HL
403      LD   LR
404      POP  HL
405      INC  H
406      POP  BC
407      DEC  C
408      JR   NZ,LOOP0
409      POP  JC
410      LD   A,B
411      AND  A
412      JR   Z,SCRLXT
413      LD   L,0
414      JR   TESTAD
415      SCRLXT POP  BC
416      POP  DE
417      PUSH BC
418      LD   L,B
419      LD   M,0
420      ADD  HL,ML
421      ADD  HL,ML
422      ADD  HL,ML
423      ADD  HL,ML
424

```

```

0191* 44          429          LD  B,M          ; COUNT TO BC
0192* 40          426          LD  C,L          ;
0193* 02          427          LD  M,D          ; ADRS. TO HL
0194* 00          428          LD  L,E          ;
0195* CC 0692*    429          CALL CALCATY    ; ATTRIBUTE ADRS. TO HL
0196* EB          430          EX  DE,HL       ;
0197* 21 PPE0     431          LD  HL,-20H     ; ADRS. OF PREV.BYTR.BYTE
0198* 19          432          ADD  HL,DE       ;
0199* EB          433          EX  DE,HL       ;
019E* ED 00       434          LDIR          ;
01A0* C1          435          POP  BC         ; PREV.BYTE DE
01A1* 79          436          LD  A,C         ; SCRCLL ATTRIBUTES
01A2* 00          437          ADD  A,0         ; ORIG. BC
01A3* 3D          438          DEC  A          ; STARTING LINE
01A4* 4F          439          LD  C,A         ; ADD NO. OF LINES MOVED
01A5* 06 01      440          LD  B,1         ; LAST LINE SCROLLED
01A7* 10 45      441          JR  CLRSO0     ; CLEAR 1 LINE
                                ; CLEAR VACATED LINE AND RETURN
                                ;
01A9* 4F          442          ;
01AA* 70          443          GRBLK LD  C,A         ;
01AB* 91          444          LD  A,0         ;
01AC* 47          445          SUB  C          ; ADJUST TOTAL LINE COUNT BY NO.
01AD* C3          446          LD  B,A         ; OF LINES THIS BLOCK
01AE* 79          447          PUSH BC        ; LOAD A NO. OF LINES THIS BLOCK
01AF* 10 02      448          LD  A,C         ;
01B0* 09          449          JR  LOOP       ;
                                ;
01B1* 09          450          ;
01B2* C5          451          STBLK DEC  B          ; ADJUST TOTAL LINE COUNT-1
01B3* 0E 00      452          PUSH BC        ;
01B4* C5          453          LD  C,0         ; SCAN COUNT
01B5* 08          454          STBLK0 PUSH BC   ; SAVE SCAN COUNT
01B6* 21 P000    455          STBLK1 EX  DE,HL   ;
01B7* 19          456          LD  HL,-720H    ;
01B8* 00          457          ADD  HL,00      ;
01B9* 01 0020    458          EX  DE,HL       ; ADRS. OF PREV. LINE
01BA* 09          459          LD  BC,32       ; BYTE COUNT
01BB* 09          460          PUSH HL        ;
01BC* ED 00      461          LDIR          ; DO MOVE
01BD* 01          462          POP  HL        ; TO NEXT SCAN LINE
01BE* 2A          463          INC  H          ;
01BF* C1          464          POP  BC        ; TEST IF MORE SCANS
01C0* 00          465          DEC  C          ; REMAINING LINE COUNT
01C1* 20 ED      466          JR  NZ,STBLK0  ;
01C2* C1          467          POP  BC        ;
01C3* 70          468          LD  A,0         ;
01C4* 47          469          AND  A          ;
01C5* 20 09      470          JR  Z,SCRHLT   ; ADJUST TO LINE 1
01C6* 11 P020    471          LD  DE,-700H   ; BC=LINE COUNT
01C7* 10          472          ADD  HL,DE       ; ML=OP ADDRESS
01C8* C3 0151*   473          JP  TESTAD     ;
01C9* 00          474          ;
01CA* 00          475          ;
01CB* 00          476          ;
01CC* 00          477          SUBTL CLEAR SCREEN
01CD* 00          478          ;
01CE* 00          479          ;
01CF* 00          480          CLRS001 LD  BC,(CLRCTL) ; HERE FROM BASIC ENTRY TO CLEAR SCREEN
01D0* ED 40 000C* 481          ; GET CONTROL INPD.
01D1* 00          482          ;
01D2* 00          483          ;
01D3* 00          484          ;
01D4* 00          485          ;
01D5* 00          486          ;
01D6* 00          487          ;
01D7* 00          488          ;
01D8* 00          489          ;
01D9* 00          490          ;
01DA* 00          491          ;
01DB* 00          492          ;
01DC* 00          493          ;
01DD* 00          494          ;
01DE* 00          495          ;
01DF* 00          496          ;
01E0* 00          497          ;
01E1* 00          498          ;
01E2* 00          499          ;
01E3* 00          500          ;
01E4* 00          501          ;
01E5* 00          502          ;
01E6* 00          503          ;
01E7* 00          504          ;
01E8* 00          505          ;
01E9* 00          506          ;
01EA* 00          507          ;
01EB* 00          508          ;
01EC* 00          509          ;
01ED* 00          510          ;
01EE* 00          511          ;
01EF* 00          512          ;
01F0* 00          513          ;
01F1* 00          514          ;
01F2* 00          515          ;
01F3* 00          516          ;
01F4* 00          517          ;
01F5* 00          518          ;
01F6* 00          519          ;
01F7* 00          520          ;
01F8* 00          521          ;
01F9* 00          522          ;
01FA* 00          523          ;
01FB* 00          524          ;
01FC* 00          525          ;
01FD* 00          526          ;
01FE* 00          527          ;
01FF* 00          528          ;
0200* 00          529          ;
0201* 00          530          ;
0202* 00          531          ;
0203* 00          532          ;
0204* 00          533          ;
0205* 00          534          ;
0206* 00          535          ;
0207* 00          536          ;
0208* 00          537          ;
0209* 00          538          ;
020A* 00          539          ;
020B* 00          540          ;
020C* 00          541          ;
020D* 00          542          ;
020E* 00          543          ;
020F* 00          544          ;
0210* 00          545          ;
0211* 00          546          ;
0212* 00          547          ;
0213* 00          548          ;
0214* 00          549          ;
0215* 00          550          ;
0216* 00          551          ;
0217* 00          552          ;
0218* 00          553          ;
0219* 00          554          ;
021A* 00          555          ;
021B* 00          556          ;
021C* 00          557          ;
021D* 00          558          ;
021E* 00          559          ;
021F* 00          560          ;
0220* 00          561          ;
0221* 00          562          ;
0222* 00          563          ;
0223* 00          564          ;
0224* 00          565          ;

```

```

0225* 00 00          530      LDIR
0227* 01            539      POP      ML
0228* 24            540      INC      M
0229* C1            541      POP      BC
022A* 00            542      OBC      C
022B* 20 07        543      JR      NZ,CLRSCA
022C* 05            544      PUSH   ML
022E* 25            545      OBC      M
022F* CD 0492*     546      CALL   CALCATY
0232* 78            547      LD      A,B
0233* 06 07        548      AND     Y
0235* 0F            549      RRCA
0236* 0F            550      RRCA
0237* 0F            551      RRCA
0238* 4F            552      LD      C,A
0239* 06 00        553      LD      B,0
023B* 00            554      OBC      C
023C* 54            555      LD      D,M
023D* 90            556      LD      E,L
023E* 3A 0039*     557      LD      A,(TTYBYT)
0241* 77            558      LD      (ML),A
0242* 13            559      INC     DE
0243* 00 00        560      LDIR
0245* 01            561      POP     ML
0246* C1            562      POP     BC
0247* 78            563      LD      A,B
0248* 87            564      AND     A
0249* 28 03        565      JR      Z,CLRSX
024A* 2E 00        566      LD      L,0
024D* C3 0202*     567      JP      CLRSCL
0250* C1            568      CLRSX  POP
0251* C3 0420*     569      JP      UPDOSH
0254* 6F            570      CLRSX  LD      C,A
0255* 76            571      LD      A,B
0256* 91            572      SUB     C
0257* 47            573      LD      B,A
0259* C1            574      PUSH   BC
025A* 79            575      LD      A,C
025B* 18 05        576      JR      CLRSCL
025C*              577      SUBYTL "GET" ROUTINES
025D*              578      ;
025E*              579      ;
025F*              580      ;
0260*              581      GTCM01:
0261*              582      ;
0262*              583      ;
0263* ED 48 0019* 584      LD      BC,(GETCTL)
0264*              585      ;
0265*              586      GTCM02:
0266* CD 060C*     587      CALL   TSTPAR
0267* CD 061D*     588      CALL   CONVPH
0268* CD 062D*     589      CALL   CALCPDS
0269* ED 58 002F* 590      LD      DE,(CHTBL)
026A* 06 60        591      LD      B,96
026B* 3E 00        592      LD      A,8CH
026C* 32 003A*     593      GTCM1  LD      (GTINDEX),A
026D* 14            594      INC     D
026E* 08            595      EX      DE,HL
026F* C5            596      GTCM2  PUSH   BC
0270* 05            597      PUSH   DE
0271* E5            598      PUSH   HL
0272* 1A            599      LD      A,(CODE)
0273* AE            600      XOR     (HL)
0274* 28 10        601      JR      Z,GTCM3
0275* F5            602      PUSH   AP
0276* 3A 003A*     603      LD      A,(GTINDEX)
0277* FE 90        604      CP      90H
0278* 20 03        605      JR      NZ,GTCM23
0279* F1            606      POP     AP
027A* 18 27        607      JR      GTCM4
027B* F1            608      GTCM23 POP
027C* 3C            609      INC     A
027D* 20 23        610      JR      NZ,GTCM4
027E* 3D            611      DEC     A
027F* 4F            612      GTCM3  LD      C,A
0280* 06 07        613      LD      B,7
0281* 14            614      GTCM31 INC     D
0282* 23            615      INC     HL
0283* 1A            616      LD      A,(CODE)
0284* AE            617      XOR     (HL)
0285* A9            618      XOR     C
0286* 20 18        619      JR      NZ,GTCM4
0287* 10 P7        620      OJNZ   GTCM31
0288*              621      ;
0289*              622      ;
028A*              623      ;
028B*              624      ;
028C*              625      LD      A,C
028D* C1            626      POP     BC
028E* C1            627      POP     BC
028F* C1            628      POP     BC
0290* 4F            629      LD      C,A
0291* 3A 003A*     630      LD      A,(GTINDEX)
0292* 90            631      SUB     B
0293* FE 20        632      CP      20H
0294* 20 05        633      JR      NZ,GTCM32
0295* 0C            634      INC     C
0296* 20 02        635      JR      NZ,GTCM32
0297* 3E 0F        636      LD      A,BPH
0298* 4F            637      GTCM32 LD      C,A
0299* 08 00        638      LD      B,0
029A* C9            639      RET
029B*              640      ;
029C*              641      GTCM4  POP     HL
029D* 11 0008      642      LD      DE,B
029E* 19            643      ADD    HL,DE
029F* D1            644      POP     DE
02A0* C1            645      POP     BC
02A1* 10 0E        646      OJNZ   GTCM2
02A2*              647      ;
02A3*              648      ;
02A4*              649      EX      DE,HL
02A5* 3A 003A*     650      LD      A,(GTINDEX)
02A6* FE 80        651      CP      80H

```

```

020E* 20 0A
02C0* ED 58 0031*
02C4* 06 10
02C6* 3E 90
02C8* 18 A7
02CA* FE 08
02CC* 20 00
02CE* ED 58 5C7B
02D2* 15
02D3* 06 15
02D5* 38 A5
02D7* 18 98
02D9* 48
02DA* AP
02DB* C9
452 JR NZ,GTCH5 ; TRY GRAPHICS
453 LD DE,(GRYBL); STD. GRAPHICS TABLE
454 LD B,16 ; NO. OF ENTRIES
455 LD A,90H ; INDEX
456 JR GTCH1 ;
457 CP 90H ; TEST IF STD. GRAPHICS
458 JR NZ,GTCH6 ; DONE IF NOT
459 LD DE,(UDG) ; TRY USER-DEFINED GRAPHICS AREA
460 DEC D ; ADJUST ADDRESS-100H
461 LD B,21 ; NO. OF ENTRIES
462 LD A,0A5H ; INDEX
463 JR GTCH1 ;
464 LD C,B ; HERE WHEN NO MATCH ANYWHERE
465 XOR A ; SET BC AND A=ZERO
466 RET
467 ;
468 ; GET ATTRIBUTE BYTE
469 ; HERE FROM BASIC ENTRY TO GET ATTRIBUTE
470 ;
471 GETAB01 LD BC,(GETCTL)
472 ;
473 ; HERE WITH LIN/COL IN BC
474 ;
475 GETATT1 CALL TSTPAR ; TEST PARAMETERS
476 CALL CONVPM ; CONVERT TO INTERNAL FORMAT
477 CALL CALCPDS ; CALCULATE DP POSITION
478 CALL CALCATT ; CALCULATE ATTRIBUTE POSITION
479 LD A,(ML) ; ATTRIBUTE BYTE TO A
480 LD C,A ; PASS BACK IN BC
481 LD B,0
482 RET
483 ; GET CURSOR POSITION
484 ; HERE FROM BASIC ENTRY
485 GETCBO1 LD BC,(CURPOS) ; GET INTERNAL POSITION
486 CALL CONVPM ; CONVERT TO USER FORMAT
487 LD A,(LINLEN)
488 CP C ; TEST IF END OF LINE(=32)
489 JR NZ,GETC1 ; NO
490 LD C,0 ; NEXT POSN. START OF NEXT LINE
491 LD A,B ;
492 INC A ; BUMP TO NEXT LINE
493 LD B,A ;
494 CP 24 ; TEST IF OFF SCREEN
495 JR C,GETC1 ; NO
496 LD B,23 ; POSN. IS AT BOTTOM LINE
497 GETC1 LD (LINCGL),BC ; STORE FOR BASIC PROGRAM
498 RET ; VALUES ALSO IN BC
499 ;
701 SUBTTL VIDEO MODE CONTROL
702 ;
703 ;
704 STMO01 ; HERE FROM BASIC ENTRY WITH MODE IN
705 ; "VMODE"
706 LD A,(VMODE)
707 ;
708 SETM001 ; ENTRY WITH MODE IN A
709 ;
710 LD B,A ; MODE TO B
711 AND A ;
712 JR Z,SETM00 ; TEST FOR VALIDITY
713 CP 80H ;
714 JR Z,SETM00 ;
715 CP 1 ;
716 JR Z,SETM00 ;
717 CP 4 ;
718 JP Z,PPPV ; HANDLE 4 AND 5
719 CP 8 ;
720 JP Z,PPPV ;
721 JP INVPR ; INVALID PARAMETER
722 SETM00 LD A,(VIDM00) ; TEST CURRENT MODE
723 AND A ;
724 JR NZ,SETM02 ; DP2 OPEN
725 OR B ; CURRENT MODE=0 - TEST IF NEW MODE=0
726 JP Z,GOODRET ; NO ACTION
727 ;
728 ; INITIAL OPENING OF 2ND DP
729 SETMG1 LD HL,SCINIT ; INITIALIZE VARIABLES
730 LD (SCRCTL),HL ; SCROLL CONTROL
731 LD A,23 ;
732 LD (BOTLN),A ; BOTTOM LINE
733 LD A,1 ;
734 LD (SCRCLT),A ; SCROLL COUNT
735 LD A,32 ;
736 LD (LINLEN),A ; LINE LENGTH
737 LD HL,CLINIT ;
738 LD (CLRCTL),HL ; CLEAR SCREEN CONTROL
739 XOR A ;
740 LD (STRGCT),A ; WRSTAG REM LINING COUNT
741 LD (STRGCT+1),A ;
742 LD HL,CHRSET ; STD.CHAR.SET
743 LD (CHYBL),HL ;
744 LD HL,GRPHST ; STD. GRAPHICS SET
745 LD (GRYBL),HL ;
746 ;
747 ; INITIALIZE BASIC INPUTS
748 LD (DATAB),A ; DATA BYTE
749 LD (LINCGL),A ; COLUMN
750 LD (LINCGL+1),A ; LINE
751 LD (GETCTL),A ; "GET" COLUMN
752 LD (GETCTL+1),A ; "GET" LINE
753 ;
754 LD HL,INSERTSZ ; TEST IF ENOUGH MEMCRY
755 LD DE,MOVESZ ;
756 ADD HL,DE ; HL=TOTAL ROOM NEEDED
757 LD DE,(STKEND) ;
758 ADD HL,DE ; ADD MEMORY IN USE
759 JP C,EXTRM ; NOT ENOUGH MEMORY TO OPEN DP2
760 LD DE,(RAMTOP) ;
761 AND A ;
762 SBC HL,DE ;
763 JP NC,EXTRM ; NOT ENOUGH MEMORY
764 LD A,B ; MODE TO A
765 CALL ENBLEXT ; ENABLE ROM EXT.
766 CALL GETVAL ; W/M VALUE TO B; VIDM00 VALUE TO C

```

```

0391* C5
0392* T8
0393* A7
0394* JC 01
0395* T9
0396* CD 060E
0397* CD 060E
0398* CD 0600
0399* C1
039E* T9
039F* 32 SCC2
03A2* 3A 0033
03A5* 3C
03A6* 4F
03A7* 06 18
03A9* 21 0000
03AC* 22 5C7D
03AD* CD 0620
03B2* C3 00BF
03B5* 0E 02
03B7* C3 00C1
767 PUSH BC I SAVE
768 LD A,B
769 AND A I TEST IF 0
770 JR NZ,SETM21
771 LD A,C I VALUE FOR CHNGVID IN C
772 CALL CHNGVID I CALL RTN. TO OPEN/CLOSE ZND OP
773 CALL ENBLHOME I REENABLE HOME BANK
774 POP BC
775 LD A,C
776 SETM03 LD (VIOM0D),A I UPDATE VIOM0D
777 UPDATE LD A,(CLINLEN) I GET LINE LENGTH
778 INC A
779 LD C,A I C COLUMN
780 LD B,SCPSZ I BC=HOME POSITION (LN.0/CCL.0)
781 LD ML,0
782 LD (CCORDS),ML I INITIALIZE PLOT POSITION
783 CALL UPDPOSN I UPDATE AND STORE HOME POSITION
784 JP GOCORET I RETURN BC=0
785 EXTRM LD C,2 I RETURN BC=2 FOR NO ROOM
786 JP ERRRET
787 I
788 I
789 I
790 I
791 I
792 I
793 I
794 I
795 I
796 I
797 I
798 I
799 I
800 I
801 I
802 I
803 I
804 I
805 I
806 PARV LD A,(VIOM0D) I ONLY VALID IF VIOM0D=00,01,04 OR 05
807 LD C,A I CURRENT VIOM0D TO C
808 BIT 7,A
809 JP Z,INVPAR I INVALID PARAMETER
810 LD A,B I MODE 4 OR 5 TO A
811 CP 4
812 JR NZ,DTADP2 I MODE 5 - DATA TO DP2
813 I
814 DTADP1 BIT 0,C I MODE 4 - DATA TO DP1
815 JP Z,GODDRET I TEST IF DP1 ALREADY DEST.
816 XOR C I NO ACTION
817 AND OPEN I SET VIOM0D=00 OR 04
818 JR SETM03 I BIT 0=0
819 I
820 I
821 DTACP2 BIT 0,C I MODE 5 - DATA TO DP2
822 JP NZ,GODDRET I TEST IF DP2 ALREADY DEST.
823 XOR C I NO ACTION
824 JR SETM03 I SET VIOM0D=01 OR 05
825 I
826 SUBTYL COPY SCREEN
827 I
828 I
829 I
830 I
831 I
832 I
833 I
834 COPYSD: I HERE FROM BASIC ENTRY
835 LD HL,(CH_ADD) I SAVE CURRENT POINTER
836 PUSH HL
837 CALL GETSTRG I GET STRING OF PARAMETERS
838 JR NZ,COPY01 I POUNC STRING
839 POP HL I RESTORE CH_ADD AND RETURN
840 LD (CH_ADD),HL I ERROR (NO STRING OR NULL STRING)
841 RET I (BC CONTAINS STATUS)
842 I
843 COPYMK DEFS 4 I WORK AREA FOR BASIC PARAMETERS
844 I
845 I
846 COPY01 LD (CH_ADD),HL I ADDR. OF STRING TO CH_ADD
847 CALL GETNO I GET PARAMETERS
848 LD (COPYMK+1),A I SOURCE LINE NO.
849 CALL GETNO I
850 LD (COPYMK),A I DEST. LINE NO.
851 CALL GETNO I
852 LD (COPYMK+3),A I SOURCE OF
853 CALL GETNO I
854 LD (COPYMK+2),A I DEST. OF
855 CALL GETNO I RESTORE CH_ADD
856 POP HL
857 LD (CH_ADD),HL
858 LD BC,(COPYMK)
859 LD DE,(COPYMK+2)
860 JR CCOPYC I TEST VALIDITY AND DO COPY
861 I
862 GETND LD HL,(CH_ADD) I LOAD ADDRESS
863 LD A,(HL) I GET CHAR. FROM STRING
864 CP 2C I TEST IF COMMA
865 JR Z,GETN2
866 CP 30H I TEST IF DIGIT
867 JR C,COPERR I INVALID PARAMETER
868 I OR END OF STRING
869 CP 3AH
870 JR NC,COPERR
871 GETN1 CALL INENT I CONVERT TO BINARY
872 CALL PD2A
873 RET NC I VALID DIGIT IN A
874 GETN2 INC HL
875 LD (CH_ADD),HL I
876 JR GETND I TEST NEXT CHAR.
877 I
878 COPERR POP HL I DISCARD RETURN FROM GETND

```

```

0436* 01          879  COPER1 PDP      HL          I RESTORE CM_ADD
0437* 22 3C30    880  LD          LD      (CM_ADD),HL
0438* C3 00C4*   881  JP          JP      INVPAR          I RETURN BC=1 FOR INVALID
                                     I PARAMETER(S)
                                     I
043D*          882  I
                                     I
043D*          883  I
043D*          884  I
043D*          885  I
043D*          886  I
043D*          887  I
043D*          888  I
043D*          889  I
043D*          890  I
043D*          891  I
043D*          892  I
043D*          893  I
043D*          894  I
043D*          895  I
043D*          896  I
043D*          897  I
043D*          898  I
043D*          899  I
043D*          900  I
043D*          901  I
043D*          902  I
043D*          903  I
043D*          904  I
043D*          905  I
043D*          906  I
043D*          907  I
043D*          908  I
043D*          909  I
043D*          910  I
043D*          911  I
043D*          912  I
043D*          913  I
043D*          914  I
043D*          915  I
043D*          916  I
043D*          917  I
043D*          918  I
043D*          919  I
043D*          920  I
043D*          921  I
043D*          922  I
043D*          923  I
043D*          924  I
043D*          925  I
043D*          926  I
043D*          927  I
043D*          928  I
043D*          929  I
043D*          930  I
043D*          931  I
043D*          932  I
043D*          933  I
043D*          934  I
043D*          935  I
043D*          936  I
043D*          937  I
043D*          938  I
043D*          939  I
043D*          940  I
043D*          941  I
043D*          942  I
043D*          943  I
043D*          944  I
043D*          945  I
043D*          946  I
043D*          947  I
043D*          948  I
043D*          949  I
043D*          950  I
043D*          951  I
043D*          952  I
043D*          953  I
043D*          954  I
043D*          955  I
043D*          956  I
043D*          957  I
043D*          958  I
043D*          959  I
043D*          960  I
043D*          961  I
043D*          962  I
043D*          963  I
043D*          964  I
043D*          965  I
043D*          966  I
043D*          967  I
043D*          968  I
043D*          969  I
043D*          970  I
043D*          971  I
043D*          972  I
043D*          973  I
043D*          974  I
043D*          975  I
043D*          976  I
043D*          977  I
043D*          978  I
043D*          979  I
043D*          980  I
043D*          981  I
043D*          982  I
043D*          983  I
043D*          984  I
043D*          985  I
043D*          986  I
043D*          987  I
043D*          988  I
043D*          989  I
043D*          990  I
043D*          991  I
043D*          992  I
043D*          993  I
043D*          994  I
043D*          995  I
043D*          996  I
043D*          997  I
043D*          998  I
043D*          999  I
043D*          1000 I

```

```

04CA* 26 C0          992      LD      M,0
04CB* 29          993      ADD     HL,HL
04CC* 29          994      ADD     HL,HL
04CD* 29          995      ADD     HL,HL
04CE* 29          996      ADD     HL,HL
04CF* 29          997      ADD     HL,HL
04D0* 44          998      LD      B,M
04D1* 40          999      LD      C,L
04D2* E1          1000     POP     HL
04D3* C0 0492*    1001     CALL   CALCATT
04D4* E8          1002     EX      DE,HL
04D5* E1          1003     POP     HL
04D6* C0 0492*    1004     CALL   CALCATT
04D7* E0 80       1005     LDIR
04D8* C3 008F*    1006     JP      GOODRET
04D9* 41          1007     I
04DA* 78          1008     COPY55 LD      B,C
04DB* 07          1009     LD      A,E
04DC* 07          1010     RLCA
04DD* 07          1011     RLCA
04DE* 07          1012     RLCA
04DF* 4F          1013     LD      C,A
04E0* 3E 08       1014     LD      A,B
04E1* 91          1015     SUB     C
04E2* 4F          1016     LD      C,A
04E3* 88          1017     CP      B
04E4* 30 08       1018     JR      C,COPY57
04E5* C1          1019     POP     BC
04E6* 78          1020     COPY56 LD      A,B
04E7* 91          1021     SUB     C
04E8* 47          1022     LD      B,A
04E9* C9          1023     PUSH   BC
04EA* 79          1024     LD      A,C
04EB* 18 48       1025     JR      COPY53
04EC* 79          1026     COPY57 LD      A,C
04ED* C1          1027     POP     BC
04EE* 4F          1028     LD      C,A
04EF* 18 P4       1029     JR      COPY56
04F0* 1030
04F1* 1032     SUBYTE EXCHANGE SCREEN
04F2* 1033
04F3* 1034     I
04F4* 1035     I
04F5* 1036     I
04F6* 1037     EXCH50 I
04F7* 1038     I
04F8* 1039     I
04F9* 2A SC5D     1040     LD      HL,(CM_ADD)
04FA* 85          1041     PUSH   HL
04FB* CC 0488*    1042     CALL   GETSTRG
04FC* 26 05       1043     JR      NZ,EXCH01
04FD* E1          1044     POP     HL
04FE* 21 SC5D     1045     LD      (CM_ADD),HL
04FF* C9          1046     RET
0500* 1047     I
0501* 22 SC5D     1048     EXCH01 LD      (CM_ADD),HL
0502* C0 0418*    1049     CALL   GETND
0503* 32 03E8*    1050     LD      (COPYNK),A
0504* C0 0418*    1051     CALL   GETND
0505* 32 03EA*    1052     LD      (COPYNK+2),A
0506* CC 0418*    1053     CALL   GETND
0507* 32 03E9*    1054     LD      (COPYNK+3),A
0508* C0 0418*    1055     CALL   GETND
0509* 32 03EA*    1056     LD      (COPYNK+3),A
050A* C0 0418*    1057     CALL   GETND
050B* E1          1058     POP     HL
050C* 22 SC5D     1059     LD      (CM_ADD),HL
050D* 80 48 03E8* 1060     LD      BC,(COPYNK)
050E* 8C 58 03EA* 1061     LD      DE,(COPYNK+2)
050F* 1062     I
0510* 1063     I
0511* 1064     EXCH5C I
0512* 1065     I
0513* 1066     I
0514* 1067     I
0515* 1068     I
0516* 1069     I
0517* 1070     I
0518* 1071     I
0519* 1072     I
0520* 1073     I
0521* 1074     I
0522* 1075     I
0523* 1076     I
0524* 1077     I
0525* A7          1078     AND     A
0526* CA 00CA*    1079     JP      Z,INVPAR
0527* FE 19       1080     CP      ZS
0528* 82 00CA*    1081     JP      NC,INVPAR
0529* F5          1082     PUSH   AP
0530* D9          1083     PUSH   DE
0531* 50          1084     LD      B,B
0532* 41          1085     LD      B,C
0533* C0 C3A8*    1086     CALL   STADRS
0534* DA 00CA*    1087     JP      C,INVPAR
0535* D1          1088     POP     DE
0536* E5          1089     PUSH   HL
0537* 43          1090     LD      B,E
0538* CC 05A8*    1091     CALL   STADRS
0539* 0A 00CA*    1092     JP      C,INVPAR
053A* 01          1093     POP     DE
053B* F1          1094     POP     AP
053C* 85          1095     PUSH   HL
053D* 85          1096     PUSH   DE
053E* F5          1097     PUSH   AP
053F* F5          1098     PUSH   AP
0540* 86 80       1099     LD      B,B
0541* E5          1100     EXCH0 EXCH1
0542* 85          1101     PUSH   HL
0543* D9          1102     PUSH   DE
0544* C9          1103     PUSH   BC

```

```

0584* 01 0020          1103      LD      BC,32          ; NO. OF BYTES IN SCAN ROW FOR
0587* CD 0593*         1104      |          ; A LINE ON THE SCREEN
0588* C1               1105      CALL   EXCHLP      ; EXCHANGE BYTES
0589* 01               1106      POP    BC          ; SCAN COUNT IN B
0590* 01               1107      POP    DE          ;
0591* 01               1108      POP    HL          ; ADDRESSES
0592* 14               1109      INC    B          ; ADJUST TO NEXT SCAN ROW
0593* 24               1110      INC    H          ;
0594* 10 P0           1111      DJNZ  EXCH1       ; DO NEXT ROW
0595* P1               1112      POP    AP          ; LINE COUNT IN A
0596* 15               1113      DEC    D          ; ADJUST ADDRESSES BACK 1 SCAN ROW
0597* 25               1114      DEC    M          ;
0598* 30               1115      DEC    A          ; ADJUST LINE COUNT
0599* 28 12           1116      JR     Z,EXCH2    ; DONE
0600* P3               1117      PUSH  AP          ; REMAINING LINE COUNT
0601* 01 0020         1118      LD     BC,32      ; ADJUST ADDRESSES TO START
0602* 09               1119      ADD   HL,BC       ; OF NEXT LINE
0603* 3E 78           1120      LD     A,78H     ;
0604* A4               1121      AND   H          ;
0605* 67               1122      LD     W,A       ;
0606* 88               1123      EX    DE,HL      ;
0607* 09               1124      ADD   HL,BC       ;
0608* 3E 78           1125      LD     A,78H     ;
0609* A4               1126      AND   H          ;
0610* 67               1127      LD     W,A       ;
0611* 88               1128      EX    DE,HL      ;
0612* 18 06           1129      JR     JC,NO     ;
0613* P1               1130      EXCH2 EXCH2      ; DO NEXT LINE
0614* 0F               1131      POP    AP          ; A=ORIG. LINE COUNT
0615* 16 00           1132      LD     L,A       ; LINE COUNT
0616* 29               1133      ADD   HL,HL       ;
0617* 29               1134      ADD   HL,HL       ;
0618* 29               1135      ADD   HL,HL       ;
0619* 29               1136      ADD   HL,HL       ;
0620* 29               1137      ADD   HL,HL       ;
0621* 44               1138      LD     B,M       ;
0622* 4D               1139      LD     C,L       ;
0623* 4D               1140      LD     M,B       ;
0624* C1               1141      POP    C         ;
0625* CD 0492*         1142      CALL  CALCATT     ; COUNT TO BC
0626* 09               1143      EX    CE,HL      ; SOURCE 1 ADRS.
0627* 01               1144      POP    HL         ; GET ADRS. OF ATTRIBUTE FILE
0628* CD 0492*         1145      CALL  CALCATT     ; SOURCE 1 ATTRIBUTE ADRS. IN DE
0629* CD 0593*         1146      CALL  EXCHLP      ; SOURCE 2 ADRS.
0630* C3 008F*        1147      JP    GDCORRET   ; SOURCE 2 ATTRIBUTE ADRS. IN HL
0631*                  1148      |          ; EXCHANGE ATTRIBUTE BYTES
0632*                  1149      |          ; RETURN B=0
0633*                  1150      |          ;
0634*                  1151      |          ; EXCHANGE LOOP
0635*                  1152      |          ; EXCHANGES DATA BETWEEN TWO
0636*                  1153      |          ; MEMORY AREAS. ADDRESSES IN
0637*                  1154      |          ; DE AND HL, BYTE COUNT IN BC
0638*                  1155      |          ;
0639* C5               1155      EXCHLP PUSH BC    ; SAVE COUNT
0640* 7E               1156      LD     A,(HL)    ; DATA FROM SOURCE 2
0641* 4F               1157      LD     C,A       ; TO WKG.REG.
0642* 1A               1158      LD     A,(DE)    ; DATA FROM SOURCE 1
0643* A9               1159      XOR   C         ; EXCHANGE VIA XOR
0644* F3               1160      PUSH  AP         ; SAVE INTERMEDIATE RESULT
0645* A9               1161      XOR   C         ; A NOW CONTAINS DATA FOR SOURCE 2
0646* 4F               1162      LD     C,A       ; C=NEW DATA FOR SOURCE 2
0647* P1               1163      POP    AP         ; INTERMEDIATE RESULT
0648* A9               1164      XOR   C         ; A=NEW DATA FOR SOURCE 1
0649* 12               1165      LD     (DE),A    ; WRITE NEW DATA
0650* 71               1166      LD     (HL),C    ;
0651* 13               1167      INC   DE         ; ADJUST TO NEXT BYTE
0652* 23               1168      INC   HL         ;
0653* C1               1169      POP    BC        ;
0654* 0B               1170      DEC   BC         ; DECREMENT COUNT
0655* 78               1171      LD     A,B       ;
0656* 01               1172      OR    C         ;
0657* 20 EC           1173      JR     NZ,EXCHLP ; TEST IF DONE
0658* C9               1174      RET              ;
0659*                  1175      |          ;
0660*                  1176      |          ;
0661*                  1177      |          ; SUB-RTN. TO GET
0662*                  1178      |          ; OF ADRS. OF LINE IN B (0-23)
0663*                  1179      |          ; FOR DP IN D (1 OR 2)
0664*                  1180      |          ; RETURNS CARRY IF INVALID PARAMETER
0665* 78               1181      GTADRS LD A,B    ;
0666* PE 18            1182      CP    24         ;
0667* 30 1E            1183      JR    NC,GAERR  ; TEST VALID LINE NO.
0668* 0F               1184      RRCA          ;
0669* 0F               1185      RRCA          ;
0670* 0F               1186      RRCA          ;
0671* 0F               1187      AND   ANO       ;
0672* 0F               1188      LD     L,A       ;
0673* 78               1189      LD     A,B       ;
0674* E6 18            1190      AND   IBM       ;
0675* P6 40            1191      OR    40H      ;
0676* 67               1192      LD     W,A       ;
0677* 7A               1193      LD     A,D       ;
0678* A7               1194      AND   A         ;
0679* 28 0E            1195      JR    Z,GAERR   ; ERROR IF NOT 1 OR 2
0680* PE 03            1196      CP    3         ;
0681* 30 0A            1197      JR    NC,GAERR  ;
0682* C8 47            1198      BIT   0,A       ;
0683* 20 04            1199      JR    NZ,GYADR1 ; TEST WHICH DP
0684* 7C               1200      LD     A,M       ;
0685* P6 20            1201      OR    20H      ;
0686* 67               1202      LD     W,A       ;
0687* 0F               1203      LD     A         ;
0688* A7               1204      GTADR1 AND A     ;
0689* C9               1205      RET              ; RETURN NO CARRY
0690* 37               1206      GAERR SCP      ;
0691* C9               1207      RET              ; SET CARRY
0692*                  1208      |          ;

```



```

1210          SUBTTL  INTERNAL SUB-RTNS.
1211          |
1212          |
1213          |
1214          | INPUT: VIDEO MODE IN A
1215          | OUTPUT: M/W SETTING IN B
1216          |          VIDMOD SETTING IN C
1217          | RETURNS NZ STATUS IF MODE INVALID
1218          |
1219          | VALID VALUES:  INPUT      M/W      VIDMOD
1220          |          -----
1221          |          =DISPLAY FILE ACTIVE AT SCREEN
1222          |
1223          |          DP1 ONLY      0      0      0
1224          |          DP1& DP2=    00     0      90
1225          |          DP1 & DP2=    1      1      01
1226          |          HIGH RES.GR.  2      2      2
1227          |          04/80-COLUMN  04 - 38  06 - 3E  46 - 7E
1228          |
09CD*  AP          1228  GETVAL  LD      C,A
09CE*  C8 47      1229          BIT   0,A
09D0*  20 16      1230          JR    NZ,TST01
09D2*  PE 80      1231          CP    00H
09D4*  20 1A      1232          JR    Z,SETO
09D6*  C8 7F      1233          BIT   7,A
09D8*  C8        1234          RET   NZ
09DA*  47        1235          LD    0,A
09DC*  PE 02      1236          CP    Z
09DE*  C8        1237          RET   Z
09E0*  E6 C6      1238          AND  0C6H
09E2*  E2 06      1239          XOR  6
09E4*  C8        1240          RET   NZ
09E6*  3E 40      1241          LD    A,40H
09E8*  91        1242          OR   C
09EA*  AP        1243          LD    C,A
09EC*  AP        1244          XOR  A
09EE*  C9        1245          RET   A
09F0*  PE 01      1246          TST01 CP    1
09F2*  C8        1247          RET   NZ
09F4*  06 01      1248          LD    0,1
09F6*  0E 01      1249          LD    C,01H
09F8*  C9        1250          RET   A
09FA*  AP        1251          SET0  XOR  A
09FC*  47        1252          LD    0,A
09FE*  C9        1253          RET   A
1254          |
1255          |
1256          |          | ROUTINES TO ENABLE CHUNK 0 IN ROM EXT.
1257          |          | FOR ACCESS TO CHNGVID ROUTINE
1258          |
09FB*  PS          1258  ENBLEXT PUSH  AP
09FD*  C8  FF      1259          IN   A,(HREXPT)
09FF*  06 40      1260          CP    00H          | SELECT ROM EXT.
09F0*  03  FF      1261          OUT  (HREXPT),A
09FA*  3E 03      1262          LD    A,3
09FC*  03  F4      1263          OUT  (DRMSPT),A
09FE*  F1        1264          POP  AP
09FF*  C9        1265          RET
1266          |
0600*  PS          1266  ENBLNDME PUSH  AP
0601*  AP          1267          XOR  A
0602*  03  F4      1268          OUT  (DRMSPT),A
0604*  0B  FF      1269          IN   A,(HREXPT)
0606*  E6 3F      1270          AND  03FH
0608*  03  FF      1271          OUT  (HREXPT),A
060A*  F1        1272          POP  AP
060C*  C9        1273          RET
1274          |
1275          |
1276          |          | TEST PARAMETER VALIDITY
1277          |          | IF NOT VALID, DISCARDS RETURN
1278          |          | AND EXITS VIA INVPAR
1279          |          | TEST LINE 323
060C*  3E 17      1279  TSTPAR LD    A,23
060E*  08        1280          CP    0
0610*  30 04      1281          JR    NZ,TSTPRI
0612*  F1        1282          PARERR POP  AP
0614*  C3 00C4*   1283          JP    INVPAR
0616*  3A 0033*   1284          TSTPRI LD    A,(CLINLEN)
0618*  3D        1285          DEC  A
061A*  09        1286          CP    C          | TEST COL. 331
061C*  30  PS     1287          JR    C,PARERR
061E*  C9        1288          RET
1289          |
1290          |          | CONVERT LINE/COL. FROM USER TO
1291          |          | INTERNAL FORMAT AND VICE VERSA
1292          |          | USER FORMATS LINES 0-23
1293          |          |          CCL:0-31
1294          |          |          INTERNAL FORMATS LINES 24-1
1295          |          |          COL. 33-2
1296          |          |          (COL.1 840 OF LINE)
061D*  3A 0033*   1296  CONVPH LD    A,(CLINLEN)
061F*  3C        1297          INC  A
0621*  91        1298          SUB  C
0623*  4F        1299          LD    C,A
0625*  3E 18      1300          LD    A,SCRSZ
0627*  90        1301          SUB  0
0629*  47        1302          LD    0,A
062B*  C8        1303          RET
1304          |
1305          |
1306          |
1307          |          | ENTER HERE WITH NC=LINE/COL.IN
1308          |          |          INTERNAL PCRMAT
1309          |          |          CALCULATE POSITION
1310          |          |          STORE UPDATED POSITION AND RETURN
0628*  C8 062D*   1307  UPDPSN          CALL  CALCPDS
062A*  18 18     1310          JR    SPSN
1311          |
1312          |
1313          |          | ROUTINE TO CALCULATE POSITION IN DP
1314          |          | RETURNS DP ADRS. IN HL. PRESERVES
1315          |          | LINE/COLUMN POSITION IN BC
1316          |
062D*  C8 063A*   1317  CALCPDS CALL  LMBU
062F*  3A 0033*   1318          LD    A,(CLINLEN)
0631*  3C        1319          INC  A
0633*  91        1320          SUB  C
0635*  5F        1321          LD    E,A
0637*  16 00      1322          LD    0,0
0639*  19        1323          ADD  HL,DE
063B*  C9        1324          RET

```

```

1325 I
1326 I
1327 I LNRU: I GET DISPLAY FILE ADDRESS
1328 I FOR START OF LINE IN 0
063A* 3E 18 1329 LD A,SCPSZ
063C* 90 1330 SUB B
063D* 57 1331 LO D,A
063E* 0F 1332 RRCA
063F* 0F 1333 RRCA
0640* 0F 1334 RRCA
0641* E6 E0 1335 AND 0E0H
0643* 6F 1336 LO L,A
0644* 7A 1337 LO A,D
0645* E6 18 1338 AND 18H
0647* P6 40 1339 OR 40H
0649* 67 1340 LO M,A
064A* 3A SCC2 1341 LO A,(VIDMOD)
064D* C8 47 1342 BIT 0,A I TEST IF UP1
064E* C8 1343 RYI Z I SET TO DP2
0650* 3E 20 1344 LO A,Z0H
0652* B4 1345 OR H
0653* 67 1346 LO M,A
0654* C9 1347 RYI
1348 I
0655* 1349 STPOS: I STORE CURSOR POSITION
0655* ED 43 0036* 1350 LD (CURPOS),BC
0659* 22 0036* 1351 LD (OPADRS),HL
065C* C9 1352 RET
1353 I
065D* 1354 LDPOS: I LOAD CURSOR POSITION
065D* ED 48 0036* 1355 LD BC,(CURPOS)
0661* 2A 0036* 1356 LD HL,(OPADRS)
0664* C9 1357 RET
1358 I
1359 I
1360 I
1361 I
0665* 1362 UPDAT: I UPDATE ATTRIBUTE BYTE FOR CHARACTER
1363 I JUST PRINTED
1364 I HL = ANT SCAN OF CHAR. IN OP
1365 I
0665* CD 0692* 1366 CALL CALCATT I ADRS. OF ATTRIBUTE BYTE TO HL
0668* 3A 0039* 1367 LD A,(ATTBYT) I
0669* 3F 1368 E,A I CURRENT ATTR. VALUE TO E
066C* 3A 003F* 1369 LD A,(ATTMSK) I
066F* 57 1370 D,A I MASK TO 0
0670* 3A 0036* 1371 LD A,(MASK0) I
0673* 47 1372 LO B,A I PLGS TO 8
0674* 7E 1373 LO A,(HL) I BYTE FROM ATTRIBUTE FILE
0675* AB 1374 XOR E
0676* 42 1375 AND D
0677* AB 1376 XOR E I NEW ATTR. FOR 0: OLD FOR 1
0678* C8 60 1377 BIT 4,B I SET INK COMPLEMENT OF PAPER?
067A* 28 09 1378 JR Z,UPDAT1 I
067C* 84 F0 1379 AND 0F0H I SET INK TO BLACK
067E* C8 6F 1380 BIT 5,A I IS PAPER 4-7
0800* 20 02 1381 JR NZ,UPDAT1 I YES - USE BLACK INK
0802* P6 07 1382 OR 7 I NO - USE WHITE INK
0804* C8 70 1383 UPDAT1 BIT 4,B I SET PAPER COMPLEMENT OF INK?
0806* 28 04 1384 JR Z,UPDAT2 I
0809* E6 C7 1385 AND 0C7H I SET PAPER TO BLACK
080A* C8 57 1386 BIT 2,A I IS INK 4-7
080C* 20 02 1387 JR NZ,UPDAT2 I YES - USE BLACK PAPER
080E* P6 38 1388 OR 30H I NO - USE WHITE PAPER
0809* 77 1389 UPDAT2 LO (HL),A I TO ATTRIBUTE FILE
0809* C9 1390 RET
1391 I
1392 I I SUB-RTN. TO CALCULATE ADRS. OF
1393 I ATTRIBUTE BYTE FROM ADRS. CP
1394 I ANT SCAN ROW IN OP
1395 I
0692* 7C 1396 CALCATT LO A,H I UPPER BYTE OF ADDRESS
0693* 0F 1397 RRCA
0694* 0F 1398 RRCA
0695* 0F 1399 RRCA
0696* E6 03 1400 AND 03H
0698* P6 58 1401 OR 58H
069C* C8 6C 1402 BIT 5,M I TEST WHICH DP
069C* 28 02 1403 JR Z,CALCA1 I DP2
069E* P6 20 1404 OR 20H I ADRS. OF ATTRIBUTE IN DP2
08A0* 67 1405 CALCA1 LD M,A
08A1* C9 1406 RET
1407 I
08A2* 1408 LDATTA I LOAD INTERNAL ATTRIBUTE VARIABLES
1409 I FROM SYSTEM VARIABLES
1410 I SAVE A
08A1* P5 1411 PUSH AP I
08A3* 3A SC00 1412 LO A,(ATTR_P) I
08A4* 32 0039* 1413 LD (ATTBYT),A I
08A9* 3A SC0E 1414 LO B,(MASK_P) I
08AC* 32 003F* 1415 LD (ATTMSK),A I
08AF* 3A SC91 1416 LO A,(CP_PLAG) I
08B2* 8F 1417 RRCA I SHFT ODD BITS TO EVEN
08B3* 32 003A* 1418 LD (MASKB),A I
08B6* P1 1419 POP AP I
08B7* C9 1420 RET
1421 I
08B8* 3A SCC3 1421 GETSTRG LD A,(PARAM) I GET STRING ID
08B8* 84 1P 1422 AND 1PH I MASK OFF UPPER BITS
08BD* P6 40 1423 OR 40H I STRING VARIABLE IDENTIFIER
08BF* 57 1424 LD 0,A I SAVE IN D
08C0* 28 SC48 1425 LD HL,(VARS) I PIND STRING
08C3* 7E 1426 GETSTR1 LD A,(HL) I
08C4* 86 7F 1427 AND 07FH I TEST IF END OF VARS AREA
08CA* 28 13 1428 JR Z,NGSTRG I NO PIND - RETURN BC=2
08CB* 8A 1429 CP D I TEST IF MATCH
08C9* 28 08 1430 JR Z,GTSTR2 I POUND STRING
08CB* 05 1431 PUSH DE I SAVE STRING ID
08CC* CC 1720 1432 CALL RECLEM I RETURNS ADRS. OF NEXT VAR. IN DE
08CP* E8 1433 EX DE,HL I ADRS. TO HL
08D0* 01 1434 POP DE I RESTORE STRING ID
08D1* 18 P0 1435 JR GETSTR1 I LOCK AGAIN
08D3* 23 1436 GETSTR2 INC HL I GET LENGTH
08D4* 4E 1437 LD C,(HL)

```


0730'	PO	1548					
0731'	PO	1549		defb 11110000b		: code 8A	graphics
0732'	PO	1550		defb 11110000b			
0733'	PO	1551		defb 11110000b			
0734'	PO	1552		defb 11110000b			
0735'	PO	1553		defb 11110000b			
0736'	PO	1554		defb 11110000b			
0737'	PO	1555		defb 11110000b			
		1556		defb 11110000b			
		1557					
0738'	PP	1558		defb 11111111b		: code 8B	graphics
0739'	PP	1559		defb 11111111b			
073A'	PP	1560		defb 11111111b			
073B'	PP	1561		defb 11111111b			
073C'	PO	1562		defb 11110000b			
073D'	PO	1563		defb 11110000b			
073E'	PO	1564		defb 11110000b			
073F'	PO	1565		defb 11110000b			
		1566					
0740'	OO	1567		defb 00000000b		: code 8C	graphics
0741'	OO	1568		defb 00000000b			
0742'	OO	1569		defb 00000000b			
0743'	OO	1570		defb 00000000b			
0744'	PP	1571		defb 11111111b			
0745'	PP	1572		defb 11111111b			
0746'	PP	1573		defb 11111111b			
0747'	PP	1574		defb 11111111b			
		1575					
0748'	OP	1576		defb 00001111b		: code 8D	graphics
0749'	OP	1577		defb 00001111b			
074A'	OP	1578		defb 00001111b			
074B'	OP	1579		defb 00001111b			
074C'	PP	1580		defb 11111111b			
074D'	PP	1581		defb 11111111b			
074E'	PP	1582		defb 11111111b			
074F'	PP	1583		defb 11111111b			
		1584					
0750'	PO	1585		defb 11110000b		: code 8E	graphics
0751'	PO	1586		defb 11110000b			
0752'	PO	1587		defb 11110000b			
0753'	PO	1588		defb 11110000b			
0754'	PP	1589		defb 11111111b			
0755'	PP	1590		defb 11111111b			
0756'	PP	1591		defb 11111111b			
0757'	PP	1592		defb 11111111b			
		1593					
0758'	PP	1594		defb 11111111b		: code 8F	graphics
0759'	PP	1595		defb 11111111b			
075A'	PP	1596		defb 11111111b			
075B'	PP	1597		defb 11111111b			
075C'	PP	1598		defb 11111111b			
075D'	PP	1599		defb 11111111b			
075E'	PP	1600		defb 11111111b			
075F'	PP	1601		defb 11111111b			
		1602					
		1603					

END

ADL - ASC 004 DUAL SCREEN MODE SUPPORT CR200/11 version 10.36.14 16-Mar-86 12147:30
 GRAPHICS CHAR.SET IGDS.SRC

A	Reserved	ATTBYT	0039	ATTBTK	003F	ATTR8P	SC00	S	Reserved
BOTLN	0020	C	Reserved	CALCAT	0092	CALCA1	06A0	CALCPD	062D
CHNGVI	008E	CHPSET	3C00	CHTBL	002F	CHPADD	5C5D	CLIMIT	1000
CLRCTL	000C	CLRSB0	0104	CLRSCB	0008 IN	CLRSCM	0108 IN	CLRSCO	018E
CLRSC1	0302	CLRSC2	0200	CLRSC3	0211	CLRSC4	0214	CLRSC5	021F
CLRSC7	0254	CLRSX1	0280	CONVPM	0610	COOR05	5C7D	COPEM	0439
COPEM1	0434	COPY5B	0040 IN	COPY5C	043D IN	COPY50	030A	COPY51	0400
COPY52	0477	COPY53	0478	COPY54	047E	COPY55	0408	COPY56	0409
COPY57	04F0	COPY5K	03E0	COPYX1	CAC2	COPY01	03EC	COPY41	040D
COPY42	04AF	CURPOS	0034	O	Reserved	DATAB	0000	DSTY7	PTC0
OPADR5	0036	OKMSPT	00F4	DRIVES	0840	OTADP1	03C8	OTADP2	03D2
E	Reserved	ENBLEX	05F3	ENLMO	0600	ERRRET	00C1	EXCHLP	0593
EXCMS0	0043 IN	EXCMSC	052D IN	EXCMS0	0AF3	EXCHX1	0579	EXCM0	054F
EXCM01	0503	EXCM1	0581	EXTNRM	0385	PIX	97C0	PIXT6L	1000
PP2A	3193	PRPV	038A	GAERR	05C0	GETAB0	02DC	GETAT8	0018 IN
GETAT1	0280 IN	GETC80	02F1	GETCL	0019	GETCUB	0021 IN	GETCUR	02F1 IN
GETY1	0309	GETNO	0410	GETN1	C420	GETN2	042F	GETSTR	0608
GETVAL	05C0	GOODRE	00A0	GRBLK	C1A9	GPPHST	05E0	GRPST	0600
GRY0L	0021	GTADRS	05A0	GTAD01	23C9	GTCHAR	0260 IN	GTCH80	025C
GTCH80	0018 IN	GTCH1	0271	GTCH2	0276	GTCH23	0280	GTCH3	028D
GTCH31	0290	GTCH32	02A0	GTCH4	C2AF	GTCH5	02CA	GTCH6	02D9
GTINOX	003A	GTSTR1	06C3	GTSTR2	C6D3	H	Reserved	HREXPY	00FF
ININT	30F9	INSCBL	013F	INSERT	12C0	INVPAR	02C4	L	Reserved
LDATTR	06A2	LOPCSN	063D	LINGCL	0097	LIMLEN	0033	LNOU	063A
LOOP	0163	LCOP0	0166	LOOP1	016E	H	Reserved	MASKB	0030
MASKSP	5C8E	MCVBSZ	0040	NDSTPG	G6D0	PARAMS	5CC3	PARERR	0611
PRANT	5C84	PW	Reserved	PSFLAG	5091	RAMTOP	5C92	RECLN	1720
SCINIT	1701	SCPCTL	0028	SCRLO	082A IN	SCRLO0	0127	SCRCT	002E
SCRXT	0186	SCRLO	0141	SCROLL	C120 IN	SCP3Z	0018	SETC00	0117
SETCUB	0009 IN	SETCUR	0119 IN	SETMOB	0029 IN	SETMO0	032A	SETMO1	033A
SETND2	030A	SETND3	039F	SETMO1	0311 IN	SETM21	0397	SET0	05F0
SP	Reserved	SPARE0	0011	SPARE1	0013	SPARE2	0015	SPARE3	0017
SPARE4	003D	STBLK	01B1	STBLK0	01B5	STBLK1	01B6	STRND	5C45
STND00	0308	STPOSM	0655	STRGCY	0030	TESTAD	0151	TSTPAR	0600
TSTPR1	0415	TST01	03E0	TVPUL1	00C0	YVPUL1	00E4	UDC	5C78
UPDATE	03A2	UPDATY	0665	UPDAT1	2684	UPDAT2	0690	UPDPS	0620
VARS	5C48	VICMD	5CC2	VIMD0E	7324	WRCHAR	0049 IN	WRCHR0	0001 IN
WRCH17	000C	WRCH0	0046	WRCH01	334C	WRCH11	006B	WRCH12	0073
WRCH13	0077	WRCH14	008D	WRCH15	008E	WRCH2	0294	WRCH3	009F
WRCH5	00A7	WRCHT	00B3	WRSTLP	00F5	WRSTR0	0004 IN	WRSTG	00P2 IN
WRSTR0	00E4	WRSTR3	00FF	WRSTN1	0100	WRSTN2	0114		

No errors detected

Cross reference listing (MREP version 4.7)

Symbol	Refs (d = definition, s = write, blank = read)
ATTBYT	1370 557 1367 14120
ATTMSK	1640 1369 14140
ATYR_P	650 1411
BOYLN	1350 268 7320
CALCA1	1403 14090
CALCAT	429 546 677 1001 1004 1141 1144
	1366 13960
CALCPD	589 676 1309 13170
CHNGVI	520 772
CHRSET	490 167 742
CHYDL	1470 201 590 7430
CH_ADD	610 835 8400 8460 8570 862 8750
	8800 1040 10450 10480 10590
CLIMIT	480 737
CLRCYL	1040 481 7380
CLRS90	107 4800
CLRS00	441 493 4970
CLRS01	3100 567
CLRS02	3190
CLRS03	3220 576
CLRS04	3240 543
CLRS05	3330
CLRS07	310 3700
CLRS08	40 1070
CLRS09	30 4830
CLRS10	365 5600
CONVPM	337 580 675 686 12900
CORDS	640 7820
COPER1	8790
COPER0	847 870 8780
COPY01	830 8440
COPY41	945 9470
COPY42	972 9740
COPY50	166 8340
COPY51	9960 100
COPY52	9310
COPY53	9340 1023
COPY54	9360 956
COPY55	916 10080
COPY56	10200 1029
COPY57	927 1210 10260
COPY58	43 1640
COPY5C	43 860 3850
COPYUK	8030 8400 8530 8920 8940 950 859
	10500 10520 10540 10560 1060 1061
COPYXT	962 9900
CURPOS	1520 683 13900 1399
DATAB	360 174 7400
DEST7	150 76
OPQRS	1540 13510 1356
OKMSPT	500 12630 12699
ORIVES	720 73 74
OTADP1	8140
OTADP2	812 9210
EMBLEX	765 12500
EMBLND	773 12670
ERRREV	2590 263 280 700
EXCNO	10990 1129
EXCNO1	1043 10400
EXC01	11000 1111
EXCHLP	1105 1145 11550 1173
EXCH50	167 10370
EXCH5B	44 1670
EXCH5C	44 10640
EXCHXT	1116 11300
EXTNAM	750 763 7650
PIX	760
PXTBL	700
PP2A	550 872
PRPV	710 720 8060
GAER0	1103 1195 1197 12060
GETAB0	115 6710
GETAT	41 1150
GETATY	39 6740
GETC1	689 695 6970
GETC80	116 6830
GETCTL	1120 504 671 7310 7320
GETCUB	41 1160
GETCUR	39 8840
GETM1	8710
GETN2	865 8740
GETND	847 849 931 933 895 8620 876
	1049 1051 1053 1055 1057
GETSTR	292 837 1042 14210
GETVAL	766 12230
GOODRE	2580 339 362 494 726 784 819
	822 1006 1146
GRBLK	385 4430
GRPHST	530 150 744
GRPST	50 14560
GRYBL	1500 190 653 7450
STADR1	1199 12030
STADR5	892 897 1086 1091 11810
GTCM1	5930 856 863
GTCM2	5960 646
GTCM3	605 6080
GTCM3	401 6120
GTCM31	6140 620
GTCM32	633 635 6370
GTCM4	607 610 619 6410
GTCM5	652 6570
GTCM6	658 6440
GTCM8	30 5800
GTCM00	114 5810
GTCM80	41 1140
GVIIDX	1590 5930 603 630 630

GYSYR1	1426	1435					
GYSYR2	1430	1436					
MREXPY	57	1259	12618	1270	12720		
ININT	54	871					
INSDBL	386						
INSERT	73	754					
INVPAR	186	188	262	395	358	360	487
		490	492	721	809	881	888
		398	693	898	1079	1061	1087
		1092	1283				
LDATR	177	300	497	1408			
LDPSM	181	1354					
LINCGL	188	333	978	748	758		
LINLEN	191	213	66	982	687	736	777
		1284	1296	1318			
LNOU	371	505	1317	13270			
LOOP	389	449					
LOOPO	391	408					
LOOPI	398						
MASK	155	227	1371	14178			
MASK_P	68	1413					
MOVSI	74	75	759				
NCSTNG	1428	1448					
PARAM	71	1421					
PARERR	1282	1287					
PRAMT	69						
P_FLAG	67	1415					
RAHTOP	88	760					
RECLEM	93	1432					
SCIN17	47	729					
SCRC7L	119	282	348	738			
SCRL0	284	361	3648				
SCRL8	40	1228					
SCRL80	122	346					
SCRLCT	140	272	276	2818	734		
SCRLST	412	4158	470				
SCROLL	38	350					
SCRSI	46	265	365	499	788	1388	1329
SET0	1232	12318					
SET80	103	331					
SETCUB	40	103					
SETCUR	38	335					
SETM1	770	772					
SETH00	712	714	716	722			
SETH01	728						
SETH02	724	764					
SETH03	776	818	824				
SETH08	42	118					
SETH80	42	788					
SPARE0	188						
SPARE1	188						
SPARE2	118						
SPARE3	118						
SPARE4	143						
STBL	377	431					
STBL0	454	44					
STBL1	45						
STHND	62	757					
STH80	118	784					
STPSM	237	1318	1349				
STRCCT	161	322	748	7418			
TESTAD	375	414	475				
YST01	1238	1246					
YSTPAR	334	987	674	1278			
YSTP1	1281	1284					
YVPUL1	274	2818					
YVPUL2	212	249					
UDC	83	194	659				
UPDAT1	1378	1381	1383				
UPDAT2	1384	1387	1389				
UPDATE	777						
UPDATY	292	1362					
UPDPS	278	338	969	783	1387		
VARS	68	1425					
VIMOD	78	722	774	884	1361		
VIMODE	117	784					
WRCH0	97	172					
WRCH01	141	384					
WRCH11	183	198					
WRCH12	181	201					
WRCH13	197	288	282				
WRCH14	214	219					
WRCH15	218	228					
WRCH2	223						
WRCH3	238	232					
WRCH5	239	249					
WRCH7	258						
WRCH8	38	176					
WRCH88	40	97					
WRCH8T	237						
WRSTLP	381	315					
WRSTP0	88	292					
WRSTP3	389	328					
WRSTP8	40	98					
WRSTP4	38	388					
WRSTP1	387	318					
WRSTP2	328						

APPENDIX C-5

SPRITE GRAPHICS SUPPORT

Name: Sprite Graphics Support

Description: This component provides support for sprite graphics. A sprite is a graphical object which can be created and manipulated by a set of services to be described below. A sprite can be up to 256 x 256 characters in size, however at most 32 x 24 can be displayed. Each sprite is identified by a sprite-id, a number greater or equal to zero, and has the following properties:

DEFINITION - a pointer to a width x height character bitmap, where width and height are user defined. A 1 indicates foreground color, a 0 indicates background color.

RS111110 - the row, col location on the screen of the upper left corner of the sprite.

COLOR - the screen attribute (foreground color). A value of 0 indicates black, 1 indicates blue, etc.

SIZE - the width and height of the sprite (in characters).

The sprites graphics package assumes that the system variables area of memory is not used by an application. The sprite support services can be invoked from machine code and BASIC. There is a BASIC interface routine, which takes as its parameter a BASIC string variable. The BASIC interface and sprite services code is loaded at SPRCODE, where SPRCODE = DE000H. The name of this variable is specified by poking in the starting character at address SPRCODE-4. For example, if POKE SPRCODE-4, CODE "C" is executed, the variable C\$ will be used to pass commands to the interface routine. This must be done before using any sprite services. The interface routine is invoked by LET <variable> =USR SPRCODE. The status code returned by the last sprite service executed is assigned to <variable>.

The BASIC interface routine causes the report "A: Invalid argument" to be produced whenever an illegal command or invalid argument to a command is found. If too few or too many arguments are given for some command, the report "Q: Parameter error" is produced. If the command string variable cannot be found, the report "C: Nonsense in BASIC" is produced. If there is not enough memory for the number of sprites specified, the report "4: Out of memory" is produced (see InitSprites). There exists a variable called COMMAND, at address SPRCODE-9, which contains the number of the last sprite command to be executed. Thus if an error occurs in a sprite command, this variable can be PEEKed to find out which command in the command string caused the error. The first command in the command string is command zero.

The machine code routines all return status codes in the BC register pair. If the value in C is 0, then no error occurred. In this case B contains a value indicating further status information. If an error occurred, C will contain an odd value (thus bit 0 is set).

The BASIC interface string variable contains sprite graphics commands, separated by spaces. The commands have the following syntax:

"<command letter><parameter list>"

where <command letter> is a single upper or lower case letter identifying the command:

I = Init_Sprites
S = InitScreen
C = Create_Sprite
W = Set_Autowrap_Mode
P = Put_Sprite
E = Erase_Sprite
M = Move_Sprite
L = Change_Sprite_Location
A = Change_Sprite_Attribute
D = Overlap?
H = Horizontal_Scroll
V = Vertical_Scroll

and where <parameter list> is a list of numbers separated by commas. The string assigned to C\$ can be a string expression, i.e., "<substr>" + STR\$(<expr>) + "<substr>". The semantics of these commands will be described in detail below.

APPLICATION SERVICES

Name: Init_Sprites

Machine code interface:

Inputs: A = max-sprites, 0..255

Outputs: BC = 0000h - OK
 = 0001h - not enough memory

Entry: SPRSVC = SPRCCDE + 219H

BASIC interface:

Inputs: LET C8 = "I<max-sprites>"

Description: This service initializes the data structures used by the sprites services. Memory immediately below the sprites code is used for global variables and memory immediately below this is used to store sprite information. Each sprite takes up 8 bytes of data (not including its definition, which is stored elsewhere). Thus the total amount of memory used by the sprites package is:

8 bytes * max-sprites + 29 byte global area + 2746 byte code area

If there is not enough memory between RAMTOP and the global variables area for the number of sprites specified, 0001h is returned. Thus, RAMTOP should be set to

SPRCCDE - 29 - 8 max-sprites

before calling InitSprites. This service must be called before any of the others.

BASIC example: "I32" initializes memory for 32 sprites.

Name: Init_Screen

Machine code interface:

Inputs: A = screen_height, 0..24
 D = background_color, 0..7
 E = border_color, 0..7

Outputs: BC = 0000h - OK
 = 0007h - illegal screenht
 = 0005h - illegal color

Entry: SPRSVC + 200H

BASIC interface:

Inputs: LET C8 =
 "S<screen_ht>,<background_color>,<border_color>"

Description: This service clears the top screen_ht lines of the screen and sets their color to the background_color. The bottom 24-screen_ht lines are cleared and set to the border_color. The top screen_ht lines are used for displaying sprites. The remaining lines can be used for text display. The border is set to the border_color.

BASIC example: "S22,S,1" clears the top 22 lines and sets their color to cyan, clears the bottom two lines and sets their color to blue, and sets the border color to blue.

Name: Create_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1
B = width, 0..255
C = height, 0..255
D = color, 0..7
ML = definition address, 0..64K-1

Outputs: BC = 0000h - OK (sprite created)
= 0003h - illegal sprite-id

Entry: SPRSVC + 5FH

BASIC interface:

Inputs: LET Cs =
"C<sprite-id>,<width>,<height>,<color>,<addr>"

Description: This service creates a sprite with the specified width, height, color, and definition address. Initially, the position of the sprite is (0, 0). If there was already a sprite with the specified id, it is destroyed. In all further operations the new sprite is identified by its sprite-id.

BASIC example: "C0,2,2,2,32768" creates sprite 0 which is 2 x 2 characters in size, is red in color, and whose definition is located at 32768. The initial position of the sprite is (0, 0).

Name: Set_Autowrap_Mode

Machine code interface:

Inputs: A = mode: 0 - off, not 0 - on

Outputs: BC = 0000h - OK

Entry: SPRSVC + B0H

BASIC interface:

Inputs: LET Cs = "W<mode>"

Description: This service turns the Autowrap mode on and off. When the Autowrap mode is on, all changes to a sprite's location are made so that the new location is on the screen (wraparound).

BASIC example: "W1" causes Autowrap mode to be turned on.

Name: Put_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1
D = row, 0..255
E = column, 0..255

Outputs: BC = 0000h - OK, nothing overwritten
C100n - OK, something overwritten
0003h - illegal sprite-id

D = row
E = column

Entry: SPRSVC + EEH

BASIC interface:

Inputs: LET Cs = "P<sprite-id>,<row>,<column>"

Description: This service writes a sprite on the screen. The row and column described above define the location at which the upper left corner of the sprite is written. If the Autowrap mode is set, the row and column values are modified to make sure that the sprite location is a legal screen location. The new row and column values are returned. If the Autowrap mode is not set, the position is not changed. However, only those parts of the sprite corresponding to legal screen positions will be displayed. If writing the sprite causes something on the screen to be overwritten, the appropriate code is returned.

BASIC example: "P0,11,0" puts sprite 0 at row 11, column 0. Anything already at that location is overwritten. If anything is overwritten, the interface routine returns 10h.

Name: Erase_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1

Outputs: BC = 0000h - OK (sprite erased)
 0003h - illegal sprite-id

Entry: SPRSVC + 190H

BASIC interface:

Inputs: LET C\$ = "E(sprite-id)"

Description: This service erases a sprite. The screen becomes blank where the sprite used to be. The sprite still exists and can be written elsewhere.

BASIC example: "E0" erases sprite 0. The screen locations taken up by the sprite become blank.

Name: Move_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1
 D = relative vertical motion, -128..127
 E = relative horizontal motion, -128..127

Outputs: BC = 0000h - OK, nothing overwritten
 0100h - OK, something overwritten
 0003h - illegal sprite-id
 D = row
 E = column

Entry: SPRSVC + 200H

BASIC interface:

Inputs: LET C\$ = "M(sprite-id),<vert>,<hor>"

Description: This service moves a sprite. The sprite is erased at its current location and written at a new location defined by:

new row = old row + relative vertical motion
new col = old col + relative horizontal motion

The location of the sprite is updated to reflect the motion. As with the "P" command, if the sprite overwrites anything, 10h is returned. If Autowrap mode is set, then the location is automatically wrapped to fit onto the screen (i.e., (24, 0) becomes (0, 0)). The new row and column values are returned. If Autowrap mode is not set, only those parts of the sprite which correspond to legal screen locations will be displayed.

BASIC example: "M0,2,0" changes the position of the sprite from (11, 0) to (13, 0). This operation is equivalent to "E0 P0,13,0".

Name: Change_Sprite_Location

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1
 D = new row, 0..255
 E = new column, 0..255

Outputs: BC = 0000h - OK (location changed)
 0003h - illegal sprite-id

Entry: SPRSVC + 237H

BASIC interface:

Inputs: LET C\$ = "L(sprite-id),<new-row>,<new-col>"

Description: This service updates the position property of a sprite in the same fashion as for Put_Sprite. The sprite is not updated on the screen until a Move_Sprite, Put_Sprite, or Erase_Sprite is executed.

BASIC example: "L0,13,0" will set the location of sprite 0 to (13, 0).

Name: Change_Sprite_Attribute

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1
D = color, 0..7

Outputs: BC = 0000h - OK (color changed)
0003h - illegal sprite-id
0005h - illegal color

Entry: SPRSVC + 24DH

BASIC interface:

Inputs: LET C\$ = "A(sprite-id),<color>"

Description: This service updates the color property of a sprite. The sprite on the screen is not updated until a Put_Sprite, Move_Sprite, or Erase_Sprite is executed.

BASIC example: "A0,2" causes the color property of sprite 0 to be set to red.

Name: Overlap?

Machine code interface:

Inputs: D = sprite-id-1, 0..max-sprites - 1
E = sprite-id-2, 0..max-sprites - 1

Outputs: BC = 0000h - OK, no overlap
0200h - OK, overlap
0003h - illegal sprite-id

Entry: SPRSVC + 26BH

BASIC interface:

Inputs: LET C\$ = "D(sprite-id-1),<sprite-id-2>"

Description: This service is used to detect if two sprites overlap.

BASIC example: "00,1" causes the interface routine to return TBS if sprites 0 and 1 overlap on the screen.

Name: Vertical_Scroll

Machine code interface:

Inputs: A = direction: positive number - down,
negative number - up

Outputs: BC = 0000h - OK

Entry: SPRSVC + 55BH

BASIC interface:

Inputs: LET C\$ = "V<direction>"

Description: This service is used to scroll the entire screen in the vertical direction by one row. The direction of scroll is up if direction is less than 0; otherwise the scroll is down. The position property of all sprites is updated by one row to reflect the effect of the scroll. If Autowrap mode is not set, then 1 is added to the row value. If Autowrap mode is set, then 1 is added to the row value, and this new value is wrapped to fit on the screen.

BASIC example: "V1" causes the screen to be scrolled down one row.

Name: Horizontal_Scroll

Machine code interface:

Inputs: A = direction: positive number - right,
negative number - left

Outputs: BC = 0000h - DK

Entry: SPRSVC + 3BDH

BASIC interface:

Inputs: LET C6 = "M<direction>"

Description: This service is used to scroll the entire screen in the horizontal direction by one column. The direction of scroll is to the left if direction is less than 0, otherwise the scroll is to the right. The position property of all sprites is updated by one column to reflect the effect of the scroll. If Autowrap mode is not set, then 1 is added to the column value. If Autowrap mode is set, then 1 is added to the column value, and this new value is wrapped to fit on the screen.

BASIC example: "M1" causes the screen to be scrolled right one column.

CHDINT CR180/11 version 10.26.14 13-Feb-74 16:18:38
Command Interpreter module CHDINT.SPC

=0000

```

1          SETC CHDINT, ABS, LDC=0E000H
2          NAME CHDINT
3          SUBTTL Command Interpreter module
4
5
6          ;-----
7          ;
8          ; Command Interpreter module
9          ;
10         ; Inputs: none.
11         ;
12         ; Outputs: a Parameter Block (see below).
13         ;
14         ; Description: this module parses a sprites command string and, for
15         ; each recognized command, produces a parameter block
16         ; describing the command. This parameter block is passed
17         ; to the interface handler module. To parse a command
18         ; string first it must be found. The global sprites
19         ; variable NAME contains the name of the string variable
20         ; containing the command string. The RCM routine "iHd_N
21         ; is used to find the variable in the VARS area of memory.
22         ; Note: symbols in all upper case are external to this
23         ; module and are defined under Imports below.
24         ;
25         ;
26         ; Following is an architecture overview showing the relation
27         ; between the modules of the sprites services component of
28         ; the application development library:
29         ;
30         ;
31         ;
32         ;
33         ;
34         ;
35         ;
36         ;
37         ;
38         ;
39         ;
40         ;
41         ;
42         ;
43         ;
44         ;
45         ;
46         ;
47         ;
48         ;
49         ;
50         ;
51         ;
52         ;

```

```

-----
command
string
| BASIC |-----| Command |
| program |-----| Interpreter |
-----
Status
Code
Parameter Block |-----| Status Code
|
| I/P
| Handler |
|-----|
Register Values |-----| Status Code
|
|-----
Reg values
| assembly |-----| Sprite
| program |-----| Services |
-----
Status
Code
|
|

```



```

#0005      157  sbitmap      equ      color+1      |definition address
#0007      158  gflags      equ      sbitmap*2      |local sprite flags
#000C      159  alloc      equ      0              | bit 0 - allocated?
#0001      160  everwrite   equ      1              | bit 1 - sprite everwrite something
#0002      161  lns        equ      2              | bit 2 - sprite in lower half screen
162
163
164
165      | Locals
166
167      | state names for parser
#0000      168  s0          equ      0
#0001      169  s1          equ      1
#0002      170  s2          equ      2
#0003      171  s_end      equ      3
#00FF      172  s_error     equ      0FFH
173
174
175      |-----|
176      |
177      | CMDINT
178      |
179      | Inputs: none.
180      |
181      | Outputs: parameter block.
182      |
183      | Globals Read: none.
184      |
185      | Globals Written: name
186      |                      command
187      |                      gflags
188      |                      parms
189      |
190      | Procedures Called: find_string
191      |                      u_or_l_alpha
192      |                      ERROR
193      |                      letter
194      |                      minus
195      |                      digit
196      |                      number
197      |                      comma
198      |                      space
199      |                      I_P_HANDLER
200      |
201      | Procedures Called By: none.
202      |
203      | Description: this is the main routine of the command interpreter.
204      | The command string is located (using #INC_W) and is
205      | recognized by the following automaton:
206      |
207      |
208      |
209      |
210      |
211      |
212      |
213      |
214      |
215      |
216      |
217      |
218      |
219      |
220      |-----|
221      |
222
223      |
224      |
225      |
226      |
227      |
228      |
229      |
230      |
231      |
232      |
233      |
234      |
235      |
236      |
237      |
238      |
239      |
240      |
241      |
242      |
243      |
244      |
245      |
246      |
247      |
248      |
249      |
250      |
251      |
252      |
253      |
254      |
255      |
256      |
257      |
258      |
259      |
260      |
261      |
262      |
263      |
264      |
265      |
266      |
267      |
268      |
269      |
270      |
271      |
272      |
273      |
274      |
275      |
276      |
277      |
278      |
279      |
280      |
281      |
282      |
283      |
284      |
285      |
286      |
287      |
288      |
289      |
290      |
291      |
292      |
293      |
294      |
295      |
296      |
297      |
298      |
299      |
300      |
301      |
302      |
303      |
304      |
305      |
306      |
307      |
308      |
309      |
310      |
311      |
312      |
313      |
314      |
315      |
316      |
317      |
318      |
319      |
320      |
321      |
322      |
323      |
324      |
325      |
326      |
327      |
328      |
329      |
330      |
331      |
332      |
333      |
334      |
335      |
336      |
337      |
338      |
339      |
340      |
341      |
342      |
343      |
344      |
345      |
346      |
347      |
348      |
349      |
350      |
351      |
352      |
353      |
354      |
355      |
356      |
357      |
358      |
359      |
360      |
361      |
362      |
363      |
364      |
365      |
366      |
367      |
368      |
369      |
370      |
371      |
372      |
373      |
374      |
375      |
376      |
377      |
378      |
379      |
380      |
381      |
382      |
383      |
384      |
385      |
386      |
387      |
388      |
389      |
390      |
391      |
392      |
393      |
394      |
395      |
396      |
397      |
398      |
399      |
400      |
401      |
402      |
403      |
404      |
405      |
406      |
407      |
408      |
409      |
410      |
411      |
412      |
413      |
414      |
415      |
416      |
417      |
418      |
419      |
420      |
421      |
422      |
423      |
424      |
425      |
426      |
427      |
428      |
429      |
430      |
431      |
432      |
433      |
434      |
435      |
436      |
437      |
438      |
439      |
440      |
441      |
442      |
443      |
444      |
445      |
446      |
447      |
448      |
449      |
450      |
451      |
452      |
453      |
454      |
455      |
456      |
457      |
458      |
459      |
460      |
461      |
462      |
463      |
464      |
465      |
466      |
467      |
468      |
469      |
470      |
471      |
472      |
473      |
474      |
475      |
476      |
477      |
478      |
479      |
480      |
481      |
482      |
483      |
484      |
485      |
486      |
487      |
488      |
489      |
490      |
491      |
492      |
493      |
494      |
495      |
496      |
497      |
498      |
499      |
500      |
501      |
502      |
503      |
504      |
505      |
506      |
507      |
508      |
509      |
510      |
511      |
512      |
513      |
514      |
515      |
516      |
517      |
518      |
519      |
520      |
521      |
522      |
523      |
524      |
525      |
526      |
527      |
528      |
529      |
530      |
531      |
532      |
533      |
534      |
535      |
536      |
537      |
538      |
539      |
540      |
541      |
542      |
543      |
544      |
545      |
546      |
547      |
548      |
549      |
550      |
551      |
552      |
553      |
554      |
555      |
556      |
557      |
558      |
559      |
560      |
561      |
562      |
563      |
564      |
565      |
566      |
567      |
568      |
569      |
570      |
571      |
572      |
573      |
574      |
575      |
576      |
577      |
578      |
579      |
580      |
581      |
582      |
583      |
584      |
585      |
586      |
587      |
588      |
589      |
590      |
591      |
592      |
593      |
594      |
595      |
596      |
597      |
598      |
599      |
600      |
601      |
602      |
603      |
604      |
605      |
606      |
607      |
608      |
609      |
610      |
611      |
612      |
613      |
614      |
615      |
616      |
617      |
618      |
619      |
620      |
621      |
622      |
623      |
624      |
625      |
626      |
627      |
628      |
629      |
630      |
631      |
632      |
633      |
634      |
635      |
636      |
637      |
638      |
639      |
640      |
641      |
642      |
643      |
644      |
645      |
646      |
647      |
648      |
649      |
650      |
651      |
652      |
653      |
654      |
655      |
656      |
657      |
658      |
659      |
660      |
661      |
662      |
663      |
664      |
665      |
666      |
667      |
668      |
669      |
670      |
671      |
672      |
673      |
674      |
675      |
676      |
677      |
678      |
679      |
680      |
681      |
682      |
683      |
684      |
685      |
686      |
687      |
688      |
689      |
690      |
691      |
692      |
693      |
694      |
695      |
696      |
697      |
698      |
699      |
700      |
701      |
702      |
703      |
704      |
705      |
706      |
707      |
708      |
709      |
710      |
711      |
712      |
713      |
714      |
715      |
716      |
717      |
718      |
719      |
720      |
721      |
722      |
723      |
724      |
725      |
726      |
727      |
728      |
729      |
730      |
731      |
732      |
733      |
734      |
735      |
736      |
737      |
738      |
739      |
740      |
741      |
742      |
743      |
744      |
745      |
746      |
747      |
748      |
749      |
750      |
751      |
752      |
753      |
754      |
755      |
756      |
757      |
758      |
759      |
760      |
761      |
762      |
763      |
764      |
765      |
766      |
767      |
768      |
769      |
770      |
771      |
772      |
773      |
774      |
775      |
776      |
777      |
778      |
779      |
780      |
781      |
782      |
783      |
784      |
785      |
786      |
787      |
788      |
789      |
790      |
791      |
792      |
793      |
794      |
795      |
796      |
797      |
798      |
799      |
800      |
801      |
802      |
803      |
804      |
805      |
806      |
807      |
808      |
809      |
810      |
811      |
812      |
813      |
814      |
815      |
816      |
817      |
818      |
819      |
820      |
821      |
822      |
823      |
824      |
825      |
826      |
827      |
828      |
829      |
830      |
831      |
832      |
833      |
834      |
835      |
836      |
837      |
838      |
839      |
840      |
841      |
842      |
843      |
844      |
845      |
846      |
847      |
848      |
849      |
850      |
851      |
852      |
853      |
854      |
855      |
856      |
857      |
858      |
859      |
860      |
861      |
862      |
863      |
864      |
865      |
866      |
867      |
868      |
869      |
870      |
871      |
872      |
873      |
874      |
875      |
876      |
877      |
878      |
879      |
880      |
881      |
882      |
883      |
884      |
885      |
886      |
887      |
888      |
889      |
890      |
891      |
892      |
893      |
894      |
895      |
896      |
897      |
898      |
899      |
900      |
901      |
902      |
903      |
904      |
905      |
906      |
907      |
908      |
909      |
910      |
911      |
912      |
913      |
914      |
915      |
916      |
917      |
918      |
919      |
920      |
921      |
922      |
923      |
924      |
925      |
926      |
927      |
928      |
929      |
930      |
931      |
932      |
933      |
934      |
935      |
936      |
937      |
938      |
939      |
940      |
941      |
942      |
943      |
944      |
945      |
946      |
947      |
948      |
949      |
950      |
951      |
952      |
953      |
954      |
955      |
956      |
957      |
958      |
959      |
960      |
961      |
962      |
963      |
964      |
965      |
966      |
967      |
968      |
969      |
970      |
971      |
972      |
973      |
974      |
975      |
976      |
977      |
978      |
979      |
980      |
981      |
982      |
983      |
984      |
985      |
986      |
987      |
988      |
989      |
990      |
991      |
992      |
993      |
994      |
995      |
996      |
997      |
998      |
999      |
1000     |

```

```

2039 7E
203A CC 0009
203D 30 05
203E CD 00C8
2042 10 03
2044 0E 20
2046 20 05
2048 CC E10D
204B 10 0A
204D CD 00D0
2050 30 05
2052 CD E121
2055 10 00
2057 0E 2C
2059 20 05
205B CD E130
205E 10 C7
2060 0E 20
2062 20 05
2064 CD E104
2067 10 0E
2069 0E 0F
206B 10 0A
206D 0E
206E A7
206F 0E 32
2071 01
2072 20 02
2074 0A 00
2076 74
2077 32 DPEC
207A C5
207B E5
207C 19
207D CD 00010
2080 C5
2081 09
2082 C1
2083 09
2084 21 DPF7
2087 34
2088 01
2089 03
208A C1
208B 10 0F
208D 00
208E C3
208F 09
2090 C1
2091 C9
201 ld s, (hl) lget next char
202 call u_or_l_alpha nc, not_alpha ljump if not upper or lower case letter
203 jr call letter lotherwise handle letter
204 jr whilel
205 cp "-"
206 not_alpha jr nz, not_minus ljump if not minus sign
207 jr call minus lotherwise handle minus
208 jr whilel
209 not_minus call DIGIT
210 jr c, not_digit ljump if not digit
211 jr call number lotherwise handle number
212 jr whilel
213 cp "."
214 not_digit jr nz, not_comma ljump if not comma
215 jr call comma lotherwise handle comma
216 jr whilel
217 not_comma cp " "
218 jr nz, bad_char lnot a space either, so must be illegal
219 jr call space lhandle space
220 jr whilel
221 bad_char ld b, s_error lstate <- s_error
222 jr whilel
223 endl push hl lhere when exited inner loop
224 end
225 sbr hl, de
226 pop hl
227 jr z, do_cmd lset end of string, so execute
228 ld b, s0 lalso save string left
229 do_cmd ld a, c lfill in num_parms list of parm block
230 ld (num_parms), r
231 push bc
232 push de
233 push hl
234 call I_P_HANDLER linvoke interface handler module
235 push bc lsave status code in bc
236 ovr hc
237 ovr nc
238 ld hl, command lincrement command counter
239 inc (hl)
240 ovr hl
241 pop de
242 pop bc
243 jr whilel
244 endl ovr bc ldone with command string, so return
245 ovr bc llast status code in bc
246 ovr bc
247 ret
313 ;
314 ;
315 ; FIND_STRING
316 ;
317 ; Inputs: name (see global variables).
318 ;
319 ; Outputs: hl -> record for string variable if found
320 ;
321 ; Globals Read: CH_ADD
322 ; PLAGS
323 ; name
324 ;
325 ; Globals Written: CH_ADD
326 ; PLAGS
327 ;
328 ; Procedures Called: FIND_M
329 ; ERROR
330 ;
331 ; Procedures Called By: cndint
332 ;
333 ; Description: this routine attempts to find the record for a string
334 ; variable with name equal to the value of NAME. If found,
335 ; hl is set to point to the length field of the record. If
336 ; the variable is not found, report "C" is produced.
337 ;
338 ;
339 ;
340 find_string ld bc, (CH_ADD) lsave CH_ADD
341 push bc
342 ld a, (PLAGS) lsave PLAGS
343 push af
344 ld bc, name
345 ld (CH_ADD), bc lset CH_ADD to point to name
346 ld a, 00h
347 ld (PLAGS), a lset runtime flag, reset others
348 call FIND_M lfind variable
349 jr nc, f_ok ljump if found variable
350 rst ERROR lalso produce report "C"
351 defb 00h
352 f_ok ovr bc
353 ld a, b
354 ld (PLAGS), a lrestore PLAGS
355 ovr bc
356 ld (CH_ADD), bc lrestore CH_ADD
357 rst
358
2092 0D 4B SC5D
2096 C9
2097 3A 5C3A
209A 09
209B 01 DPPC
209E 0C 43 SC5D
20A2 3E 00
20A4 32 5C3A
20A7 CD 2C70
20AA 30 02
20AC CP
20AD 00
20AE C1
20AF 70
20B0 32 5C3A
20B3 C1
20B4 0D 43 SC5D
20B8 C9

```

```

360
361 |=====
362 |
363 | U_DR_L_ALPHA
364 |
365 | Inputs: current character in A.
366 |
367 | Outputs: sets carry flag if current letter is an upper or lower case
368 | letter. Otherwise, resets the carry flag.
369 |
370 | Globals Read: none.
371 |
372 | Globals Written: none.
373 |
374 | Procedures Called: none.
375 |
376 | Procedures Called By: cndint
377 |
378 | Description: this routine checks if the current character is an upper
379 | or lower case letter. If so, the carry flag is set.
380 | otherwise the carry flag is reset. If the character is a
381 | lower case letter, it is converted to an upper case letter.
382 |
383 |=====
384
385
386 u_or_l_alpha    ca    "A"        !check if upper case letter
387                ccf
388                ret    nc        !less than "A", so return
389                ca    "Z"+1
390                ret    c         !is upper case letter
391                ca    "a"
392                ccf            !check if lower case letter
393                ret    nc        !less than "a", so return
394                ca    "z"+1
395                ret    nc        !greater than "z", so return
396                sub    "a"- "A"
397                ccf            !convert to upper case
398                ret            !set carry flag
399
400
401 |=====
402 |
403 | LETTER
404 |
405 | Input: current character in A. This character is an upper case letter.
406 | hl = current string pointer.
407 | b = parser state
408 |
409 | Output: svc_code in the parameter block.
410 | b = parser state
411 |
412 | Globals Read: none.
413 |
414 | Globals Written: none
415 |
416 | Procedures Called: none.
417 |
418 | Procedures Called By: cndint
419 |
420 | Description: this routine translates the current character, which is
421 | known to be a letter, into a code. If this code = err,
422 | then the letter is not a legal command character. In this
423 | case state is set to s_error. Otherwise, the code is the
424 | service code appropriate to the command letter and is put
425 | into svc_code in the parameter block. state is set to sl.
426 |
427 |=====
428
429
430 err            equ    GPPH
431
432
433 letter        push    af
434                ld     b, s0
435                cp     b
436                jr     z, l_ah0    !state = s0, so ok
437                pop    af
438                ld     b, s_error  !otherwise, state is illegal
439                ret
440
441 l_ah0        pop    af
442                push    de
443                push    hl
444                ld     hl, l_table  !hl -> letter state table
445                sub    "A"
446                ld     d, 0
447                ld     e, 0
448                add    hl, de      !hl -> desired table entry
449                ld     a, (hl)
450                cp     err
451                jr     nz, l_ah1    !code is ok, so jump
452                ld     b, s_error  !else code is illegal
453                jr     l_exit
454                ld     b, (svc_code), a !otherwise put code in param block
455                ld     b, sl      !state <- sl
456                pop    hl
457                inc    hl          !advance string pointer
458                pop    de
459                ret
460
461 l_table      defb    7          !Change_Sprite_Attribute
462                defb    err
463                defb    1          !Create_Sprite
464                defb    err
465                defb    4          !Erase_Sprite
466                defb    err
467                defb    err
468                defb    10         !Horizontal_Scroll

```


A	Reserved	ALLGC	0003	AUTDWR	0001	S	Reserved	BACKGC	0000
BABSCN	E069	C	Reserved	CHSADD	1C50	CMOINT	E030	COL	0001
COLOR	0004	COMMA	E159	COMMAN	00F7	CSCK	E160	C	Reserved
DIGIT	3009	DDACMD	E074	E	Reserved	ENDW0	E080	ENDW1	E060
ERR	00FF	ERROR	000E	FINDN	0070	FINDS	E092	PLAGS	SC3A
PPZBC	3160	PIOK	E0AE	GPLAGS	00F8	GLOBAL	0010	"	Reserved
HEIGHT	0003	ININT	3009	ISPSMA	0001 EX	L	Reserved	LETTER	E0C9
LMS	0002	LEXIT	E0EF	LDRK	E0D9	LDRK1	ECE4	LOTABL	E0F3
M	Reserved	MAXCO	0020	MAXRD	0018	MAXSP	00FA	MINUS	E100
MSCK	E116	NAME	00FC	NEG	C000	NOTAL	E044	NOTCO	E060
NOTBD1	E057	NOTMI	Z040	NUMBER	F121	NUMPA	F121	OK	E02C
OVERNR	0001	PARM1	00E8	PARM1	00E0	PARM2	00E0	PARM3	00F1
PARM4	00F3	PARM5	00F5	PBITMA	C005	POS	E140	PSW	Reserved
RDW	0000	SCRATC	00E3	SCREEN	00F9	SPLAGS	0007	SP	Reserved
SPACE	E164	SPRCO	E000	SPRITE	00F8	SVCOC	00E0	SSEND	0003
SERR0	00FF	SOK	E16C	S	0000	S1	0001	S2	0002
USDRAL	E089	WHILE0	E01C	WHILE1	E027	WIDTH	0002		

No errors detected

Cross reference listing (MREF version 4.7)

Symbol	Refs (0 = definition, 1 = write, <blank> = read)
ALLOC	1590
AUTDWR	1310
BACKGC	1350 136
BAD_CM	279 2820
CH_ADD	1030 341 3400 3570 563 5650 5070
CMOINT	2260
CCL	1530 154
COLOR	1560 157
COMMA	276 6190
COMMAN	1360 137 2290 300
C_OK	921 6240
DIGIT	1090 270
DD_CMD	260 280 2900
ENDW0	260 3000
ENDW1	255 2040
ERR	4300 449 442 466 466 467 470
	471 474 477 480 481
	484 480
ERROR	1050 251 351
FIND_N	1080 349
FIND_S	233 3410
PLAGS	1020 343 3400 3550
PPZBC	1070 569
P_OK	350 3530
GPLAGS	1290 132 230 510 570
GLOBAL	1230
HEIGHT	1550 156
ININT	1060 566
I_MA	1110 295
LETTER	264 4330
LMS	1610
L_EXIT	452 4590
L_OK0	436 4400
L_OK1	450 4530
L_TABL	443 4610
MAX_CO	1210
MAX_RD	1200
MAX_SP	1320 133
MINUS	260 5120
N_OK	515 5180
NAME	1280 123 2270 345
NEG	1300 231 971 500
NOT_AL	263 2450
NOT_CO	275 2780
NOT_COI	271 2740
NOT_MI	267 2700
NUMBER	272 5610
NUM_PA	1440 145 2010
OK	250 2930
OVERNR	1000
PARM1	1450 146 245
PARM2	1460 147
PARM3	1470 148
PARM4	1480 149
PARM5	1490
PARMS	1370 138 143
PBITMA	1570 158
POS	572 - 5800
RDW	1520 153
S0	1680 243 246 209 434 693
S1	1670 454 513 625
S2	1700 591 619
SCRATC	1380
SCREEN	1330 135
SPLAGS	1580
SPACE	280 6930
SPRCO	1220 127 223
SPRITE	1270 128
SVC_CO	1430 144 4530
S_END0	1710 253 654
S_END1	1720 282 430 451 516 622
S_OK	655 6580
U_DR_L	262 3860
WHILE0	2440 305
WHILE1	2400 265 249 273 277 281 303
WIDTH	1500 155

```

1          NAME IPHANDLER
2          SUBTTL Interface handler module
3
4          |-----|
5          |
6          | Interface handler module
7          |
8          | Inputs: a parameter block.
9          |
10         | Outputs: a status code in BC.
11         |
12         | Description: this module translates a parameter block into a call to
13         | the appropriate sprite service. The number of parameters
14         | specified in the parm block is checked against a table
15         | entry for the service containing the correct number of
16         | parameters. If these values do not match, the report
17         | "C: Parameter error" is produced. Otherwise the interface
18         | routine appropriate to the service is invoked. This
19         | interface routine puts the values of the parms in the
20         | parm block into the appropriate registers. The sprite
21         | service is then invoked. When the service returns, the
22         | status code is checked and, if an error occurred, an
23         | appropriate report is produced.
24         |
25         |-----|
26
27
28         | Imports
29
30         +-----+
31         | ERROR          equ      0
32         | INIT_SPRITES  equ     0E219H
33         | INIT_SCREEN   equ     0E4E9H
34         | CREATE_SPRITE equ     0E278H
35         | SET_AUTOWRAP  equ     0E2C9H
36         | PUT_SPRITE    equ     0E307H
37         | ERASE_SPRITE  equ     0E381H
38         | MOVE_SPRITE   equ     0E426H
39         | CH_LGC        equ     0E450H
40         | CH_ATTR       equ     0E466H
41         | OVERLAP       equ     0E484H
42         | M_SCRCLL     equ     0E5D6H
43         | V_SCRCLL     equ     0E771H
44
45
46         | Globals
47
48         |-----+
49         | INCLUDE sprites.s
50
51         | Global constants
52
53         +-----+
54         | max_rows      equ      24          |number of rows on the screen
55         | max_cols      equ      32          |number of columns on the screen
56         | sprcode       equ     0E000H      |location of sprites code
57         | globcollen    equ      29          |length of global vars
58
59         | Global variables
60
61         +-----+
62         | sprites       equ     sprcode-2    |location of sprites data
63         | name          equ     sprites-2    |name of command string var.
64         | gflags       equ     name-1       |global flags
65         | res          equ      0           | bit 0 - parm < 0
66         | autowrap     equ      1           | bit 1 - autowrap mode flag
67         | max_sprites  equ     gflags-1     |number of sprites allocated
68         | screen_ht    equ     max_sprites-1|number of lines of screen used by
69         |              | sprites
70         | background   equ     screen_ht-1  |permanent background color
71         | command      equ     background-1 |number of commands being executed
72         | parms        equ     command-12   |parameter block
73         | scratchpad   equ     parms-8     |workspace area
74
75         | Data structures
76
77         |-----+
78         | | parameter block
79         | |
80         | | svc_code    equ     parms       |code identifying service
81         | | num_parms   equ     svc_code+1  |number of parms found
82         | | parm1      equ     num_parms+1  |first parm
83         | | parm2      equ     parm1+2     |
84         | | parm3      equ     parm2+2     |
85         | | parm4      equ     parm3+2     |
86         | | parm5      equ     parm4+2     |last possible parm
87
88         | | sprite data effects
89         | |
90         | | row        equ      0           |row position
91         | | col        equ     row+1       |column position
92         | | width      equ     col+1       |sprite width
93         | | height     equ     width+1     |sprite height
94         | | color      equ     height+1    |sprite color
95         | | attrmap    equ     color+1     |definition address
96         | | flags      equ     attrmap+2   |local sprite flags
97         | | alloc      equ      0           | bit 0 - allocated?
98         | | overwrite  equ      1           | bit 1 - sprite overwrite something
99         | | lns        equ      2           | bit 2 - sprite in lower half screen
100
101
102
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
126
127
128
129
130
131
132
133
134
135
136
137
138
139
140
141
142
143
144
145
146
147
148
149
150
151
152
153
154
155
156
157
158
159
160
161
162
163
164
165
166
167
168
169
170
171
172
173
174
175
176
177
178
179
180
181
182
183
184
185
186
187
188
189
190
191
192
193
194
195
196
197
198
199
200

```



```

201
202
203 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
204 ;
205 ; JMP_IX
206 ;
207 ; Input: IX = address to be called.
208 ;
209 ; Outputs: none.
210 ;
211 ; Globals Read: none.
212 ;
213 ; Globals Written: none.
214 ;
215 ; Procedures Called: none.
216 ;
217 ; Procedures Called By: i_f_handler
218 ;
219 ; Description: this routine is called to call the routine whose entry
220 ; point is in IX.
221 ;
222 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
223
224
225
226
227
228
229 ; This table contains the interface definitions for the sprite services. The
230 ; first item is the number of parameters to be passed to the service. The
231 ; second item is the interface routine for doing this. The third item is the
232 ; address of the service.
233
234
235
236
237
238
239
240
241
242
243
244
245
246
247
248
249
250
251
252
253
254
255
256
257
258
259
260
261
262
263
264
265
266
267
268
269
270
271
272
273
274
275
276
277
278
279
280
281 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
282 ;
283 ; I_F1
284 ;
285 ;
286 ; Input: ix -> first parameter
287 ;
288 ; Output: a c- value of first parameter
289 ;
290 ; Globals Read: none.
291 ;
292 ; Globals Written: none.
293 ;
294 ; Procedures Called: none.
295 ;
296 ; Procedures Called By: i_f_handler
297 ;
298 ; Description: this is the interface routine for sprite services with
299 ; one parameter.
300 ;
301 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
346
347
348
349
350
351
352
353
354
355
356
357
358
359
360
361
362
363
364
365
366
367
368
369
370
371
372
373
374
375
376
377
378
379
380
381
382
383
384
385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420
421
422
423
424
425
426
427
428
429
430
431
432
433
434
435
436
437
438
439
440
441
442
443
444
445
446
447
448
449
450
451
452
453
454
455
456
457
458
459
460
461
462
463
464
465
466
467
468
469
470
471
472
473
474
475
476
477
478
479
480
481
482
483
484
485
486
487
488
489
490
491
492
493
494
495
496
497
498
499
500
501
502
503
504
505
506
507
508
509
510
511
512
513
514
515
516
517
518
519
520
521
522
523
524
525
526
527
528
529
530
531
532
533
534
535
536
537
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580
581
582
583
584
585
586
587
588
589
590
591
592
593
594
595
596
597
598
599
600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681
682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719
720
721
722
723
724
725
726
727
728
729
730
731
732
733
734
735
736
737
738
739
740
741
742
743
744
745
746
747
748
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
769
770
771
772
773
774
775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
836
837
838
839
840
841
842
843
844
845
846
847
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865
866
867
868
869
870
871
872
873
874
875
876
877
878
879
880
881
882
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917
918
919
920
921
922
923
924
925
926
927
928
929
930
931
932
933
934
935
936
937
938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962
963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999
1000

```

E1P9* 00 7E 00
E1P8* 00 56 02
E1P8* C9

E1PC* 00 7E 00
E1PP* 00 56 02
E202* 00 5F 04
E203* C9

E206* 00 7E 00
E209* 00 46 02
E20C* 00 4E 04
E20P* 00 36 06
E212* 00 6E 08
E215* 00 66 09
E218* C9

```
306
307
308 |=====|
309 |
310 | I_F2
311 |
312 | Inputs: ix -> first parameter
313 |
314 | Outputs: a <- value of first parameter
315 |          d <- value of second parameter
316 |
317 | Globals Read: none.
318 |
319 | Globals Written: none.
320 |
321 | Procedures Called: none.
322 |
323 | Procedures Called By: i_f_handler
324 |
325 | Description: this is the interface routine for write services with
326 |             two parameters.
327 |
328 |=====|
329
330
331 I_F2          ld      a, (ix)
332              ld      d, (ix+2)
333              ret
334
335 |=====|
336 |
337 |
338 | I_F3
339 |
340 | Inputs: ix -> first parameter
341 |
342 | Outputs: a <- value of first parameter
343 |          d <- value of second parameter
344 |          e <- value of third parameter
345 |
346 | Globals Read: none.
347 |
348 | Globals Written: none.
349 |
350 | Procedures Called: none.
351 |
352 | Procedures Called By: i_f_handler
353 |
354 | Description: this is the interface routine for write services with
355 |             three parameters.
356 |
357 |=====|
358
359
360 I_F3          ld      a, (ix)
361              ld      d, (ix+2)
362              ld      e, (ix+4)
363              ret
364
365 |=====|
366 |
367 |
368 | I_F5
369 |
370 | Inputs: ix -> first parameter
371 |
372 | Outputs: a <- value of first parameter
373 |          b <- value of second parameter
374 |          c <- value of third parameter
375 |          d <- value of fourth parameter
376 |          hl <- value of fifth parameter
377 |
378 | Globals Read: none.
379 |
380 | Globals Written: none.
381 |
382 | Procedures Called: none.
383 |
384 | Procedures Called By: i_f_handler
385 |
386 | Description: this is the interface routine for write services with
387 |             five parameters.
388 |
389 |=====|
390
391
392 I_F5          ld      a, (ix)
393              ld      b, (ix+2)
394              ld      c, (ix+4)
395              ld      d, (ix+6)
396              ld      l, (ix+8)
397              ld      h, (ix+9)
398              ret
399
400
401          end
```

IPHANDLER CRZ80/11 version 10.36.14 15-May-84 9:52:10
 Interface handler module IPHANDLER.SPC

A	Reserved	ALLOC	0000	AUTDNR	0001	B	Reserved	BACKGC	0008
C	Reserved	CHGATT	E466	CHSLDC	E490	CCL	0001	CGLCR	0004
COMMAN	DPF7	CREATE	E278	D	Reserved	E	Reserved	ENOUGH	E1AF
ERASEB	E361	ERRCR	0008	GPLAGS	DPFB	GLOBAL	0010	"	Reserved
HEIGHT	0003	HASCPC	E506	INITSC	E4E9	INITSS	E219	ISFMA	E16E
ISPTA	E185	IS*1	E1P1	IS*2	E1P5	IOP3	E1PC	ISPS	E206
JMPML	E182	JMPDIX	E1B3	L	Reserved	LMS	0002	"	Reserved
MAXSCO	0020	MAXSRC	0019	MAXSP	OPPA	MCVEIS	E424	NAME	DPFC
NEG	0000	NUMSPA	CPEC	OK	E106	OVERLA	E484	DVERNR	0001
PARMS	DPB8	PARML	DFEC	PARM2	DPEF	PARM3	DPF1	PARM4	DPF3
PARMS	DPF5	PBITHA	0005	PSW	Reserved	PUTSP	E307	ROW	0000
SCRATC	DPB3	SCREEN	DPF9	SETBAU	E2C9	SPLACS	0007	SP	Reserved
SPRCOD	E000	SPRITE	DPFE	SVCACD	DPB8	VASCRO	E771	WIDTH	0002

No errors detected

SPRSVC CRZ80/11 version 10.36.14 15-May-84 9:51:01
 Sprite services module SPRSVC.SPC

```

1          NAME: SPRSVC
2          SUBTTL Sprite services module
3
4
5          !!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
6          !
7          ! Sprite Services module
8          !
9          ! Inputs: registers set to parameter values for called service.
10         !
11         ! Outputs: status code in BC: 0000 - OK
12         !          0001 - not enough memory
13         !          0003 - illegal sprite id
14         !          0005 - illegal color
15         !          0007 - illegal number-lines
16         !          0100 - OK, something overwritten
17         !          0200 - OK, overlap
18         !
19         ! Description: This module implements the various sprite graphics services.
20         ! Upon entry to this module, the parameters to the desired
21         ! service are put into the appropriate registers. A status
22         ! code is returned in BC (see above).
23         !
24         !!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
25
26
27         ! Imports (Note: all symbols in upper case are imported)
28
29         !From the Name
30
31         !Variables
32
33         *SC1A          equ          %C1AH
34         *SCB2          equ          %CB2H
35         *SC1B          equ          %C1BH
36         *SCD0          equ          %CD0H
37         *SCD8          equ          %CD8H
38         *IC1C          equ          %C1CH
39         *SC40          equ          %C40H
40         *OPPE          equ          %PEH
41
42         !Routines
43
44         *SB2           equ          %B2H
45         *FNQTV         equ          %QTVH
46         *SELECT       equ          %1230H
47         *CLS_B        equ          %97FH
48
49
50         !From module ICL
51
52         !Variables
53
54         *E890          equ          %E890H          ! Address of ROM Char.Table
55
56         !Services
57
58         *E890          equ          %E890H          WRITE_CHAR
59         *E90E          equ          %E90EH          SET_PRINT_POS
60         *E91F          equ          %E91FH          GET_CHAR
61
62
63         ! Globals
64
65         INCLUDE spritedef
66
67         ! Global constants
68
69         *0018          equ          24              !number of rows on the screen
70         *0020          equ          32              !number of columns on the screen
71         *E000          equ          %E000H          !location of sprites code
72         *0010          equ          29              !length of global vars

```



```

73
74 | Global variables
75
=OPPE 76 sprites      equ    sprcode-2      location of sprites data
=OPPC 77 name         equ    sprcode-2      name of command string var.
=CPPB 78 gflags      equ    name-1        global flags
=C000 79 neg         equ    0            | bit 0 - parm < 0
=C001 80 autoursap   equ    1            | bit 1 - autoursap mode flag
=CPPA 81 max_sprites equ    gflags-1      number of sprites allocated
=OPPY 82 screen_ht   equ    max_sprites-1  number of lines of screen used by
83 | sprites
84 bgcolor     equ    screen_ht-1    increment background color
=OPPT 85 command     equ    bgcolor-1      number of command being executed
=CPEB 86 parms      equ    command-12     parameter block
=OPEB 87 scratchpad equ    parms-5       workspace area
88
89 | Data structures
90
91 | parameter block
=CPEB 92 svc_code    equ    parms         |code identifying service
=CPEC 93 num_parms   equ    svc_code-1    number of parms found
=OPEB 94 parm1      equ    num_parms-1    |first parm
=OPEB 95 parm2      equ    parm1-2
=OPEB 96 parm3      equ    parm2-2
=OPEB 97 parm4      equ    parm3-2
=OPEB 98 parm5      equ    parm4-2      |last possible parm
99
100 | sprite data affects
=O000 101 row        equ    0            |row position
=O001 102 col        equ    row+1        |column position
=O002 103 width     equ    col-1        |sprite width
=O003 104 height   equ    width+1     |sprite height
=O004 105 color    equ    height+1     |sprite color
=O005 106 sbiteap  equ    color+1     |definition address
=C007 107 gflags     equ    sbiteap-2     |local sprite flags
=O000 108 alloc     equ    0            | bit 0 - allocated?
=C001 109 overwrite equ    1            | bit 1 - sprite overrode something
=O002 110 lns       equ    2            | bit 2 - sprite in lower half screen
111
112
113
114 |-----
115 |
116 | INIT_SPRITES
117 |
118 | Inputs: A = number-sprites, the number of sprites for which memory is
119 |           to be allocated.
120 |
121 | Outputs: BC = 0000 - OK
122 |           0001 - not enough memory
123 |
124 | Globals Read: RAMTOP
125 |
126 | Globals Written: screen_ht
127 |                 gflags
128 |                 max_sprites
129 |                 sprites
130 |                 sprites data structure
131 |
132 | Procedures Called: none.
133 |
134 | Procedures Called By: I_P_HANDLER
135 |                       user program
136 |
137 | Description: this routine allocates memory for sprite data. (max_sprites)
138 | is set to number-sprites. One sprite requires 8 bytes of
139 | data. If there is not enough free memory between (RAMTOP)
140 | and the sprites global variables, then 01 is returned.
141 | Otherwise, (sprites) is set to point to the start of the
142 | sprite data area and the sprite data is cleared.
143 |
144 |-----
145
=E219 146          ORG    O210H
147
148
149 init_sprites  ld      hl, screen_ht  |initialize screen_ht to 24
150              ld      (hl), max_rows
151              ld      hl, gflags
152              ld      (hl), 0         |clear global flags
153              ld      (max_sprites), a
154              ld      c, a
155              ld      b, 0
156              and     a
157              r1      c
158              r1      b
159              r1      c
160              r1      b
161              r1      c
162              r1      b
163              r1      c
164              r1      b
165              push   bc              |bc = 8*number-sprites
166              ld     hl, sprcode
167              and     a
168              sbc    hl, bc
169              ld     bc, globalien
170              and     a
171              sbc    hl, bc          |hl -> start of sprite data
172              pop    bc
173              ld     de, (RAMTOP)    |see if there is enough free memory
174              push  hl
175              and     a
176              sbc    hl, de
177              pop    hl
178              jr     nc, enough_room |if enough room, then jump
179              ld     bc, 0001        |else return 01
180              ret
181              enough_room  ld     (sprites), hl
182              clear_loop  ld     (hl), 0
183              cbc     bc
184              inc    hl
185              ld     de, c
186              or     c
187              jr     nc, clear_loop  |return 00

```



```

300 ;
301 ;
302 ; CHECK_ID
303 ;
304 ; Input: A = sprite-id
305 ;
306 ; Outputs: CF is set if sprite-id is legal, reset if it is illegal
307 ;
308 ; Globals Read: sprites
309 ;         sprite data structure
310 ;
311 ; Globals Written: none.
312 ;
313 ; Procedures Called: spr_addr
314 ;
315 ; Procedures Called By: put_sprite
316 ;                   erase_sprite
317 ;                   ch_loc
318 ;                   ch_attr
319 ;                   overlap
320 ;
321 ; Description: this routine checks (1) that sprite-id is in the range
322 ;             [0..max_sprites-1] and (2) that the record for that
323 ;             sprite-id has been initialized.
324 ;
325 ;
326 ;
327 ;
328 check_id      push    ix
329              push    hl
330              ld      hl, max_sprites
331              cb      (hl)
332              pop     hl
333              ret     nc          ;sprite-id out of range
334              call   spr_addr
335              rrc      (ix+flags)
336              rlc      (ix+flags) ;illoc flag is now in carry flag
337              pop     ix
338              ret
339 ;
340 ;
341 ;
342 ;
343 ; SET_AUTOWRAP
344 ;
345 ; Input: A = 0 - off
346 ;         <> 0 - on
347 ;
348 ; Output: BC = 0000 - OK
349 ;
350 ; Globals Read: none.
351 ;
352 ; Globals Written: gflags
353 ;
354 ; Procedures Called: none.
355 ;
356 ; Procedures Called By: I_P_HANDLER
357 ;                   user program
358 ;
359 ; Description: this routine sets the autowrap flag according to the
360 ;             value of A. If A = 0, the flag is reset; otherwise
361 ;             the flag is set.
362 ;
363 ;
364 ;
365 ;
366 set_autowrap  push    hl
367              ld      hl, gflags
368              and    a
369              jr     z, off          ;if A = 0, then jump
370              set    autowrap, (hl) ;false turn flag on
371              jr     z, off_exit
372              set    autowrap, (hl)
373              pop     hl
374              ld      bc, 0          ;return 00 in BC
375              ret
376 ;
377 ;
378 ;
379 ;
380 ; DISPLAY_CHAR
381 ;
382 ; Input: B = row
383 ;         C = column
384 ;         DE = location of dot pattern for char to be written
385 ;
386 ; Output: DE = location of next char
387 ;
388 ; Globals Read: none.
389 ;
390 ; Globals Written: CMTBL
391 ;
392 ; Procedures Called: SET_PRINT_POS
393 ;                   WRITE_CHAR
394 ;
395 ; Procedures Called By: put_sprite
396 ;                   erase_sprite
397 ;
398 ; Description: this routine displays the specified character at the
399 ;             specified screen location.
400 ;
401 ;
402 ;
403 ;
404 display_char  push    hl
405              push    bc
406              push    bc
407              call   SET_PRINT_POS ;set the print position to BC
408              pop     bc
409              ld      a, " "
410              ld      (CMTBL), de   ;replace (CMTBL) with loc of char
411              push    bc
412              call   WRITE_CHAR    ;write the character
413              pop     bc
414              ld      hl, B
415              add    hl, de
416              or     de, hl

```

```

E282* 00 85
E284* 81
E285* 21 0FFA
E286* 88
E287* 81
E288* 00
E289* 00 E25A*
E28E* 00 CF 07 0F
E2C1* 00 CF 07 06
E2C4* 00 81
E2C8* C9

```

```

E2C9* 85
E2CA* 21 0FFB
E2CD* 87
E2CE* 20 04
E2D0* C8 CE
E2D2* 18 02
E2D4* C8 8E
E2D6* 81
E2D7* 01 0000
E2DA* C9

```

```

E2D8* 85
E2DC* C5
E2DD* C5
E2DE* CC 890E
E2E1* C1
E2E2* 3E 10
E2E4* 8D 53 8990
E2E8* C5
E2E9* CD 8990
E2EC* C1
E2ED* 21 0000
E2FC* 19
E2F1* 85

```

```

E2P2* 21 3C00
E2P3* 22 0090
E2P6* C1
E2P9* 01
E2PA* C9

```

```

E2P8* 3A 0PP0
E2P9* 3D
E2PP* 08
E300* 3F
E301* 00
E302* 32 1P
E304* 09
E305* 3F
E306* C9

```

```

E307* F9
E308* 09
E309* F5
E32A* F3
E30B* C0 E202*
E30E* 30 06
E310* 01 00C3
E313* C3 E39A*
E316* C0 E25A*
E319* 3E 02
E31A* 09
E31C* CC 1230
E31P* 01
E320* 0C 0E 05
E323* 70 04 06
E326* 42
E327* 48
E328* 0C 70 30

```

```

417         ld      hl, 3C00h
418         (CTRL), hl      ; restore (CTRL) to original value
419         de_0x1
420         pop     bc
421         pop     hl
422         ret
423
424 ;-----
425 ;
426 ; VISIBLE
427 ;
428 ; Input: B = row
429 ;        C = column
430 ;
431 ; Output: CF set if BC is a legal screen location, otherwise CF is reset
432 ;
433 ; Globals Read: screen_ht
434 ;
435 ; Globals Written: none.
436 ;
437 ; Procedures Called: none.
438 ;
439 ; Procedures Called By: put_sprite
440 ;                   erase_sprite
441 ;
442 ; Description: this routine checks that the specified row and column
443 ;              are within the legal ranges for the screen width and
444 ;              height. If so, the carry flag is set, otherwise it is
445 ;              reset.
446 ;
447 ;-----
448 ;
449 ;
450         visible    ld      b, (screen_ht) ; is 0 <= row <= (screen_ht)-1
451                   dec     b
452                   ca     b
453                   ccf
454                   ret     nc          ; no, so return
455                   ld     b, max_cols-1 ; is 0 <= col <= max_cols-1
456                   ca     b
457                   ccf
458                   ret
459 ;
460 ;
461 ;-----
462 ;
463 ; PUT_SPRITE
464 ;
465 ; Input: A = sprite-id
466 ;        D = row
467 ;        E = column
468 ;
469 ; Output: BC = 0000 - OK, nothing overwritten
470 ;         = 0100 - OK, something overwritten
471 ;         = 0003 - illegal sprite-id
472 ;
473 ; Globals Read: sprite data structure
474 ;
475 ;         bgcolor
476 ;         gflags
477 ;         screen_ht
478 ;
479 ; Globals Written: sprite data structure
480 ;
481 ;         ATTR_P
482 ;
483 ; Procedures Called: check_id
484 ;                   spr_addr
485 ;                   SELECT
486 ;                   visible
487 ;                   GET_CHAR
488 ;                   display_char
489 ;
490 ; Procedures Called By: move_sprite
491 ;                   upd_cols
492 ;                   upd_rows
493 ;                   I_P_HANDLER
494 ;                   user program
495 ;
496 ; Description: this routine writes the specified sprite to the screen.
497 ;              If autwrap mode is set, then the specified row and column
498 ;              are wrapped to fit onto the screen size. If autwrap
499 ;              mode is not set, then only the portion of the sprite
500 ;              which is visible on the screen will be written.
501 ;
502 ;-----
503 ;
504 ;
505         put_sprite  push    af
506                   push    de
507                   push    hl
508                   di
509                   call    check_id    ; validate sprite-id
510                   jr     c, p_id_ok    ; sprite-id ok, so jump
511                   ld     bc, 0003    ; else return 03 in BC
512                   jp     a_exit
513                   call    spr_addr    ; get location of sprite record
514                   ld     de, 2       ; select stream 2
515                   push    de
516                   call    SELECT
517                   pop     de
518                   ld     l, (ix+bits) ; hl -> sprite bitmap
519                   ld     h, (ix+bits+1)
520                   ld     b, d         ; b = row
521                   ld     c, e         ; c = col
522                   ld     d, (ix+row), b ; store sprite position

```

```

E32B 00 71 01          521      ld      (ix+col), c
E32E 3A 0FF9          522      ld      a, (backcolor)
E331 86 F8            523      and
E333 00 56 04          524      or
E336 32 9C6D          525      ld      (ATTR_P), a      !set attr to screen background color
                                ! and sprite foreground color
E339 11 0100          526
E33C A7              527      ld      de, 100h
E33D 8D 52            528      and
E33F F8              529      orb   hl, de
                                !de -> first scan of sprite bitmap
                                ! minus 100h, acts like (CHARS)
                                !l = row count
E340 2E 00            530
E342 C5              531      push
E343 83              532      bc
E344 40              533      orl   sp, hl
E345 83              534      ld      c, l
                                !c <- leftmost column of sprite
E346 24 00            535      ld      n, 0
                                !n = column count
E348 CD E2F9          536      innerl call visible
                                !is BC a legal screen location?
E34B 38 20            537      jr    nz, no_jump
E34D 3A 0FF8          538      ld      a, (gflags)
                                !is autowrap mode set?
E34F C6 4F            539      bit   autowrap, a
E352 28 34            540      jr    nz, nextchar
                                !if not autowrap mode then jump
E354 3E 1F            541      col_loop ld      a, max_col-1
                                !map current column onto screen
E356 89              542      cb
E357 30 04            543      jr    nc, chrow1
E359 79              544      ld      a, a
E35A 06 20            545      orb   max_col
E35C 4F              546      ld      c, a
E35D 18 F5            547      jr    col_loop
E35F 85              548      innerl push hl
E360 21 0FF9          549      ld      hl, screen_ht
                                !map current row onto screen
E363 78              550      row_loop ld      a, (hl)
E364 3D              551      dec   a
E365 88              552      cb
E366 30 07            553      jr    nc, conti
E368 08              554      orl   of, of'
E369 7C              555      ld      a, b
E36A 94              556      orb   (hl)
E36B 47              557      ld      b, a
E36C 08              558      orl   of, of'
E36D 18 F4            559      jr    row_loop
E36F 81              560      conti pop hl
E370 7C              561      ld      a, h
E371 85              562      orl   l
E372 20 06            563      jr    nz, no_store
                                !first time through loops, store
E374 DC 70 00          564      no_store (ix+row), b
                                ! wrapped screen location
E377 DC 71 01          565      wrtchar push bc
E37A C5              566      call GET_CHAR
                                !check if writing the char will
E37B C8 E91F          567      pop   bc      ! overwrite something
E37E C1              568
E37F FE 20            569      cb

E381 28 04            570      jr    z, no_overwrite
E383 DC C8 07 CE          571      overwrite, (ix+oflags) !leave fact that something
                                ! was overwritten
E387 C8 E208          572      no_overwrite call display_char
E38A 0C              573      inc   c
                                !increment column position
E38B 24              574      inc   h
                                !increment column count
E38C 00 7E 02          575      row_loop ld      a, (ix+width)
E38E 8C              576      cb
E38F 20 06            577      jr    nz, innerl
                                !jump if more columns to write
E392 04              578      inc   b
                                !also increment row position
E393 2C              579      inc   l
                                !increment row count
E394 0C 7E 03          580      ld      a, (ix+height)
E397 80              581      cb
E398 20 A9            582      jr    nz, outerl
                                !jump if more rows to write
E39A C1              583      _exit pop bc
E39B F8              584
E39C 81              585      pop   hl
E39D 01              586      pop   de
E39E F1              587      pop   ei
E39F 0D C8 07 4E          588      bit   overwrite, (ix+oflags)
E3A3 00 C8 07 0E          589      row overwrite, (ix+oflags)
E3A7 28 94            590      jr    nz, overwrite
                                !sprite overwrite something
E3A9 01 0000          591      ld      bc, 0
                                !return 00 in BC, nothing overwritten
E3AC C9              592      overwrite ld
E3AD 01 0100          593      ret   bc, 0100h
E3B0 C9              594      ret

600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634

```

```

635
636
637 erase_sprite push af
638 push de
639 push hl
640 ei
641 call check_id invalidate sprite-id
642 jr e, e_id_ok !sprite-id ok, so jump
643 ld bc, 0003 !else return 03 in 0C
644 ja e_exit
645 e_id_ok call spr_addr !get location of sprite record
646 ld a, 2 !select stream 2
647 push de
648 call SELECT
649 pop de
650 ld b, (inrow) !B = row
651 ld e, (incol) !e = col
652 ld a, (backcolor)
653 ld (ATTR_7), a !set attr to screen background color
654 ld l, 0 !l = row count
655 push bc
656 outer2 ex (sp), hl
657 ld e, l !e ← leftmost column of sprite
658 ex (sp), hl
659 ld n, 0 !n = column count
660 inner2 call visible !is 0C a legal screen location?
661 jr c, ora_char !yes, so jump
662 ld b, (qflags) !is autorep mode set?
663 bit autorep, b
664 jr z, natcher !if not autorep mode then jump
665 c_loop ld a, max_col-1
666 to c
667 jr nc, cloop2
668 ld a, c
669 sub max_col
670 ld c, a
671 jr c, c_loop
672 cloop2 push hl
673 ld hl, screen_ht
674 ld a, (hl) !map current row onto screen
675 mov a, b
676 r_loop ex
677 jr nc, cont2
678 ex af, af'
679 ld a, b
680 sub (hl)
681 ld b, a
682 ex af, af'
683 jr r_loop
684 cont2 pop hl
685 ora_char ld de, (CHARS) !de → bitmap of blank char
686 call display_char
687 inc c !increment column position
688 inc h !increment column count
689 ld a, (ix*width)
690 cp h
691 jr nz, inner2 !jump if more columns to write
692 inc b !else increment row position
693 inc l !increment row count
694 ld a, (ix*height)
695 cp l
696 jr nz, outer2 !jump if more rows to write
697 pop bc !else done
698 ld bc, 0 !return 00 in 0C, nothing overwritten
699 e_exit ei
700 pop hl
701 pop de
702 pop sp
703 ret
704
705
706
707
708 ;=====
709 ;
710 ; MOVE_SPRITE
711 ;
712 ; Inputs: A = sprite-id
713 ; D = relative vertical action
714 ; E = relative horizontal action
715 ;
716 ; Outputs: 0C = 0000 - 0E, nothing overwritten
717 ; = C100 - 0E, something overwritten
718 ; = 0003 - illegal sprite-id
719 ;
720 ; Globals Read: screen_ht
721 ; gflags
722 ;
723 ; Globals Written: sprite data structure
724 ;
725 ; Procedures Called: erase_sprite
726 ; put_sprite
727 ;
728 ; Procedures Called By: I_P_HANDLER
729 ; user program
730 ;
731 ; Description: this routine moves the specified sprite by the specified
732 ; amount. It is equivalent to erase_sprite followed by
733 ; put_sprite.
734 ;=====
735

```

```

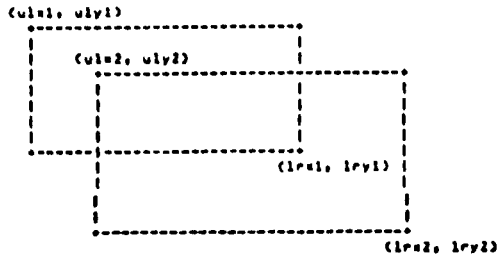
E426* 05          736  move_sprite  push  hl
E427* 05          737  push  af
E428* CD E301*   738  call  move_sprite
E429* C0 41      739  bit   0, c           ;if an error occurred, exit
E42D* 10 1E      740  jr    nz, m_exit
E42F* 0C 7E 00   741  ld   a, (ix+row)    ;update sprite position
E432* 02          742  add  a, d
E433* 21 0FF8     743  ld   hl, gflags
E436* C0 4E      744  bit   autotran, (hl)
E438* 20 08      745  jr    z, row_ok
E43A* C0 7F      746  bit   7, a           ;is row negative?
E43C* 20 04      747  jr    z, row_ok     ;no, so jump
E43E* 21 0FF9     748  ld   hl, screen_ht ;yes, so add it to (screen_ht)
E441* 06          749  add  a, (hl)
E442* 17          750  dc  a
E443* DD 7E 01   751  ld   a, (ix+col)
E446* 03          752  add  a, a
E447* 1F          753  ld   a, a
E448* F1          754  pop  af
E449* 05          755  push af
E44A* CD E307*   756  call  put_sprite
E44D* F1          757  pop  af
E44E* 01          758  pop  hl
E44F* C9          759  ret
760
761
762  ;-----
763  ;
764  ; CH_LOC
765  ;
766  ; Inputs: A = sprite-id
767  ;         D = new row
768  ;         E = new col
769  ;
770  ; Outputs: BC = 1000 - OK
771  ;           = 1001 - illegal sprite-id
772  ;
773  ; Globals Read: none.
774  ;
775  ; Globals Written: sprite data structure
776  ;
777  ; Procedures Called: check_id
778  ;                   spr_addr
779  ;
780  ; Procedures Called By: I_P_HANDLER
781  ;                   user program
782  ;
783  ; Description: this routine updates the row and column in the record for
784  ; the sprite identified by sprite-id.
785  ;
786  ;-----
787
788  ch_loc      call  check_id      ;validate sprite-id
789             jr    c, ch_l_id_ok ;if id is ok then jump
790             ld   bc, 0003       ;else return 03 in BC
791             ret
792
793  ch_l_id_ok  call  spr_addr      ;ix -> loc of sprite record
794             ld   (ix+row), d
795             ld   (ix+col), e
796             ld   bc, 00         ;return 00 in BC
797             ret
798
799
800  ;-----
801  ;
802  ; CH_ATTR
803  ;
804  ; Inputs: A = sprite-id
805  ;         D = new color
806  ;
807  ; Outputs: BC = 0000 - OK
808  ;           = 0001 - illegal sprite-id
809  ;           = 0005 - illegal color
810  ;
811  ; Globals Read: none.
812  ;
813  ; Globals Written: sprite data structure
814  ;
815  ; Procedures Called: check_id
816  ;                   spr_addr
817  ;
818  ; Procedures Called By: I_P_HANDLER
819  ;                   user program
820  ;
821  ; Description: this routine puts a new color into the record for the
822  ; sprite identified by sprite-id.
823  ;
824  ;-----
825
826
827  ch_attr     call  check_id      ;validate sprite-id
828             jr    c, ch_e_id_ok ;if id is ok then jump
829             ld   bc, 0003       ;else return 03 in BC
830             push af             ;validate color
831             ld   a, 7
832             dc  d
833             jr    nc, ch_a_color_ok ;jump if color ok
834             pop  af
835             ld   bc, 0005       ;else return 05 in BC
836             ret
837
838  ch_a_color_ok pop  af
839             call  spr_addr
840             ld   (ix+color), d
841             ld   bc, 0         ;return 00 in BC
842             ret

```

```

843
844 |-----|
845 |
846 | OVERLAP
847 |
848 | Inputs: D = sprite-id1
849 |         E = sprite-id2
850 |
851 | Outputs: SC = 0000 - OK, no overlap
852 |            = 0100 - OK, overlap
853 |            = 0003 - illegal sprite-id
854 |
855 | Globals Read: sprite data structure
856 |
857 | Globals Written: none.
858 |
859 | Procedures Called: check_id
860 |                   spr_addr
861 |
862 | Procedures Called By: upd_cols
863 |                   upd_rows
864 |                   I_P_HANDLER
865 |                   user program
866 |
867 | Description: this routine checks if two sprites overlap on the screen.
868 | This is done by comparing the ranges covered by each sprite
869 | in the x- and y-directions. If there is overlap in both
870 | directions, then the sprites must overlap. The following
871 | diagram defines terms mentioned below:
872 |
873 |
874 |
875 |
876 |
877 |
878 |
879 |
880 |
881 |
882 |
883 |
884 |
885 |
886 |
887 |
888 |
889 |
890 |-----|
891 |
892 |

```



```

E486 00 E9
E486 05
E487 05
E488 05
E489 7A
E48A CC E201
E48B 30 04
E48C 01 0003
E492 C9
E493 42
E494 48
E495 78
E496 CC E202
E497 30 FA
E498 78
E49C CC E25F
E49D 00 50 01
E4A2 00 50 02
E4A5 79
E4A6 CC E25F
E4A7 00 60 01
E4AC 00 60 02
E4AF 7A
E4B0 03
E4B1 30
E4B2 0C
E4B3 30 10
E4B5 7C
E4B6 85
E4B7 30
E4B8 8A
E4B9 30 10
E4BB 78
E4BC CC E25F
E4BD CC 50 00
E4C2 00 50 03
E4C5 79
E4C6 CC E25F
E4C9 00 60 10
E4CC 00 60 03
E4CF 7A
E4D0 03
E4D1 30
E4D2 9C
E4D3 30 00
E4D5 7C
E4D6 85
E4D7 30
E4D8 8A
E4D9 30 05
E4DB 01 0200
E4DE 10 03
E4E0 01 0300
E4E3 E1
E4E4 01
E4E5 01
E4E6 00 E1
E4E8 C9

```

```

893 overlap      push    ix
894              push    sp
895              push    de
896              push    hl
897              ld      a, d          ;validate sprite-id1
898              call   check_id
899              jr      c, e_ok1
900              ld      bc, 0003    ;illegal sprite-id, return 3 in BC
901              ret
902              ld      a, d
903              ld      c, e
904              ld      a, e          ;validate sprite-id2
905              call   check_id
906              jr      nc, e_id_no
907              ld      a, b
908              ld      a, b          ;here if both sprite-ids ok
909              ;check if there is any overlap in
910              ; the x-direction
911              call   spr_addr
912              ld      d, (ix+col)
913              ld      e, (ix+width)
914              ld      a, e
915              call   spr_addr
916              ld      h, (ix+col)
917              ld      l, (ix+width)
918              ld      a, d
919              dec    a
920              cp    h
921              jr      c, no_coll    ;if ul2 > lr1, then no overlap
922              ld      a, h
923              add    a, l
924              dec    a
925              cp    d
926              jr      c, no_coll    ;if ul1 > lr2, then no overlap
927              ld      a, b
928              ;check if there is any overlap in the
929              ; y-direction
930              call   spr_addr
931              ld      d, (ix+row)
932              ld      e, (ix+height)
933              ld      a, e
934              call   spr_addr
935              ld      h, (ix+row)
936              ld      l, (ix+height)
937              ld      a, d
938              add    a, e
939              dec    a
940              cp    h
941              jr      c, no_coll    ;if lr1 < ul2, then no overlap
942              ld      a, h
943              add    a, l
944              dec    a
945              cp    d
946              jr      c, no_coll    ;if lr2 < ul1, then no overlap
947              ld      bc, 0100    ;return 01, overlap
948              jr      e_ok1
949              ld      bc, 0
950              ;return 00, no overlap
951              pop    hl
952              pop    de
953              pop    sp
954              ret

```



```

955
956 |=====
957 |
958 | INITSBREEN
959 |
960 | Input: A = screen-height
961 |         0 = background-color
962 |         B = border-color
963 |
964 | Output: BC = 0000 - OK
965 |           = 0005 - illegal color
966 |           = 0007 - illegal screen-height
967 |
968 | Globals Read: ATTR_P
969 |              TVFLAG
970 |              screen_ht
971 |
972 | Globals Written: screen_ht
973 |                 ATTR_P
974 |                 bgcolor
975 |                 BORDCR
976 |                 TVFLAG
977 |
978 | Procedures Called: CLS_B
979 |
980 | Procedures Called By: I_P_HANDLER
981 |                      user program
982 |
983 | Description: this routine sets the screen_ht variable to the specified
984 | value.  If screen-height is not in the range 0 - 24, 07
985 | is returned in BC.  If background-color or border-color is
986 | not in the range 0 - 7, 05 is returned in BC.  The top
987 | screen-height lines of the screen are cleared and set to
988 | the specified permanent background color.  The remaining
989 | lines of the screen are cleared and set to the specified
990 | border color.
991 |
992 |=====
993
994
995
996
997
998
999
1000
1001
1002
1003
1004
1005
1006
1007
1008
1009
1010
1011
1012
1013
1014
1015
1016
1017
1018
1019
1020
1021
1022
1023
1024
1025
1026
1027
1028
1029
1030
1031
1032
1033
1034
1035
1036
1037
1038
1039
1040
1041
1042
1043
1044
1045
1046
1047
1048
1049
1050
1051
1052
1053
1054
1055

```

```

E4E9* P2 19
E4EB* 38 04
E4ED* 01 0007
E4F0* C9
E4F1* 32 0FF9
E4F4* F9
E4F5* 3E 07
E4F7* 0A
E4F8* 30 05
E4FA* 01 0005
E4FD* F1
E4FE* C9
E4FF* 08
E500* 30 05
E502* 01 0005
E505* F1
E506* C9
E507* 3A 5C00
E50A* E4 C7
E50C* C8 22
E50E* C8 22
E510* C8 22
E512* 02
E513* 32 5C40
E516* 32 0FF0
E519* 70
E51A* 03 F1
E51C* C8 27
E51E* C8 27
E520* C8 27
E522* C8 6F
E524* 20 02
E526* EE 07
E528* 32 5C40
E529* 05
E52C* E9
E52D* 21 5C3C
E530* 70
E531* F9
E532* C8 06
E534* 06 10
E536* E3
E537* CC 097F
E53A* E1
E53B* 3A 0FF9
E53E* 4F
E53F* 70
E540* 91
E541* 47
E542* 28 05
E544* C8 C6
E546* CD 097F
E549* F1
E54A* 32 5C3C
E54D* 01
E54E* D1
E54F* F1
E550* 01 0000
E553* C9

```

```

initscreen    ca    max_rows+1    Ivalidate screen-height
              jr    c, sh_ok      Iif screen-height ok, the jump
              ld    bc, 0007      Ielse return 07 in BC
              ret

sh_ok         ld    (screen_ht), a    Istore screen-height
              push af
              ld    a, 7              Ivalidate background-color
              cp    d
              jr    nc, bc_ok        Iif background-color ok, then jump
              ld    bc, 0005        Ielse return 05 in BC
              pop  af

bc_ok         cp    0
              jr    nc, bcc_ok       Iif border-color ok, then jump
              ld    bc, 0003        Ielse return 03 in BC
              pop  af

bcc_ok        ld    a, (ATTR_P)
              and  1100011B         Iclear current background color
              sla  d
              sla  d
              sla  d
              or   d
              ld    (ATTR_P), a      Iset new background color
              ld    (bgcolor), a     Iset new border color
              out  (BORDCR), a       Ichange the border

black_ink     bit  5, a              Iset the appropriate ink color
              jr    nz, black_ink    Iif light border, then black ink
              xor  7
              ld    (BORDCR), a
              push de
              push hl
              ld    hl, TVFLAG
              ld    a, (hl)         Isave (TVFLAG)
              push af
              pop  b, (hl)         Iclear top part of screen
              ld    b, max_rows
              push hl
              call CLS_B
              pop  hl
              ld    a, (screen_ht)  Iclear bottom part of screen
              ld    c, 0
              ld    a, b
              sub  c
              ld    b, a           Ib <- max_rows - (screen_ht)
              jr    z, no_clear
              set  0, (hl)
              call CLS_B
              pop  af
              ld    (TVFLAG), a     Irestore (TVFLAG)
              pop  hl
              pop  de
              pop  af
              ld    bc, 0           Ireturn 0 in BC
              ret

```

```

1056 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
1057 ;
1058 ; UPD_COLS
1059 ;
1060 ; Input: A = column adjustment (1 for right scroll, -1 for left scroll)
1061 ;
1062 ; Output: none.
1063 ;
1064 ; Globals Read: max_sprites
1065 ;             screen_ht
1066 ;             gflags
1067 ;
1068 ; Globals Written: max_sprites
1069 ;                 sprite data structure
1070 ;                 scratchpad
1071 ;
1072 ; Procedures Called: spr_addr
1073 ;                   overlap
1074 ;                   out_sprite
1075 ;
1076 ; Procedures Called 0: h_scroll
1077 ;
1078 ; Description: This routine updates the column position of all sprites.
1079 ; It also checks if, as a result of the horizontal scrolls,
1080 ; each sprite should be written to the screen. This is done
1081 ; by creating two dummy sprites (sprite-id is max_sprites)
1082 ; whose size and location correspond to the right and left
1083 ; columns of the screen respectively. If a given sprite
1084 ; overlaps either of these dummy sprites, it is written
1085 ; to the screen.
1086 ;
1087 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
1088 ;
1089 ;
1090 upd_cols    push    ix
1091            push    bc
1092            push    de
1093            push    hl
1094            ld     h, a           ; save column adjustment in h
1095            ld     a, (max_sprites)
1096            inc    a             ; temporarily increment max_sprites
1097            ld     de, (max_sprites), a ; to allow for the dummy sprite
1098            dec    a
1099            ld     e, a         ; e = sprite-id of the dummy sprite
1100            ld     ix, scratchpad ; initialize the dummy sprite
1101            ld     (ix+row), 0
1102            ld     (ix+flags), 1
1103            ld     (ix+width), 1
1104            push  af
1105            ld     e, (screen_ht)
1106            dec    e
1107            ld     (ix+height), a
1108            pop   af
1109            ld     (ix+col), max_cols-1
1110            dec    a             ; a = id of current sprite
1111            call  spr_addr      ; update column position
1112            ld     l, a
1113            ld     a, (ix+col)
1114            add    a, h
1115            push  hl
1116            ld     hl, gflags
1117            bit   autowrap, (hl)
1118            pop   hl
1119            jr    z, uc_cont    ; if not autowrap mode, then jump
1120            cp   max_cols
1121            jr    nc, ucl       ; if column <= max_cols then jump
1122            or   a, ucl         ; else column <- 0
1123            jr    uc_cont
1124            ld     uc_cont, -1
1125            jr    nc, uc_cont    ; if column <= -1, then jump
1126            ld     a, max_cols-1 ; else column <- max_cols-1
1127            ld     (ix+col), a   ; don't need to wrap position as this
1128            ld     de, l         ; is done by out_sprite
1129            call  overlap
1130            bit   l, b
1131            ld     a, l
1132            jr    nz, art_spr
1133            push  ix
1134            ld     ix, scratchpad
1135            ld     (ix+col), 0
1136            pop   ix
1137            call  overlap
1138            ld     l, b
1139            jr    z, next_spr    ; if no overlap, go on to the next one
1140            ld     l, a
1141            ld     de, (ix+row)
1142            ld     e, (ix+col)
1143            call  out_sprite     ; overlaps, so write sprite to screen
1144            ld     de, l
1145            and   a
1146            jr    nz, updc_loop
1147            ld     hl, max_sprites
1148            dec    (hl)         ; restore max_sprites to its original
1149            ; value
1150            pop   hl
1151            pop   de
1152            pop   bc
1153            pop   ix
1154            ret
1155
1093      ES58*  E9             1093
1094      ES59*  67             1094
1095      ES5A*  3A DFFA        1095
1096      ES5B*  3C             1096
1097      ES5C*  32 DFFA        1097
1098      ES5D*  3D             1098
1099      ES5E*  5F             1099
1100      ES5F*  DD 21 DFE3     1100
1101      ES60*  CC 36 00 00    1101
1102      ES61*  DD 36 07 01    1102
1103      ES62*  DD 36 02 01    1103
1104      ES63*  F5             1104
1105      ES64*  3A DFF9        1105
1106      ES65*  3D             1106
1107      ES66*  DC 77 03      1107
1108      ES67*  F1             1108
1109      ES68*  DD 36 01 1F    1109
1110      ES69*  3C             1110
1111      ES6A*  DD E25F*       1111
1112      ES6B*  6F             1112
1113      ES6C*  DD 7E 01      1113
1114      ES6D*  84             1114
1115      ES6E*  E5             1115
1116      ES6F*  21 DFF6        1116
1117      ES70*  C8 4E         1117
1118      ES71*  E1             1118
1119      ES72*  28 0D         1119
1120      ES73*  FE 20         1120
1121      ES74*  20 03         1121
1122      ES75*  AF             1122
1123      ES76*  18 06         1123
1124      ES77*  FE 9F         1124
1125      ES78*  20 02         1125
1126      ES79*  3E 1F         1126
1127      ES7A*  DD 77 01      1127
1128      ES7B*  55             1128
1129      ES7C*  C8 E484*       1129
1130      ES7D*  C8 48         1130
1131      ES7E*  7D             1131
1132      ES7F*  20 13         1132
1133      ES80*  DD 85         1133
1134      ES81*  DD 21 DFE3     1134
1135      ES82*  DC 36 01 00    1135
1136      ES83*  DC E1         1136
1137      ES84*  CC E484*       1137
1138      ES85*  C8 48         1138
1139      ES86*  28 08         1139
1140      ES87*  48             1140
1141      ES88*  DC 36 00      1141
1142      ESC2*  08 3E 01      1142
1143      ESC5*  CC E307*       1143
1144      ESC8*  9D             1144
1145      ESC9*  A7             1145
1146      ESCA*  20 84         1146
1147      ESCC*  21 DFFA        1147
1148      ESCF*  38             1148
1149
1150      ESDB*  E1             1150
1151      ESD1*  D1             1151
1152      ESD2*  C1             1152
1153      ESD3*  DD E1         1153
1154      ESD5*  C9             1154
1155
1156

```



```

E651" 08          1265      0x      dx, h1
E652" 34 00      1266      ld      (h1), 0          ;clear first column
E654" 01 011E    1267      ld      bc, 156*(max_cols-2)
E657" 09          1268      add     hl, bc          ;hl -> end of next scan - 1 byte
E658" 34          1269      ld      d, h
E659" 50          1270      ld      e, l
E65A" 13          1271      inc     de              ;de -> end of next scan
E65B" 30          1272      dec     a
E65C" 20 FE      1273      jr      nz, rt_inner   ;more scans to do, so jump
E65E" 09          1274      swap   a
E65F" 01 001F    1275      ld      bc, 31
E662" EC 88      1276      ld      a, (background) ;move attributes
E664" 3A 0FF8    1277      ld      (de), a        ;clear leftmost column to permanent
E667" 12          1278      ld      bc, 2*max_cols-1 ;attribute
E668" 01 003F    1279      add     hl, bc          ;hl -> second to last byte of next line
E669" 09          1280      ld      d, h
E66C" 34          1281      ld      e, l
E66D" 9C          1282      inc     de              ;de -> last byte of next line
E66E" 13          1283      out
E66F" 06          1284      pop     bc
E670" C1          1285      djnz   rt_outer      ;more lines to do, so jump
E671" 10 CD      1286      pop     hl
E673" 81          1287      ld      a, l
E674" 3E 01      1288      call   hs_cont
E676" CD E554"    1289      call   upd_cols
E679" F8          1290      or
E67A" 09          1291      swap   hl
E67B" 81          1292      pop     de
E67C" 01          1293      pop     bc
E67D" C1          1294      or
E67E" 09          1295      swap   hl
E67F" 81          1296      pop     de
E680" 01          1297      pop     af
E681" F1          1298      ld      bc, 0
E682" 01 0000    1299      ret
E685" C9          1300

```

1301
1302
1303 ;This table contains the destination address for each row for scrolling left
1304

```

E686" 4008      1305      left_table  defb 4008H
E688" 4020      1306      defb 4020H
E68A" 4040      1307      defb 4040H
E68C" 4060      1308      defb 4060H
E68E" 4080      1309      defb 4080H
E690" 40A0      1310      defb 40A0H
E692" 40C0      1311      defb 40C0H
E694" 40E0      1312      defb 40E0H
E696" 4100      1313      defb 4100H
E698" 4120      1314      defb 4120H
E69A" 4140      1315      defb 4140H
E69C" 4160      1316      defb 4160H
E69E" 4180      1317      defb 4180H
E6A0" 41A0      1318      defb 41A0H
E6A2" 41C0      1319      defb 41C0H
E6A4" 41E0      1320      defb 41E0H
E6A6" 4200      1321      defb 4200H
E6A8" 4220      1322      defb 4220H
E6AA" 4240      1323      defb 4240H
E6AC" 4260      1324      defb 4260H
E6AE" 4280      1325      defb 4280H
E6B0" 42A0      1326      defb 42A0H
E6B2" 42C0      1327      defb 42C0H
E6B4" 42E0      1328      defb 42E0H

```

1329
1330
1331 ;This table contains the destination address for each row for scrolling right
1332

```

E6B6" 401F      1333      rt_table   defb 401FH
E6B8" 403F      1334      defb 403FH
E6BA" 405F      1335      defb 405FH
E6BC" 407F      1336      defb 407FH
E6BE" 409F      1337      defb 409FH
E6C0" 40BF      1338      defb 40BFH
E6C2" 40DF      1339      defb 40DFH
E6C4" 40FF      1340      defb 40FFH
E6C6" 411F      1341      defb 411FH
E6C8" 413F      1342      defb 413FH
E6CA" 415F      1343      defb 415FH
E6CC" 417F      1344      defb 417FH
E6CE" 419F      1345      defb 419FH
E6D0" 41BF      1346      defb 41BFH
E6D2" 41DF      1347      defb 41DFH
E6D4" 41FF      1348      defb 41FFH
E6D6" 501F      1349      defb 501FH
E6D8" 503F      1350      defb 503FH
E6DA" 505F      1351      defb 505FH
E6DC" 507F      1352      defb 507FH
E6DE" 509F      1353      defb 509FH
E6E0" 50BF      1354      defb 50BFH
E6E2" 50DF      1355      defb 50DFH
E6E4" 50FF      1356      defb 50FFH

```

```

1359 ;
1360 ;
1361 ; UPG_ROW
1362 ;
1363 ; Inputs: A = row adjustment (1 for scroll down, -1 for scroll up)
1364 ;
1365 ; Outputs: none.
1366 ;
1367 ; Globals Read: max_sprites
1368 ; screen_ht
1369 ; gflags
1370 ;
1371 ; Globals Written: max_sprites
1372 ; sprite data structure
1373 ; scratchpad
1374 ;
1375 ; Procedures Called: spr_addr
1376 ; overlap
1377 ; out_sprite
1378 ;
1379 ; Procedures Called By: v_scroll
1380 ;
1381 ; Description: This routine updates the row position of all sprites.
1382 ; It also checks if, as a result of the vertical scroll,
1383 ; each sprite should be written to the screen. This is done
1384 ; by creating two dummy sprites (sprite-id is max_sprites)
1385 ; whose size and location correspond to the top and bottom
1386 ; rows of the screen respectively. If a given sprite
1387 ; overlaps either of these dummy sprites, it is written
1388 ; to the screen.
1389 ;
1390 ;
1391 ;
1392 ;
1393 ;
1394 ;
1395 ;
1396 ;
1397 ;
1398 ;
1399 ;
1400 ;
1401 ;
1402 ;
1403 ;
1404 ;
1405 ;
1406 ;
1407 ;
1408 ;
1409 ;
1410 ;
1411 ;
1412 ;
1413 ;
1414 ;
1415 ;
1416 ;
1417 ;
1418 ;
1419 ;
1420 ;
1421 ;
1422 ;
1423 ;
1424 ;
1425 ;
1426 ;
1427 ;
1428 ;
1429 ;
1430 ;
1431 ;
1432 ;
1433 ;
1434 ;
1435 ;
1436 ;
1437 ;
1438 ;
1439 ;
1440 ;
1441 ;
1442 ;
1443 ;
1444 ;
1445 ;
1446 ;
1447 ;
1448 ;
1449 ;
1450 ;
1451 ;
1452 ;
1453 ;
1454 ;
1455 ;
1456 ;
1457 ;
1458 ;
1459 ;
1460 ;
1461 ;
1462 ;
1463 ;
1464 ;
1465 ;

```

```

E6E6 00 05
E6E8 C9
E6E9 03
E6EA 05
E6EB 07
E6EC 3A 0FFA
E6ED 3C
E6EE 32 00FA
E6EF 3C
E6F0 3F
E6F1 30 21 0FE3
E6F2 30 36 01 00
E6FD CC 36 07 01
E701 0C 36 02 20
E705 00 36 03 01
E709 35
E70A 3A 0FF9
E70D 3D
E70E DD 77 00
E711 31
E712 3C
E713 CD E25F
E716 0F
E717 DD 7E 00
E71A 04
E71B 05
E71C 21 0FF8
E71F C8 4E
E721 01
E722 28 16
E724 05
E725 21 0FF9
E728 0E
E729 01
E72A 20 03
E72C 0F
E72D 18 08
E72F 0E 0F
E731 20 07
E733 05
E734 21 0FF9
E737 7E
E738 30
E739 01
E73A DD 77 00
E73D 35
E73E CC 0404
E741 C8 4E
E743 7D
E744 20 13
E746 00 05
E748 DD 21 0FE3
E74C DD 36 00 00
E750 00 01
E752 CD E404
E755 C8 4E
E757 28 08
E759 08
E75A DD 36 00
E75D DD 3E 01
E760 CD E307
E763 5D
E764 A7
E765 20 08
E767 21 0FFA
E76A 35
E76B 01
E76C 01
E76D C1
E76E DD 01
E770 C9

```

```

vnd_rows      push    ix
               push    bx
               push    dx
               push    hl
               ld      h, a          ; save row adjustment in h
               ld      b, (max_sprites)
               inc     a             ; temporarily increment max_sprites
               ld      (max_sprites), a ; to allow for the dummy sprite
               dec     0
               ld      e, b          ; e = sprite-id of the dummy sprite
               ld      ix, scratchpad ; initialize the dummy sprite
               ld      (ix+col), 0
               ld      (ix+flags), 1
               ld      (ix+width), max_cols
               ld      (ix+height), 1
               push    af
               ld      b, (screen_ht)
               dec     0
               ld      (ix+row), a
               pop     af
vndr_loop      dec     a             ; a = id of current sprite
               call    spr_addr      ; update column position
               ld      l, a
               ld      a, (ix+row)
               rld
               push   hl
               ld      hl, gflags
               bit    autoreap, (hl)
               pop    hl
               jr     z, ur_cnt      ; if not autoreap mode, then jump
               push   hl
               ld      hl, screen_ht
               cp     (hl)
               pop    hl
               jr     nz, ur1        ; if row < screen_ht then jump
               or     a, 0           ; else row <= 0
               jr     z, ur1
               cp     -1
               jr     nz, ur_cnt     ; if row < -1, then jump
               push   hl
               ld      hl, screen_ht
               ld      a, (hl)
               ld     row, (hl)     ; else row <= screen_ht-1
               dec     a
               pop    hl
ur_cnt         ld      (ix+row), a
               ld      d, 1
               call   overlap
               bit    l, b
               ld      e, l
               jr     nz, ut_spr
               push   ix
               ld      ix, scratchpad
               ld      (ix+row), 0
               pop    ix
               call   overlap
               bit    l, b
               jr     z, ut_spr      ; if no overlap, go on to the next one
               ld      l, 0
               ld      d, (ix+row)
               ld      e, (ix+col)
               call   out_sprite     ; overlaps, so write sprite to screen
               ld      a, l
               and    a
               jr     nz, vndr_loop
               ld      hl, max_sprites
               dec     (hl)           ; restore max_sprites to its original
                                   ; value
               pop    hl
               pop    dx
               pop    bx
               pop    ix
               ret

```

```

1466
1467
1468
1469 | V_SCROLL
1470 |
1471 | Input: A < 0 - left scroll
1472 |       A >= 0 - right scroll
1473 |
1474 | Output: BC = 0000 - OK
1475 |
1476 | Global Read: screen_ht
1477 |            bgcolor
1478 |
1479 | Global Written: ATTR_T
1480 |
1481 | Procedures Called: SET_PRINT_POS
1482 |                  WRITE_CHAR
1483 |
1484 | Procedures Called By: I_P_HANDLER
1485 |                    user program
1486 |
1487 | Description: This routine causes the contents of the top (screen_ht)
1488 |             lines of the screen to be scrolled left or right one
1489 |             column, depending on the value of A. All sprites have
1490 |             their column position updated by 1 or -1, depending on
1491 |             the direction of scroll.
1492 |
1493
1494
1495
1496 v_scroll      push    a7
1497             push    de
1498             push    hl
1499             orl     a, 0
1500             push    bc
1501             push    de
1502             push    hl
1503             orl     a, 0
1504             orl     a, 7, a
1505             bit    7, a
1506             jr     nz, down_scroll    ; if A >= 0, then jump
1507             orl     a, 0             ; use alt. regs. for attr pointers
1508             ld     hl, $0000
1509             ld     de, $0000
1510             orl     a, 0
1511             ld     a, (screen_ht)
1512             dec    a
1513             ld     b, a             ; b = line counter
1514             ld     hl, vs_table
1515             push  hl
1516             mov   up_outer, hl
1517             ld     e, (hl)
1518             inc   hl
1519             ld     d, (hl)         ; de -> dest for next line
1520             inc   hl
1521             push  de             ; put dest pointer on stack
1522             ld     e, (hl)
1523             inc   hl
1524             ld     d, (hl)         ; de -> source for next line
1525             dec   hl
1526             orl     a, hl         ; exchange dest pointer and tabl;
1527             orl     a, hl         ; pointer
1528             ld     de, hl         ; put pointers in proper regs
1529             orl     a, 0
1530             push  bc
1531             ld     up_inner, bc; max_cols
1532             ld     dir, bc; max_cols
1533             ld     bc, 256-max_cols
1534             ld     hl, bc         ; hl -> start of next scan
1535             orl     a, 0
1536             orl     a, hl
1537             ld     de, hl         ; de -> start of next scan dest
1538             dec   a
1539             jr     nz, up_inner    ; more scans to do, so jump
1540             orl     a, 0
1541             ld     bc, max_cols
1542             ld     dir, bc
1543             ld     orl     a, 0
1544             orl     a, bc
1545             djnz  up_outer        ; if more lines to do, then jump
1546             pop   hl
1547             ld     a, (screen_ht) ; clear the bottom line
1548             dec   a
1549             ld     b, a
1550             ld     c, 0
1551             call  SET_PRINT_POS
1552             ld     b, max_cols
1553             ld     a, (bgcolor) ; set background color to permanent
1554             ld     ; value
1555             ld     (ATTR_T), a
1556             push  bc
1557             ld     a, " "
1558             call  WRITE_CHAR
1559             pop   bc
1560             djnz  cl_bot_line
1561             ld     a, -1
1562             jr     vs_cont
1563             ld     de, (screen_ht) ; set up table pointer
1564             dec   a
1565             push  a7
1566             orl     a, 0
1567             ld     e, a
1568             ld     b, 0
1569             ld     hl, vs_table
1570             orl     a, hl         ; hl -> entry for last line
1571             orl     a, bc
1572             push  hl
1573             push  hl
1574             orl     a, 0
1575             orl     a, 7, a
1576             bit    7, a
1577             jr     nz, down_scroll
1578             orl     a, 0
1579             ld     hl, $0000
1580             ld     de, $0000
1581             orl     a, 0
1582             ld     a, (screen_ht)
1583             dec   a
1584             ld     b, a
1585             ld     hl, vs_table
1586             push  hl
1587             mov   up_outer, hl
1588             ld     e, (hl)
1589             inc   hl
1590             ld     d, (hl)
1591             inc   hl
1592             push  de
1593             ld     e, (hl)
1594             inc   hl
1595             ld     d, (hl)
1596             dec   hl
1597             orl     a, hl
1598             orl     a, hl
1599             ld     de, hl
1600             orl     a, 0
1601             push  bc
1602             ld     up_inner, bc; max_cols
1603             ld     dir, bc; max_cols
1604             ld     bc, 256-max_cols
1605             ld     hl, bc
1606             orl     a, 0
1607             orl     a, hl
1608             ld     de, hl
1609             dec   a
1610             jr     nz, up_inner
1611             orl     a, 0
1612             ld     bc, max_cols
1613             ld     dir, bc
1614             ld     orl     a, 0
1615             orl     a, bc
1616             djnz  up_outer
1617             pop   hl
1618             ld     a, (screen_ht)
1619             dec   a
1620             ld     b, a
1621             ld     c, 0
1622             call  SET_PRINT_POS
1623             ld     b, max_cols
1624             ld     a, (bgcolor)
1625             ld     ; value
1626             ld     (ATTR_T), a
1627             push  bc
1628             ld     a, " "
1629             call  WRITE_CHAR
1630             pop   bc
1631             djnz  cl_bot_line
1632             ld     a, -1
1633             jr     vs_cont
1634             ld     de, (screen_ht)
1635             dec   a
1636             push  a7
1637             orl     a, 0
1638             ld     e, a
1639             ld     b, 0
1640             ld     hl, vs_table
1641             orl     a, hl
1642             orl     a, bc
1643             push  hl
1644             push  hl
1645             orl     a, 0
1646             orl     a, 7, a
1647             bit    7, a
1648             jr     nz, down_scroll
1649             orl     a, 0
1650             ld     hl, $0000
1651             ld     de, $0000
1652             orl     a, 0
1653             ld     a, (screen_ht)
1654             dec   a
1655             ld     b, a
1656             ld     hl, vs_table
1657             push  hl
1658             mov   up_outer, hl
1659             ld     e, (hl)
1660             inc   hl
1661             ld     d, (hl)
1662             inc   hl
1663             push  de
1664             ld     e, (hl)
1665             inc   hl
1666             ld     d, (hl)
1667             dec   hl
1668             orl     a, hl
1669             orl     a, hl
1670             ld     de, hl
1671             orl     a, 0
1672             push  bc
1673             ld     up_inner, bc; max_cols
1674             ld     dir, bc; max_cols
1675             ld     bc, 256-max_cols
1676             ld     hl, bc
1677             orl     a, 0
1678             orl     a, hl
1679             ld     de, hl
1680             dec   a
1681             jr     nz, up_inner
1682             orl     a, 0
1683             ld     bc, max_cols
1684             ld     dir, bc
1685             ld     orl     a, 0
1686             orl     a, bc
1687             djnz  up_outer
1688             pop   hl
1689             ld     a, (screen_ht)
1690             dec   a
1691             ld     b, a
1692             ld     c, 0
1693             call  SET_PRINT_POS
1694             ld     b, max_cols
1695             ld     a, (bgcolor)
1696             ld     ; value
1697             ld     (ATTR_T), a
1698             push  bc
1699             ld     a, " "
1700             call  WRITE_CHAR
1701             pop   bc
1702             djnz  cl_bot_line
1703             ld     a, -1
1704             jr     vs_cont
1705             ld     de, (screen_ht)
1706             dec   a
1707             push  a7
1708             orl     a, 0
1709             ld     e, a
1710             ld     b, 0
1711             ld     hl, vs_table
1712             orl     a, hl
1713             orl     a, bc
1714             push  hl
1715             push  hl
1716             orl     a, 0
1717             orl     a, 7, a
1718             bit    7, a
1719             jr     nz, down_scroll
1720             orl     a, 0
1721             ld     hl, $0000
1722             ld     de, $0000
1723             orl     a, 0
1724             ld     a, (screen_ht)
1725             dec   a
1726             ld     b, a
1727             ld     hl, vs_table
1728             push  hl
1729             mov   up_outer, hl
1730             ld     e, (hl)
1731             inc   hl
1732             ld     d, (hl)
1733             inc   hl
1734             push  de
1735             ld     e, (hl)
1736             inc   hl
1737             ld     d, (hl)
1738             dec   hl
1739             orl     a, hl
1740             orl     a, hl
1741             ld     de, hl
1742             orl     a, 0
1743             push  bc
1744             ld     up_inner, bc; max_cols
1745             ld     dir, bc; max_cols
1746             ld     bc, 256-max_cols
1747             ld     hl, bc
1748             orl     a, 0
1749             orl     a, hl
1750             ld     de, hl
1751             dec   a
1752             jr     nz, up_inner
1753             orl     a, 0
1754             ld     bc, max_cols
1755             ld     dir, bc
1756             ld     orl     a, 0
1757             orl     a, bc
1758             djnz  up_outer
1759             pop   hl
1760             ld     a, (screen_ht)
1761             dec   a
1762             ld     b, a
1763             ld     c, 0
1764             call  SET_PRINT_POS
1765             ld     b, max_cols
1766             ld     a, (bgcolor)
1767             ld     ; value
1768             ld     (ATTR_T), a
1769             push  bc
1770             ld     a, " "
1771             call  WRITE_CHAR
1772             pop   bc
1773             djnz  cl_bot_line
1774             ld     a, -1
1775             jr     vs_cont
1776             ld     de, (screen_ht)
1777             dec   a
1778             push  a7
1779             orl     a, 0
1780             ld     e, a
1781             ld     b, 0
1782             ld     hl, vs_table
1783             orl     a, hl
1784             orl     a, bc
1785             push  hl
1786             push  hl
1787             orl     a, 0
1788             orl     a, 7, a
1789             bit    7, a
1790             jr     nz, down_scroll
1791             orl     a, 0
1792             ld     hl, $0000
1793             ld     de, $0000
1794             orl     a, 0
1795             ld     a, (screen_ht)
1796             dec   a
1797             ld     b, a
1798             ld     hl, vs_table
1799             push  hl
1800             mov   up_outer, hl
1801             ld     e, (hl)
1802             inc   hl
1803             ld     d, (hl)
1804             inc   hl
1805             push  de
1806             ld     e, (hl)
1807             inc   hl
1808             ld     d, (hl)
1809             dec   hl
1810             orl     a, hl
1811             orl     a, hl
1812             ld     de, hl
1813             orl     a, 0
1814             push  bc
1815             ld     up_inner, bc; max_cols
1816             ld     dir, bc; max_cols
1817             ld     bc, 256-max_cols
1818             ld     hl, bc
1819             orl     a, 0
1820             orl     a, hl
1821             ld     de, hl
1822             dec   a
1823             jr     nz, up_inner
1824             orl     a, 0
1825             ld     bc, max_cols
1826             ld     dir, bc
1827             ld     orl     a, 0
1828             orl     a, bc
1829             djnz  up_outer
1830             pop   hl
1831             ld     a, (screen_ht)
1832             dec   a
1833             ld     b, a
1834             ld     c, 0
1835             call  SET_PRINT_POS
1836             ld     b, max_cols
1837             ld     a, (bgcolor)
1838             ld     ; value
1839             ld     (ATTR_T), a
1840             push  bc
1841             ld     a, " "
1842             call  WRITE_CHAR
1843             pop   bc
1844             djnz  cl_bot_line
1845             ld     a, -1
1846             jr     vs_cont
1847             ld     de, (screen_ht)
1848             dec   a
1849             push  a7
1850             orl     a, 0
1851             ld     e, a
1852             ld     b, 0
1853             ld     hl, vs_table
1854             orl     a, hl
1855             orl     a, bc
1856             push  hl
1857             push  hl
1858             orl     a, 0
1859             orl     a, 7, a
1860             bit    7, a
1861             jr     nz, down_scroll
1862             orl     a, 0
1863             ld     hl, $0000
1864             ld     de, $0000
1865             orl     a, 0
1866             ld     a, (screen_ht)
1867             dec   a
1868             ld     b, a
1869             ld     hl, vs_table
1870             push  hl
1871             mov   up_outer, hl
1872             ld     e, (hl)
1873             inc   hl
1874             ld     d, (hl)
1875             inc   hl
1876             push  de
1877             ld     e, (hl)
1878             inc   hl
1879             ld     d, (hl)
1880             dec   hl
1881             orl     a, hl
1882             orl     a, hl
1883             ld     de, hl
1884             orl     a, 0
1885             push  bc
1886             ld     up_inner, bc; max_cols
1887             ld     dir, bc; max_cols
1888             ld     bc, 256-max_cols
1889             ld     hl, bc
1890             orl     a, 0
1891             orl     a, hl
1892             ld     de, hl
1893             dec   a
1894             jr     nz, up_inner
1895             orl     a, 0
1896             ld     bc, max_cols
1897             ld     dir, bc
1898             ld     orl     a, 0
1899             orl     a, bc
1900             djnz  up_outer
1901             pop   hl
1902             ld     a, (screen_ht)
1903             dec   a
1904             ld     b, a
1905             ld     c, 0
1906             call  SET_PRINT_POS
1907             ld     b, max_cols
1908             ld     a, (bgcolor)
1909             ld     ; value
1910             ld     (ATTR_T), a
1911             push  bc
1912             ld     a, " "
1913             call  WRITE_CHAR
1914             pop   bc
1915             djnz  cl_bot_line
1916             ld     a, -1
1917             jr     vs_cont
1918             ld     de, (screen_ht)
1919             dec   a
1920             push  a7
1921             orl     a, 0
1922             ld     e, a
1923             ld     b, 0
1924             ld     hl, vs_table
1925             orl     a, hl
1926             orl     a, bc
1927             push  hl
1928             push  hl
1929             orl     a, 0
1930             orl     a, 7, a
1931             bit    7, a
1932             jr     nz, down_scroll
1933             orl     a, 0
1934             ld     hl, $0000
1935             ld     de, $0000
1936             orl     a, 0
1937             ld     a, (screen_ht)
1938             dec   a
1939             ld     b, a
1940             ld     hl, vs_table
1941             push  hl
1942             mov   up_outer, hl
1943             ld     e, (hl)
1944             inc   hl
1945             ld     d, (hl)
1946             inc   hl
1947             push  de
1948             ld     e, (hl)
1949             inc   hl
1950             ld     d, (hl)
1951             dec   hl
1952             orl     a, hl
1953             orl     a, hl
1954             ld     de, hl
1955             orl     a, 0
1956             push  bc
1957             ld     up_inner, bc; max_cols
1958             ld     dir, bc; max_cols
1959             ld     bc, 256-max_cols
1960             ld     hl, bc
1961             orl     a, 0
1962             orl     a, hl
1963             ld     de, hl
1964             dec   a
1965             jr     nz, up_inner
1966             orl     a, 0
1967             ld     bc, max_cols
1968             ld     dir, bc
1969             ld     orl     a, 0
1970             orl     a, bc
1971             djnz  up_outer
1972             pop   hl
1973             ld     a, (screen_ht)
1974             dec   a
1975             ld     b, a
1976             ld     c, 0
1977             call  SET_PRINT_POS
1978             ld     b, max_cols
1979             ld     a, (bgcolor)
1980             ld     ; value
1981             ld     (ATTR_T), a
1982             push  bc
1983             ld     a, " "
1984             call  WRITE_CHAR
1985             pop   bc
1986             djnz  cl_bot_line
1987             ld     a, -1
1988             jr     vs_cont
1989             ld     de, (screen_ht)
1990             dec   a
1991             push  a7
1992             orl     a, 0
1993             ld     e, a
1994             ld     b, 0
1995             ld     hl, vs_table
1996             orl     a, hl
1997             orl     a, bc
1998             push  hl
1999             push  hl
2000             orl     a, 0
2001             orl     a, 7, a
2002             bit    7, a
2003             jr     nz, down_scroll
2004             orl     a, 0
2005             ld     hl, $0000
2006             ld     de, $0000
2007             orl     a, 0
2008             ld     a, (screen_ht)
2009             dec   a
2010             ld     b, a
2011             ld     hl, vs_table
2012             push  hl
2013             mov   up_outer, hl
2014             ld     e, (hl)
2015             inc   hl
2016             ld     d, (hl)
2017             inc   hl
2018             push  de
2019             ld     e, (hl)
2020             inc   hl
2021             ld     d, (hl)
2022             dec   hl
2023             orl     a, hl
2024             orl     a, hl
2025             ld     de, hl
2026             orl     a, 0
2027             push  bc
2028             ld     up_inner, bc; max_cols
2029             ld     dir, bc; max_cols
2030             ld     bc, 256-max_cols
2031             ld     hl, bc
2032             orl     a, 0
2033             orl     a, hl
2034             ld     de, hl
2035             dec   a
2036             jr     nz, up_inner
2037             orl     a, 0
2038             ld     bc, max_cols
2039             ld     dir, bc
2040             ld     orl     a, 0
2041             orl     a, bc
2042             djnz  up_outer
2043             pop   hl
2044             ld     a, (screen_ht)
2045             dec   a
2046             ld     b, a
2047             ld     c, 0
2048             call  SET_PRINT_POS
2049             ld     b, max_cols
2050             ld     a, (bgcolor)
2051             ld     ; value
2052             ld     (ATTR_T), a
2053             push  bc
2054             ld     a, " "
2055             call  WRITE_CHAR
2056             pop   bc
2057             djnz  cl_bot_line
2058             ld     a, -1
2059             jr     vs_cont
2060             ld     de, (screen_ht)
2061             dec   a
2062             push  a7
2063             orl     a, 0
2064             ld     e, a
2065             ld     b, 0
2066             ld     hl, vs_table
2067             orl     a, hl
2068             orl     a, bc
2069             push  hl
2070             push  hl
2071             orl     a, 0
2072             orl     a, 7, a
2073             bit    7, a
2074             jr     nz, down_scroll
2075             orl     a, 0
2076             ld     hl, $0000
2077             ld     de, $0000
2078             orl     a, 0
2079             ld     a, (screen_ht)
2080             dec   a
2081             ld     b, a
2082             ld     hl, vs_table
2083             push  hl
2084             mov   up_outer, hl
2085             ld     e, (hl)
2086             inc   hl
2087             ld     d, (hl)
2088             inc   hl
2089             push  de
2090             ld     e, (hl)
2091             inc   hl
2092             ld     d, (hl)
2093             dec   hl
2094             orl     a, hl
2095             orl     a, hl
2096             ld     de, hl
2097             orl     a, 0
2098             push  bc
2099             ld     up_inner, bc; max_cols
2100             ld     dir, bc; max_cols
2101             ld     bc, 256-max_cols
2102             ld     hl, bc
2103             orl     a, 0
2104             orl     a, hl
2105             ld     de, hl
2106             dec   a
2107             jr     nz, up_inner
2108             orl     a, 0
2109             ld     bc, max_cols
2110             ld     dir, bc
2111             ld     orl     a, 0
2112             orl     a, bc
2113             djnz  up_outer
2114             pop   hl
2115             ld     a, (screen_ht)
2116             dec   a
2117             ld     b, a
2118             ld     c, 0
2119             call  SET_PRINT_POS
2120             ld     b, max_cols
2121             ld     a, (bgcolor)
2122             ld     ; value
2123             ld     (ATTR_T), a
2124             push  bc
2125             ld     a, " "
2126             call  WRITE_CHAR
2127             pop   bc
2128             djnz  cl_bot_line
2129             ld     a, -1
2130             jr     vs_cont
2131             ld     de, (screen_ht)
2132             dec   a
2133             push  a7
2134             orl     a, 0
2135             ld     e, a
2136             ld     b, 0
2137             ld     hl, vs_table
2138             orl     a, hl
2139             orl     a, bc
2140             push  hl
2141             push  hl
2142             orl     a, 0
2143             orl     a, 7, a
2144             bit    7, a
2145             jr     nz, down_scroll
2146             orl     a, 0
2147             ld     hl, $0000
2148             ld     de, $0000
2149             orl     a, 0
2150             ld     a, (screen_ht)
2151             dec   a
2152             ld     b, a
2153             ld     hl, vs_table
2154             push  hl
2155             mov   up_outer, hl
2156             ld     e, (hl)
2157             inc   hl
2158             ld     d, (hl)
2159             inc   hl
2160             push  de
2161             ld     e, (hl)
2162             inc   hl
2163             ld     d, (hl)
2164             dec   hl
2165             orl     a, hl
2166             orl     a, hl
2167             ld     de, hl
2168             orl     a, 0
2169             push  bc
2170             ld     up_inner, bc; max_cols
2171             ld     dir, bc; max_cols
2172             ld     bc, 256-max_cols
2173             ld     hl, bc
2174             orl     a, 0
2175             orl     a, hl
2176             ld     de, hl
2177             dec   a
2178             jr     nz, up_inner
2179             orl     a, 0
2180             ld     bc, max_cols
2181             ld     dir, bc
2182             ld     orl     a, 0
2183             orl     a, bc
2184             djnz  up_outer
2185             pop   hl
2186             ld     a, (screen_ht)
2187             dec   a
2188             ld     b, a
2189             ld     c, 0
2190             call  SET_PRINT_POS
2191             ld     b, max_cols
2192             ld     a, (bgcolor)
2193             ld     ; value
2194             ld     (ATTR_T), a
2195             push  bc
2196             ld     a, " "
2197             call  WRITE_CHAR
2198             pop   bc
2199             djnz  cl_bot_line
2200             ld     a, -1
2201             jr     vs_cont
2202             ld     de, (screen_ht)
2203             dec   a
2204             push  a7
2205             orl     a, 0
2206             ld     e, a
2207             ld     b, 0
2208             ld     hl, vs_table
2209             orl     a, hl
2210             orl     a, bc
2211             push  hl
2212             push  hl
2213             orl     a, 0
2214             orl     a, 7, a
2215             bit    7, a
2216             jr     nz, down_scroll
2217             orl     a, 0
2218             ld     hl, $0000
2219             ld     de, $0000
2220             orl     a, 0
2221             ld     a, (screen_ht)
2222             dec   a
2223             ld     b, a
2224             ld     hl, vs_table
2225             push  hl
2226             mov   up_outer, hl
2227             ld     e, (hl)
2228             inc   hl
2229             ld     d, (hl)
2230             inc   hl
2231             push  de
2232             ld     e, (hl)
2233             inc   hl
2234             ld     d, (hl)
2235             dec   hl
2236             orl     a, hl
2237             orl     a, hl
2238             ld     de, hl
2239             orl     a, 0
2240             push  bc
2241             ld     up_inner, bc; max_cols
2242             ld     dir, bc; max_cols
2243             ld     bc, 256-max_cols
2244             ld     hl, bc
2245             orl     a, 0
2246             orl     a, hl
2247             ld     de, hl
2248             dec   a
2249             jr     nz, up_inner
2250             orl     a, 0
2251             ld     bc, max_cols
2252             ld     dir, bc
2253             ld     orl     a, 0
2254             orl     a, bc
2255             djnz  up_outer
2256             pop   hl
2257             ld     a, (screen_ht)
2258             dec   a
2259             ld     b, a
2260             ld     c, 0
2261             call  SET_PRINT_POS
2262             ld     b, max_cols
2263             ld     a, (bgcolor)
2264             ld     ; value
2265             ld     (ATTR_T), a
2266             push  bc
2267             ld     a, " "
2268             call  WRITE_CHAR
2269             pop   bc
2270             djnz  cl_bot_line
2271             ld     a, -1
2272             jr     vs_cont
2273             ld     de, (screen_ht)
2274             dec   a
2275             push  a7
2276             orl     a, 0
2277             ld     e, a
2278             ld     b, 0
2279             ld     hl, vs_table
2280             orl     a, hl
2281             orl     a, bc
2282             push  hl
2283             push  hl
2284             orl     a, 0
2285             orl     a, 7, a
2286             bit    7, a
2287             jr     nz, down_scroll
2288             orl     a, 0
2289             ld     hl, $0000
2290             ld     de, $0000
2291             orl     a, 0
2292             ld     a, (screen_ht)
2293             dec   a
2294             ld     b, a
2295             ld     hl, vs_table
2296             push  hl
2297             mov   up_outer, hl
2298             ld     e, (hl)
2299             inc   hl
2300             ld     d, (hl)
2301             inc   hl
2302             push  de
2303             ld     e, (hl)
2304             inc   hl
2305             ld     d, (hl)
2306             dec   hl
2307             orl     a, hl
2308             orl     a, hl
2309             ld     de, hl
2310             orl     a, 0
2311             push  bc
2312             ld     up_inner, bc; max_cols
2313             ld     dir, bc; max_cols
2314             ld     bc, 256-max_cols
2315             ld     hl, bc
2316             orl     a, 0
2317             orl     a, hl
2318             ld     de, hl
2319             dec   a
2320             jr     nz, up_inner
2321             orl     a, 0
2322             ld     bc, max_cols
2323             ld     dir, bc
2324             ld     orl     a, 0
2325             orl     a, bc
2326             djnz  up_outer
2327             pop   hl
2328             ld     a, (screen_ht)
2329             dec   a
2330             ld     b, a
2331             ld     c, 0
2332             call  SET_PRINT_POS
2333             ld     b, max_cols
2334             ld     a, (bgcolor)
2335             ld     ; value
2336             ld     (ATTR_T), a
2337             push  bc
2338             ld     a, " "
2339             call  WRITE_CHAR
2340             pop   bc
2341             djnz  cl_bot_line
2342             ld     a, -1
2343             jr     vs_cont
2344             ld     de, (screen_ht)
2345             dec   a
2346             push  a7
2347             orl     a, 0
2348             ld     e, a
2349             ld     b, 0
2350             ld     hl, vs_table
2351             orl     a, hl
2352             orl     a, bc
2353             push  hl
2354             push  hl
2355             orl     a, 0
2356             orl     a, 7, a
2357             bit    7, a
2358             jr     nz, down_scroll
2359             orl     a, 0
2360             ld     hl, $0000
2361             ld     de, $0000
2362             orl     a, 0
2363             ld     a, (screen_ht)
2364             dec   a
2365             ld     b, a
2366             ld     hl, vs_table
2367             push  hl
2368             mov   up_outer, hl
2369             ld     e, (hl)
2370             inc   hl
2371             ld     d, (hl)
2372             inc   hl
2373             push  de
2374             ld     e, (hl)
2375             inc   hl
2376             ld     d, (hl)
2377             dec   hl
2378             orl     a, hl
2379             orl     a, hl
2380             ld     de, hl
2381             orl     a, 0
2382             push  bc
2383             ld     up_inner, bc; max_cols
2384             ld     dir, bc; max_cols
2385             ld     bc, 256-max_cols
2386             ld     hl, bc
2387             orl     a, 0
2388             orl     a, hl
2389             ld     de, hl
2390             dec   a
2391             jr     nz, up_inner
2392             orl     a, 0
2393             ld     bc, max_cols
2394             ld     dir, bc
2395             ld     orl     a, 0
2396             orl     a, bc
2397             djnz  up_outer
2398             pop   hl
2399             ld     a, (screen_ht)
2400             dec   a
2401             ld     b, a
2402             ld     c, 0
2403             call  SET_PRINT_POS
2404             ld     b, max_cols
2405             ld     a, (bgcolor)
2406             ld     ; value
2407             ld     (ATTR_T), a
2408             push  bc
2409             ld     a, " "
2410             call  WRITE_CHAR
2411             pop   bc
2412             djnz  cl_bot_line
2413             ld     a, -1
2414             jr     vs_cont
2415             ld     de, (screen_ht)
2416             dec   a
2417             push  a7
2418             orl     a, 0
2419             ld     e, a
2420             ld     b, 0
2421             ld     hl, vs_table
2422             orl     a, hl
2423             orl     a, bc
2424             push  hl
2425             push  hl
2426             orl     a, 0
2427             orl     a, 7, a
2428             bit    7, a
2429             jr     nz, down_scroll
2430             orl     a, 0
2431             ld     hl, $0000
2432             ld     de, $0000
2433             orl     a, 0
2434             ld     a, (screen_ht)
2435             dec   a
2436             ld     b, a
2437             ld     hl, vs_table
2438             push  hl
2439             mov   up_outer, hl
2440             ld     e, (hl)
2441             inc   hl
2442             ld     d, (hl)
2443             inc   hl
2444             push  de
2445             ld     e, (hl)
2446             inc   hl
2447             ld     d, (hl)
2448             dec   hl
2449             orl     a, hl
2450             orl     a, hl
2451             ld     de, hl
2452             orl     a, 0
2453             push  bc
2454             ld     up_inner, bc; max_cols
2455             ld     dir, bc; max_cols
2456             ld     bc, 256-max_cols
2457             ld     hl, bc
2458             orl     a, 0
2459             orl     a, hl
2460             ld     de, hl
2461             dec   a
2462             jr     nz, up_inner
2463             orl     a, 0
2464             ld     bc, max_cols
2465             ld     dir, bc
2466             ld     orl     a, 0
2467             orl     a, bc
2468             djnz  up_outer
2469             pop   hl
2470             ld     a, (screen_ht)
2471             dec   a
2472             ld     b, a
2473             ld     c, 0
2474             call  SET_PRINT_POS
2475             ld     b, max_cols
2476             ld     a, (bgcolor)
2477             ld     ; value
2478             ld     (ATTR_T), a
2479             push  bc
2480             ld     a, " "
2481             call  WRITE_CHAR
2482             pop   bc
2483             djnz  cl_bot_line
2484             ld     a, -1
2485             jr     vs_cont
2486             ld     de, (screen_ht)
2487             dec   a
2488             push  a7
2489             orl     a, 0
2490             ld     e, a
2491             ld     b, 0
2492             ld     hl, vs_table
2493             orl     a, hl
2494             orl     a, bc
2495             push  hl
2496             push  hl
2497             orl     a, 0
2498             orl     a, 7, a
2499             bit    7, a
2500             jr     nz, down_scroll
2501             orl     a, 0
2502             ld     hl, $0000
2503             ld     de, $0000
2504             orl     a, 0
2505             ld     a, (screen_ht)
2506             dec   a
2507             ld     b, a
2508             ld     hl, vs_table
2509             push  hl
2510             mov   up_outer, hl
2511             ld     e, (hl)
2512             inc   hl
2513             ld     d, (hl)
2514             inc   hl
2515             push  de
2516             ld     e, (hl)
2517             inc   hl
2518             ld     d,
```

```

E788* E1          1574      pop     hl
E789* 5E          1575      ld      hl, (hl)
E78A* 5E          1576      inc    hl
E78B* 5E          1577      ld      hl, (hl)
E78C* 80          1578      ex     hl, hl      ;hl -> 1st scan of last line
E78D* 7C          1579      ld      hl, h      ;calc. address of attr for last line
E78E* 0F          1580      rrca   ; as done in ATTRVT in Home ACM
E78F* 0F          1581      rrca
E790* 0F          1582      rrca
E791* 16 03      1583      end
E792* F4 38      1584      or     00000118
E793* 47          1585      ld      hl, a
E794* 36          1586      ld      hl, d, h      ;de -> 1st attr byte of last line
E795* 3C          1587      ld      hl, l
E796* 01 0020    1588      ld      hl, bc, max_cols
E797* A7          1589      end
E798* EC 42      1590      sbc   hl, bc      ;hl -> 1st attr byte of next to last
E799* 09          1591      or     l line
E79A* E1          1592      pop     hl
E79B* F1          1593      pop     hl
E79C* 23          1594      inc    hl
E79D* E5          1595      push  hl
E79E* 47          1596      ld      hl, a      ;l = line counter
E79F* E1          1597      pop     hl
E800* 36          1598      ld      hl, (hl)
E801* 20          1599      dec    hl
E802* 20          1600      ld      hl, (hl)      ;de -> dest for next line
E803* 05          1601      dec    hl
E804* 36          1602      push  hl      ;put dest pointer on stack
E805* 20          1603      ld      hl, (hl)
E806* 20          1604      dec    hl
E807* 5E          1605      ld      hl, (hl)      ;de -> source for next line
E808* 23          1606      inc    hl
E809* E3          1607      ex     hl, hl      ;exchange dest pointer and table
                        ; pointer
E80A* EB          1608      ex     hl, hl      ;put pointers in proper regs
E80B* 3E 08      1609      ld      hl, b      ;l = scan counter
E80C* C5          1610      push  hl
E80D* 01 0020    1611      ld      hl, bc, max_cols
E80E* ED 80      1612      ld      hl, bc, max_cols      ;move scan down
E80F* 01 0CE0    1613      add    hl, bc      ;hl -> start of next scan
E810* 09          1614      ex     hl, hl
E811* EB          1615      add    hl, bc      ;de -> start of next scan dest
E812* 09          1616      ex     hl, hl
E813* 88          1617      dec    hl
E814* 3D          1618      jr     nz, down_inner ;more scans to do, so jump
E815* 20 F1      1619      jr     nz, down_inner ;more scans to do, so jump
E816* 09          1620      ld      hl, bc, max_cols
E817* 01 0020    1621      ld      hl, bc, max_cols      ;move attributes
E818* 80 80      1622      ld      hl, bc, 2*max_cols
E819* 47          1623      end
E81A* EC 42      1624      sbc   hl, bc
E81B* 88          1625      ex     hl, hl
E81C* ED 42      1626      sbc   hl, bc
E81D* 88          1627      ex     hl, hl
E81E* 09          1628      or     hl, hl
E81F* 09          1629      or     hl, hl
E820* C1          1630      pop     hl
E821* 10 CE      1631      djnz  down_outer   ;if more lines to do, then jump
E822* E1          1632      pop     hl
E823* 01 0000    1633      ld      hl, bc, 0      ;clear top line
E824* CD E90E    1634      call  SET_PRINT_POS
E825* 06 20      1635      ld      hl, b, max_cols
E826* 3A DFF6    1636      ld      hl, a, (background) ;set background color to permanent
                        ; l value
E827* 32 5C8F    1637      ld      hl, (ATTR_T), a
E828* C5          1638      or     hl, a
E829* 3E 20      1639      ld      hl, " "
E82A* CD E91D    1640      call  WRITE_CHAR
E82B* C1          1641      pop     hl
E82C* 10 F7      1642      djnz  cl_top_line
E82D* 3E 01      1643      ld      hl, a, l      ;set parameter for upd_cols
E82E* CB E4E6*   1644      call  upd_rows
E82F* F8          1645      or     hl, hl
E830* 09          1646      or     hl, hl
E831* E1          1647      pop     hl
E832* 01          1648      pop     hl
E833* C1          1649      pop     hl
E834* 01          1650      pop     hl
E835* C1          1651      pop     hl
E836* 09          1652      or     hl, hl
E837* E1          1653      pop     hl
E838* 01          1654      pop     hl
E839* F1          1655      pop     hl
E83A* 01 0000    1656      ld      hl, bc, 0
E83B* C9          1657      ret
E83C*            1658
E83D*            1659
E840*            1660      ;This table contains the starting address of each row for use in vertical
E841*            1661      ; scrolling
E842*            1662
E843*            1663      vs_table      defb 4000H
E844*            1664      defb 4020H
E845*            1665      defb 4040H
E846*            1666      defb 4060H
E847*            1667      defb 4080H
E848*            1668      defb 40A0H
E849*            1669      defb 40C0H
E84A*            1670      defb 40E0H
E84B*            1671      defb 4100H
E84C*            1672      defb 4120H
E84D*            1673      defb 4140H
E84E*            1674      defb 4160H
E84F*            1675      defb 4180H
E850*            1676      defb 41A0H
E851*            1677      defb 41C0H
E852*            1678      defb 41E0H
E853*            1679      defb 4200H
E854*            1680      defb 4220H
E855*            1681      defb 4240H
E856*            1682      defb 4260H
E857*            1683      defb 4280H
E858*            1684      defb 42A0H
E859*            1685      defb 42C0H
E85A*            1686      defb 42E0H
E85B*            1687      defb 4300H
E85C*            1688      defb 4320H
E85D*            1689      defb 4340H
E85E*            1690      defb 4360H
E85F*            1691      defb 4380H
E860*            1692      defb 43A0H
E861*            1693      defb 43C0H
E862*            1694      defb 43E0H
E863*            1695      defb 4400H
E864*            1696      defb 4420H
E865*            1697      defb 4440H
E866*            1698      defb 4460H
E867*            1699      defb 4480H
E868*            1700      defb 44A0H
E869*            1701      defb 44C0H
E86A*            1702      defb 44E0H
E86B*            1703      defb 4500H
E86C*            1704      defb 4520H
E86D*            1705      defb 4540H
E86E*            1706      defb 4560H
E86F*            1707      defb 4580H
E870*            1708      defb 45A0H
E871*            1709      defb 45C0H
E872*            1710      defb 45E0H
E873*            1711      defb 4600H
E874*            1712      defb 4620H
E875*            1713      defb 4640H
E876*            1714      defb 4660H
E877*            1715      defb 4680H
E878*            1716      defb 46A0H
E879*            1717      defb 46C0H
E87A*            1718      defb 46E0H
E87B*            1719      defb 4700H
E87C*            1720      defb 4720H
E87D*            1721      defb 4740H
E87E*            1722      defb 4760H
E87F*            1723      defb 4780H
E880*            1724      defb 47A0H
E881*            1725      defb 47C0H
E882*            1726      defb 47E0H
E883*            1727      defb 4800H
E884*            1728      defb 4820H
E885*            1729      defb 4840H
E886*            1730      defb 4860H
E887*            1731      defb 4880H
E888*            1732      defb 48A0H
E889*            1733      defb 48C0H
E88A*            1734      defb 48E0H
E88B*            1735      defb 4900H
E88C*            1736      defb 4920H
E88D*            1737      defb 4940H
E88E*            1738      defb 4960H
E88F*            1739      defb 4980H
E890*            1740      defb 49A0H
E891*            1741      defb 49C0H
E892*            1742      defb 49E0H
E893*            1743      defb 4A00H
E894*            1744      defb 4A20H
E895*            1745      defb 4A40H
E896*            1746      defb 4A60H
E897*            1747      defb 4A80H
E898*            1748      defb 4AA0H
E899*            1749      defb 4AC0H
E89A*            1750      defb 4AE0H
E89B*            1751      defb 4B00H
E89C*            1752      defb 4B20H
E89D*            1753      defb 4B40H
E89E*            1754      defb 4B60H
E89F*            1755      defb 4B80H
E8A0*            1756      defb 4BA0H
E8A1*            1757      defb 4BC0H
E8A2*            1758      defb 4BE0H
E8A3*            1759      defb 4C00H
E8A4*            1760      defb 4C20H
E8A5*            1761      defb 4C40H
E8A6*            1762      defb 4C60H
E8A7*            1763      defb 4C80H
E8A8*            1764      defb 4CA0H
E8A9*            1765      defb 4CC0H
E8AA*            1766      defb 4CE0H
E8AB*            1767      defb 4D00H
E8AC*            1768      defb 4D20H
E8AD*            1769      defb 4D40H
E8AE*            1770      defb 4D60H
E8AF*            1771      defb 4D80H
E8B0*            1772      defb 4DA0H
E8B1*            1773      defb 4DC0H
E8B2*            1774      defb 4DE0H
E8B3*            1775      defb 4E00H
E8B4*            1776      defb 4E20H
E8B5*            1777      defb 4E40H
E8B6*            1778      defb 4E60H
E8B7*            1779      defb 4E80H
E8B8*            1780      defb 4EA0H
E8B9*            1781      defb 4EC0H
E8BA*            1782      defb 4EE0H
E8BB*            1783      defb 4F00H
E8BC*            1784      defb 4F20H
E8BD*            1785      defb 4F40H
E8BE*            1786      defb 4F60H
E8BF*            1787      defb 4F80H
E8C0*            1788      defb 4FA0H
E8C1*            1789      defb 4FC0H
E8C2*            1790      defb 4FE0H
E8C3*            1791      defb 5000H
E8C4*            1792      defb 5020H
E8C5*            1793      defb 5040H
E8C6*            1794      defb 5060H
E8C7*            1795      defb 5080H
E8C8*            1796      defb 50A0H
E8C9*            1797      defb 50C0H
E8CA*            1798      defb 50E0H
E8CB*            1799      defb 5100H
E8CC*            1800      defb 5120H
E8CD*            1801      defb 5140H
E8CE*            1802      defb 5160H
E8CF*            1803      defb 5180H
E8D0*            1804      defb 51A0H
E8D1*            1805      defb 51C0H
E8D2*            1806      defb 51E0H
E8D3*            1807      defb 5200H
E8D4*            1808      defb 5220H
E8D5*            1809      defb 5240H
E8D6*            1810      defb 5260H
E8D7*            1811      defb 5280H
E8D8*            1812      defb 52A0H
E8D9*            1813      defb 52C0H
E8DA*            1814      defb 52E0H
E8DB*            1815      defb 5300H
E8DC*            1816      defb 5320H
E8DD*            1817      defb 5340H
E8DE*            1818      defb 5360H
E8DF*            1819      defb 5380H
E8E0*            1820      defb 53A0H
E8E1*            1821      defb 53C0H
E8E2*            1822      defb 53E0H
E8E3*            1823      defb 5400H
E8E4*            1824      defb 5420H
E8E5*            1825      defb 5440H
E8E6*            1826      defb 5460H
E8E7*            1827      defb 5480H
E8E8*            1828      defb 54A0H
E8E9*            1829      defb 54C0H
E8EA*            1830      defb 54E0H
E8EB*            1831      defb 5500H
E8EC*            1832      defb 5520H
E8ED*            1833      defb 5540H
E8EE*            1834      defb 5560H
E8EF*            1835      defb 5580H
E8F0*            1836      defb 55A0H
E8F1*            1837      defb 55C0H
E8F2*            1838      defb 55E0H
E8F3*            1839      defb 5600H
E8F4*            1840      defb 5620H
E8F5*            1841      defb 5640H
E8F6*            1842      defb 5660H
E8F7*            1843      defb 5680H
E8F8*            1844      defb 56A0H
E8F9*            1845      defb 56C0H
E8FA*            1846      defb 56E0H
E8FB*            1847      defb 5700H
E8FC*            1848      defb 5720H
E8FD*            1849      defb 5740H
E8FE*            1850      defb 5760H
E8FF*            1851      defb 5780H
E800*            1852      defb 57A0H
E801*            1853      defb 57C0H
E802*            1854      defb 57E0H
E803*            1855      defb 5800H
E804*            1856      defb 5820H
E805*            1857      defb 5840H
E806*            1858      defb 5860H
E807*            1859      defb 5880H
E808*            1860      defb 58A0H
E809*            1861      defb 58C0H
E80A*            1862      defb 58E0H
E80B*            1863      defb 5900H
E80C*            1864      defb 5920H
E80D*            1865      defb 5940H
E80E*            1866      defb 5960H
E80F*            1867      defb 5980H
E810*            1868      defb 59A0H
E811*            1869      defb 59C0H
E812*            1870      defb 59E0H
E813*            1871      defb 5A00H
E814*            1872      defb 5A20H
E815*            1873      defb 5A40H
E816*            1874      defb 5A60H
E817*            1875      defb 5A80H
E818*            1876      defb 5AA0H
E819*            1877      defb 5AC0H
E81A*            1878      defb 5AE0H
E81B*            1879      defb 5B00H
E81C*            1880      defb 5B20H
E81D*            1881      defb 5B40H
E81E*            1882      defb 5B60H
E81F*            1883      defb 5B80H
E820*            1884      defb 5BA0H
E821*            1885      defb 5BC0H
E822*            1886      defb 5BE0H
E823*            1887      defb 5C00H
E824*            1888      defb 5C20H
E825*            1889      defb 5C40H
E826*            1890      defb 5C60H
E827*            1891      defb 5C80H
E828*            1892      defb 5CA0H
E829*            1893      defb 5CC0H
E82A*            1894      defb 5CE0H
E82B*            1895      defb 5D00H
E82C*            1896      defb 5D20H
E82D*            1897      defb 5D40H
E82E*            1898      defb 5D60H
E82F*            1899      defb 5D80H
E830*            1900      defb 5DA0H
E831*            1901      defb 5DC0H
E832*            1902      defb 5DE0H
E833*            1903      defb 5E00H
E834*            1904      defb 5E20H
E835*            1905      defb 5E40H
E836*            1906      defb 5E60H
E837*            1907      defb 5E80H
E838*            1908      defb 5EA0H
E839*            1909      defb 5EC0H
E83A*            1910      defb 5EE0H
E83B*            1911      defb 5F00H
E83C*            1912      defb 5F20H
E83D*            1913      defb 5F40H
E83E*            1914      defb 5F60H
E83F*            1915      defb 5F80H
E840*            1916      defb 5FA0H
E841*            1917      defb 5FC0H
E842*            1918      defb 5FE0H
E843*            1919      defb 6000H
E844*            1920      defb 6020H
E845*            1921      defb 6040H
E846*            1922      defb 6060H
E847*            1923      defb 6080H
E848*            1924      defb 60A0H
E849*            1925      defb 60C0H
E84A*            1926      defb 60E0H
E84B*            1927      defb 6100H
E84C*            1928      defb 6120H
E84D*            1929      defb 6140H
E84E*            1930      defb 6160H
E84F*            1931      defb 6180H
E850*            1932      defb 61A0H
E851*            1933      defb 61C0H
E852*            1934      defb 61E0H
E853*            1935      defb 6200H
E854*            1936      defb 6220H
E855*            1937      defb 6240H
E856*            1938      defb 6260H
E857*            1939      defb 6280H
E858*            1940      defb 62A0H
E859*            1941      defb 62C0H
E85A*            1942      defb 62E0H
E85B*            1943      defb 6300H
E85C*            1944      defb 6320H
E85D*            1945      defb 6340H
E85E*            1946      defb 6360H
E85F*            1947      defb 6380H
E860*            1948      defb 63A0H
E861*            1949      defb 63C0H
E862*            1950      defb 63E0H
E863*            1951      defb 6400H
E864*            1952      defb 6420H
E865*            1953      defb 6440H
E866*            1954      defb 6460H
E867*            1955      defb 6480H
E868*            1956      defb 64A0H
E869*            1957      defb 64C0H
E86A*            1958      defb 64E0H
E86B*            1959      defb 6500H
E86C*            1960      defb 6520H
E86D*            1961      defb 6540H
E86E*            1962      defb 6560H
E86F*            1963      defb 6580H
E870*            1964      defb 65A0H
E871*            1965      defb 65C0H
E872*            1966      defb 65E0H
E873*            1967      defb 6600H
E874*            1968      defb 6620H
E875*            1969      defb 6640H
E876*            1970      defb 6660H
E877*            1971      defb 6680H
E878*            1972      defb 66A0H
E879*            1973      defb 66C0H
E87A*            1974      defb 66E0H
E87B*            1975      defb 6700H
E87C*            1976      defb 6720H
E87D*            1977      defb 6740H
E87E*            1978      defb 6760H
E87F*            1979      defb 6780H
E880*            1980      defb 67A0H
E881*            1981      defb 67C0H
E882*            1982      defb 67E0H
E883*            1983      defb 6800H
E884*            1984      defb 6820H
E885*            1985      defb 6840H
E886*            1986      defb 6860H
E887*            1987      defb 6880H
E888*            1988      defb 68A0H
E889*            1989      defb 68C0H
E88A*            1990      defb 68E0H
E88B*            1991      defb 6900H
E88C*            1992      defb 6920H
E88D*            1993      defb 6940H
E88E*            1994      defb 6960H
E88F*            1995      defb 6980H
E890*            1996      defb 69A0H
E891*            1997      defb 69C0H
E892*            1998      defb 69E0H
E893*            1999      defb 6A00H
E894*            2000      defb 6A20H
E895*            2001      defb 6A40H
E896*            2002      defb 6A60H
E897*            2003      defb 6A80H
E898*            2004      defb 6AA0H
E899*            2005      defb 6AC0H
E89A*            2006      defb 6AE0H
E89B*            2007      defb 6B00H
E89C*            2008      defb 6B20H
E89D*            2009      defb 6B40H
E89E*            2010      defb 6B60H
E89F*            2011      defb 6B80H
E8A0*            2012      defb 6BA0H
E8A1*            2013      defb 6BC0H
E8A2*            2014      defb 6BE0H
E8A3*            2015      defb 6C00H
E8A4*            2016      defb 6C20H
E8A5*            2017      defb 6C40H
E8A6*            2018      defb 6C60H
E8A7*            2019      defb 6C80H
E8A8*            2020      defb 6CA0H
E8A9*            2021      defb 6CC0H
E8AA*            2022      defb 6CE0H
E8AB*            2023      defb 6D00H
E8AC*            2024      defb 6D20H
E8AD*            2025      defb 6D40H
E8AE*            2026      defb 6D60H
E8AF*            2027      defb 6D80H
E8B0*            2028      defb 6DA0H
E8B1*            2029      defb 6DC0H
E8B2*            2030      defb 6DE0H
E8B3*            2031      defb 6E00H
E8B4*            2032      defb 6E20H
E8B5*            2033      defb 6E40H
E8B6*            2034      defb 6E60H
E8B7*            2035      defb 6E80H
E8B8*            2036      defb 6EA0H
E8B9*            2037      defb 6EC0H
E8BA*            2038      defb 6EE0H
E8BB*            2039      defb 6F00H
E8BC*            2040      defb 6F20H
E8BD*            2041      defb 6F40H
E8BE*            2042      defb 6F60H
E8BF*            2043      defb 6F80H
E8C0*            2044      defb 6FA0H
E8C1*            2045      defb 6FC0H
E8C2*            2046      defb 6FE0H
E8C3*            2047      defb 7000H
E8C4*            2048      defb 7020H
E8C5*            2049      defb 7040H
E8C6*            2050      defb 7060H
E8C7*            2051      defb 7080H
E8C8*            2052      defb 70A0H
E8C9*            2053      defb 70C0H
E8CA*            2054      defb 70E0H
E8CB*            2055      defb 7100H
E8CC*            2056      defb 7120H
E8CD*            2057      defb 7140H
E8CE*            2058      defb 7160H
E8CF*            2059      defb 7180H
E8D0*            2060      defb 71A0H
E8D1*            2061      defb 71C0H
E8D2*            2062      defb 71E0H
E8D3*            2063      defb 7200H
E8D4*            2064      defb 7220H
E8D5*            2065      defb 7240H
E8D6*            2066      defb 7260H
E8D7*            2067      defb 7280H
E8D8*            2068      defb 72A0H
E8D9*            2069      defb 72C0H
E8DA*            2070      defb 72E0H
E8DB*            2071      defb 7300H
E8DC*            2072      defb 7320H
E8DD*            2073      defb 7340H
E8DE*            2074      defb 7360H
E8DF*            2075      defb 7380H
E8E0*            2076      defb 73A0H
E8E1*            2077      defb 73C0H
E8E2*            2078      defb 73E0H
E8E3*            2079      defb 7400H
E8E4*            2080      defb 7420H
E8E5*            2081      defb 7440H
E8E6*            2082      defb 7460H
E8E7*            2083      defb 7480H
E8E8*            2084      defb 74A0H
E8E9*            2085      defb 74C0H
E8EA*            2086      defb 74E0H
E8EB*            2087      defb 7500H
E8EC*            2088      defb 7520H
E8ED*            2089      defb 7540H
E8EE*            2090      defb 7560H
E8EF*            2091      defb 7580H
E8F0*            2092      defb 75A0H
E8F1*            2093      defb 75C0H
E8F2*            2094      defb 75E0H
E8F3*            2095      defb 7600H
E8F4*            2096      defb 7620H
E8F5*            2097      defb 7640H
E8F6*            2098      defb 7660H
E8F7*            2099      defb 7680H
E8F8*            2100      defb 76A0H
E8F9*            2101      defb 76C0H
E8FA*            2102      defb 76E0H
E8FB*            2103      defb 7700H
E8FC*            2104      defb 7720H
E8FD*            2105      defb 7740H
E8FE*            2106      defb 7760H
E8FF*            2107      defb 7780H
E800*            2108      defb 77A0H
E801*            2109      defb 77C0H
E802*            2110      defb 77E0H
E803*            2111      defb 7800H
E804*            2112      defb 7820H
E805*            2113      defb 7840H
E806*            2114      defb 7860H
E807*            2115      defb 7880H
E808*            2116      defb 78A0H
E809*            2117      defb 78C0H
E80A*            2118      defb 78E0H
E80B*            2119      defb 7900H
E80C*            2120      defb 7920H
E80D*            2121      defb 7940H
E80E*            2122      defb 7960H
E80F*            2123      defb 7980H
E810*            2124      defb 79A0H
E811*            2125      defb 79C0H
E812*            2126      defb 79E0H
E813*            2127      defb 7A00H
E814*            2128      defb 7A20H
E815*            2129      defb 7A40H
E816*            2130      defb 7A60H
E817*            2131      defb 7A80H
E818*            2132      defb 7AA0H
E819*            2133      defb 7AC0H
E81A*            2134      defb 7AE0H
E81B*            2135      defb 7B00H
E81C*            2136      defb 7B20H
E81D*            2137      defb 7B40H
E81E*            2138      defb 7B60H
E81F*            2139      defb 7B80H
E820*            2140      defb 7BA0H
E821*            2141      defb 7BC0H
E822*            2142      defb 7BE0H
E823*            2143      defb 7C00H
E824*            2144      defb 7C20H
E825*            2145      defb 7C40H
E826*            2146      defb 7C60H
E827*            2147      defb 7C80H
E828*            2148      defb 7CA0H
E829*            2149      defb 7CC0H
E82A*            2150      defb 7CE0H
E82B*            2151      defb 7D00H
E82C*            2152      defb 7D20H
E82D*            2153      defb 7D40H
E82E*            2154      defb 7D60H
E82F*            2155      defb 7D80H
E830*            2156      defb 7DA0H
E831*            2157      defb 7DC0H
E
```

A	Reserved	ALLOC	0000	ATTRSP	SCBD	ATTRBT	SCBP	AUTDWR	0001
B	Reserved	BACKGC	0FF0	BCDOK	E4FF	BCDOK	E507	BLACKB	E928
BDRDGR	SC48	SCRDPY	00FE	C	Reserved	CMARS	SC36	CHECKB	E282
CHTAB	E890	CHBATT	E466	CM9ASC	E479	CM9ABI	E46E	CMELCC	E450
CH9L91	E459	CH9DM1	E35F	CH9DM2	E3F5	CLEARB	E236	CL50B	097P
CL9BDY	E7CA	CL9TOP	E845	CCL	0001	CCLDR	0004	CLDRB	E278
CDL9LO	E354	COMMAN	0FF7	CDNT1	E36F	CDNY2	E405	CREATE	E278
CDL9DP	E3EA	D	Reserved	DCSEXI	E2F0	OISPLA	E20B	DDW4BI	E413
DDNH9D	E904	DDNH83	E7D7	E	Reserved	ENDUGH	E253	ERASEB	E381
ERASCH	E406	EEXIT	E421	EVID8D	E3C0	GETACH	E91F	GPLAGS	0FF0
GLOBAL	001D	H	Reserved	HEIGHT	0003	HSVCDB	E676	HSVCDB	E5D6
ID9OK	E284	INITSC	E4E9	INITAS	E219	INNER1	E348	INNER2	E3D2
L	Reserved	LEPTSI	E609	LEFT9D	E5F0	LEPT9S	E5E0	LEPT9T	E606
LMS	0002	M	Reserved	MAX9CD	0020	MAX9RD	0018	MAX9SP	0FFA
NDV885	E426	MEXIT	E44D	NAME	0FF0	NEG	0000	NEATCH	E38A
NE7FS	E5C9	NDSCLE	E549	NDSCCL	E480	NDRCVB	E387	NDSTCO	E37A
NUM9PA	DFFC	NITCHA	E40C	NHS5PR	E744	OP	E3D4	OUT9P1	E343
OUT9R2	E3D9	OVERLA	E484	CVERWR	0001	QVRWRD	E3AD	QREXIT	E4E3
ODID9H	E48F	QCHK1	E493	PARMS	0FFB	PARM1	0FF0	PARM2	0FF0
PARM3	0FF1	PARM4	0FF3	PARMS	0FF5	PRITMA	0005	PSW	Reserved
PUT9SP	E307	PEXIT	E390	P9TORG	E316	RAMTOP	SC92	RIGMBS	E430
ROM	0000	ROM9LO	E365	POH9CK	E442	RTBINW	E44C	RTQOUT	E640
RT9TAB	E636	RSLQDP	E3F9	SAS9KI	E2D6	SCRATC	0FF3	SCREEN	0FF0
SEL9CT	1230	SENDTV	0500	SET9AT	0582	SET9AU	E2C9	SET9PR	0FF0
SPLAGS	0007	SH9CK	E4F1	SP	Reserved	SPRCDD	E000	SPRITE	E92E
SP9AD	E25F	SVCS9C	0FFB	TYPLAG	SC3C	UC9CDB	E59F	UC1	E599
UP99AL	E580	UP99RL	E712	UP99CO	E554	UP99RD	E6E6	UP99INW	E79E
UP99OUT	E78F	UP99SCR	E77E	UR99CDB	E73A	URI	E72F	VISIBL	E2F8
VS99COM	E850	VS99TAB	E760	V99SC9C	E771	WIDTM	0002	WRITESB	E89D
WRT9CA	E37A	WRT9SP	E58E	WT9SPR	E759	Y	SC3A	Y999CO	E4D8

No errors detected

```

1      NAME IOLOM
2      SUBTTL Low level I/O module
3
4
5      ;*****
6      ;
7      ; Low Level I/O Module
8      ;
9      ; Inputs: parameters to desired service in appropriate registers.
10     ;
11     ; Outputs: none.
12     ;
13     ; Description: this module provides the following low level i/o services:
14     ;
15     ;   Set_Print_Pos - sets the print position
16     ;   Write_Char - writes a character to the current print position
17     ;   Get_Char - returns the character code of the character at the
18     ;                 desired position
19     ;
20     ;   These services are based on the routines SETCUR, WPCNAR,
21     ;   and GTCNAR of the dual-column support component of the
22     ;   Applications Development Library.
23     ;
24     ;*****
25
26
27     I Imports (Note: all symbols in upper case are imported)
28
29     IFrom the Home RBN
30
31     IVariables
32
33     *SC78      UDG          EQU      SC78H
34     *SC80      ATTR_P     EQU      SC80H
35     *SC8E      MASK_P     EQU      SC8EH
36     *SC91      P_FLAG     EQU      SC91H
37     *SCC2      VIDMOD     EQU      SCC2H
38
39
40     I Exports
41
42     IVariables
43
44     I          chtbl
45
46     I Services
47
48     I          write_Char, Set_Print_Pos, Get_Char
49
50
51     I Locals
52
53     I Constants
54
55     *0018      sercs       equ      24      I 24 lines
56     *3C00      chroot     equ      3c00h    I row char.table-100h
57     *893A      gphst      equ      ((gprst)-100h) Istd.graphics char.
58
59
60     I Variables
61     *E890      ORG        @E890H
62
63     E890      3C00      ctbl      defb      chroot    I address of character table  57391
64     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
65     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
66     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
67     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
68     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
69     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
70     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
71     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
72     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
73     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
74     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
75     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
76     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;
77     ;          ;         ;         ;         ;         ;         ;         ;         ;         ;         ;         ;

```



```

78
79 ;
80 ;
81 ; WRITE_CHAR
82 ;
83 ; Inputs: A = character code.
84 ;
85 ; Outputs: BC = 0800 - Bx
86 ;           = 0CXX - error; XI < 00
87 ;
88 ; Globals Read: UDG
89 ;               grtbl
90 ;               chtbl
91 ;               linlen
92 ;               maskb
93 ;
94 ; Globals Written: none.
95 ;
96 ; Procedures Called: ldattr
97 ;                   ldpsn
98 ;                   updatt
99 ;                   stpsn
100 ;
101 ; Procedures Called By: DISPLAY_CHAR
102 ;                      V_SKBLL
103 ;
104 ; Description: this service writes the specified character to the current
105 ;              print position.
106 ;
107 ;
108 ;
109 ;
110 ;
111 write_char      push    af
112                push    de
113                push    hl
114                call   ldattr      ; entry with code in a
115 ;                                ; get attribute and mask controls
116 ;
117 ;
118 ; entry to use attribute values in
119 ; internal variables without reloading
120 ; load registers for current position
121 ; bc=cursor position from "cursor"
122 ; hl=display file address from "ofsora"
123 ; encode for char. to be displayed
124
125                push    bc
126                cp      80h
127                jr      c, wrch12
128                cp      90h
129                jr      c, wrch11
130                ld      bc, (UDG)
131                sub     b, 00h
132                jr      wrch13
133                ld      bc, (grtbl)
134                sub     b, 40h
135                jr      wrch13
136                ld      bc, (chtbl)
137                or      de, hl
138                ld      hl, 0
139                ld      l, a
140                orl    hl, hl
141                orl    de, de
142                orl    hl, hl
143                add    hl, bc
144                pop    bc
145                or     de, hl
146                ld     c, c
147                dec   a
148                ld   a, (linlen)
149                jr   nc, wrch16
150                inc  a
151                dec  b
152                ld  c, a
153                jr  wrch15
154                wrch14 wrch13
155                wrch13 wrch2
156                push  bc
157                push  hl
158                ld   a, (maskb)
159                ld   b, -1
160                rrc  c, wrch3
161                inc  b
162                rrc  b
163                rrc  a
164                rrc  a
165                or   a, a
166                ld  c, a
167                or  a, 8
168                and a
169                or  de, hl
170                or  af, af
171                ld  a, (de)
172                and b
173                xrl hl)
174                xor c
175                ld  (de), a
176                or  af, af
177                inc d
178                inc hl
179                dec a
180                jr  nz, wrch5
181                dec d
182                or  de, hl
183                call updatt

```

```

E8FF E1          184          pop     hl          ; starting address
E900 C1          185          pop     bc          ; cursor position
E901 0D          186          dec     c           ; adjust cursor position
E902 23          187          inc     hl          ; adjust df address
E903 CC E9D7     188          wrchnt call    steven     ; store df position
E904 CE 00       189          goodret ld     c, 0      ; return bc=0
E908 06 00       190          errret ld     b, 0      ; enter here with c non-zero
E90A E1          191          pop     hl
E90B 01          192          pop     de
E90C F1          193          pop     sp
E90D C9          194          ret
          195
          196
          197
          198
          199 ; SET_PRINT_POS
          200 ;
          201 ; Input: B = row
          202 ;       C = column
          203 ;
          204 ; Output: BC = GOOD - OK
          205 ;
          206 ; Globals Read: none.
          207 ;
          208 ; Globals Written: none.
          209 ;
          210 ; Procedures Called: convfm
          211 ;                   updpspn
          212 ;
          213 ; Procedures Called By: DISPLAY_CHAR
          214 ;                   V_SCROLL
          215 ;
          216 ; Description: this service sets the print position to the specified row
          217 ; and column. The row position must be in the range 0..23
          218 ; and the column position must be in the range 0..31.
          219 ;
          220 ;
          221 ;
          222
          223 set_print_pos  push  sp
          224                push  de
          225                push  hl
          226                call  convfm      ; entry with line/col. in bc reg.
          227                ; convert to internal format
          228                call  updpspn     ; calc. and save the print posn.
          229                ld     c, 0      ; return bc=0
          230                ld     b, 0      ; enter here with c non-zero
          231                pop   hl
          232                pop   de
          233                pop   sp
          234                ret
          235
          236
          237 ;
          238 ;
          239 ; GET_CHAR
          240 ;
          241 ; Input: B = row
          242 ;       C = column
          243 ;
          244 ; Output: A = character code, if a char is found, 0 otherwise
          245 ;       BC = 00xx - 0x, char found, 0x = A
          246 ;       = 0000 - 0x, no match retnd
          247 ;
          248 ; Globals Read: chtbl
          249 ;               gtindx
          250 ;               grtbl
          251 ;               UDS
          252 ;
          253 ; Globals Written: gtindx
          254 ;
          255 ; Procedures Called: convfm
          256 ;                   calcpos
          257 ;
          258 ; Procedures Called By: PUT_SPRITE
          259 ;
          260 ; Description: this service returns the character code of the character
          261 ; at the specified screen location. The row and column must
          262 ; be in the range specified above for Set_Print_Pos.
          263 ;
          264 ;
          265 ;
          266
          267 get_char      push  de
          268                push  hl
          269                call  convfm      ; entry with position in bc
          270                ; convert to internal format
          271                call  calcpos     ; get display file adrs.
          272                ld     de, (chtbl) ; char.table
          273                ld     b, 96      ; no. of printable characters
          274                ld     a, 80h    ; set adjustment index
          275          gtbl  ld     (gtindx), a
          276                inc     d
          277                or     hl
          278          gtbl  push  bc
          279                push  de
          280                push  hl
          281                ld     a, (de)
          282                xor     (hl)
          283                jr     z, gtbl3
          284                push  sp
          285                ld     a, (gtindx)
          286                cp     90h
          287                jr     nz, gtbl23 ; test if std.graphics
          288                pop   sp
          289                jr     gtbl4
          290          gtbl23 pop   sp
          291                inc     a
          292                ; do not test for inverse
          293                ; test inverse

```

```

E948 20 24          292      jr      nz, gtcH4
E94A 3C             293      dec     a                | a=-1 for inverse
E94B 4P             294      ld     c, a             | c=0 for match -1 for inverse
E94C 06 07          295      ld     b, 7
E94E 14             296      inc     d                | next scan in d'
E94F 23             297      inc     hl              | next byte in char.set
E950 1A             298      ld     a, (de)
E951 AE             299      mov     r0, (hl)
E952 A9             300      mov     c
E953 20 19          301      jr      nz, gtcH4       | no match
E955 10 P7          302      djnz   gtcH31          | for next scan
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
346
347
348
349
350
351
352
353
354
355
356
357
358
359
360
361
362
363
364
365
366
367
368
369
370
371
372
373
374
375
376
377
378
379
380
381
382
383
384
385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420
421
422
423
424
425
426
427
428
429
430
431
432
433
434
435
436
437
438
439
440
441
442
443
444
445
446
447
448
449
450
451
452
453
454
455
456
457
458
459
460
461
462
463
464
465
466
467
468
469
470
471
472
473
474
475
476
477
478
479
480
481
482
483
484
485
486
487
488
489
490
491
492
493
494
495
496
497
498
499
500
501
502
503
504
505
506
507
508
509
510
511
512
513
514
515
516
517
518
519
520
521
522
523
524
525
526
527
528
529
530
531
532
533
534
535
536
537
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580
581
582
583
584
585
586
587
588
589
590
591
592
593
594
595
596
597
598
599
600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681
682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719
720
721
722
723
724
725
726
727
728
729
730
731
732
733
734
735
736
737
738
739
740
741
742
743
744
745
746
747
748
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
769
770
771
772
773
774
775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
836
837
838
839
840
841
842
843
844
845
846
847
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865
866
867
868
869
870
871
872
873
874
875
876
877
878
879
880
881
882
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917
918
919
920
921
922
923
924
925
926
927
928
929
930
931
932
933
934
935
936
937
938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962
963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999

```

```

391 |=====
392 |
393 | UPDPGSM
394 |
395 | Inputs: B = row in internal format
396 |         C = column in internal format
397 |
398 | Outputs: none
399 |
400 | Globals Read: none.
401 |
402 | Globals Written: none
403 |
404 | Procedures Called: calcpos
405 |                 steppn
406 |
407 | Procedures Called By: set_print_pos
408 |
409 | Description: this routine set the print position to the specified value.
410 |
411 |=====
412 |
413 |
414 |udposn                                | enter here with bc=line/col.in
415 |                                     | internal format
416 |
417 |         call    calcpos             | calculate position
418 |         call    steppn              | store updated position and return
419 |         ret
420 |
421 |
422 |=====
423 |
424 | CALCPCS
425 |
426 | Inputs: B = row in internal format
427 |         C = column in internal format
428 |
429 | Outputs: HL = address in display file
430 |
431 | Globals Read: linlen
432 |
433 | Globals Written: none.
434 |
435 | Procedures Called: lnbu
436 |
437 | Procedures Called By: get_char
438 |                 udposn
439 |
440 | Description: this routine calculates the position in the display file
441 | corresponding to the specified row and column position.
442 |
443 |=====
444 |
445 |
446 | calcpos    call    lnbu             | get display file addr.
447 |            ld     a, (linlen)      | add column offset
448 |            inc   a
449 |            sub   c
450 |            ld   a, a
451 |            ld   d, 0
452 |            add  hl, de
453 |            ret
454 |
455 |
456 |=====
457 |
458 | LNBUS
459 |
460 | Inputs: B = row in internal format
461 |
462 | Outputs: HL = address in display file of start of line B.
463 |
464 | Globals Read: VIDMOD
465 |
466 | Globals Written: none.
467 |
468 | Procedures Called: none.
469 |
470 | Procedures Called By: calcpos
471 |
472 | Description: this routine calculates the address of the start of row B
473 | in the display file. This is the first byte of the first
474 | scan line.
475 |
476 |=====
477 |
478 |
479 | lnbu                                | get display file address
480 |                                     | for start of line in b
481 |            ld     a, 0x00
482 |            sub   b
483 |            ld   d, a
484 |            rrcs
485 |            rrcs
486 |            rrcs
487 |            and  0x0h
488 |            ld   l, a
489 |            ld   h, a
490 |            and  10h
491 |            or   40h
492 |            ld   h, a
493 |            ld   a, (VIDMOD)
494 |            bit  0, a                | test if df1
495 |            ret
496 |            ld   a, 20h              | set to d72
497 |            or   h
498 |            ld   h, a
499 |            ret
500 |
501 |

```

```

E9A8 CC E9A9
E9AB CC E9B7
E9AE C9

```

```

E9AF CC E9BC
E9B2 9A E994
E9B3 3C
E9B6 91
E9B7 5F
E9B8 1A 00
E9BA 10
E9BB C9

```

```

E9BC
E9BC 3E 10
E9BD 70
E9BE 37
E9BF 0F
E9C0 0F
E9C1 0F
E9C2 0F
E9C3 0A 10
E9C4 7A
E9C7 0A 10
E9C9 0A 00
E9CA 07
E9CB 3A E9C2
E9CC C9 47
E9D1 C9
E9D2 3E 20
E9D4 8A
E9D5 07
E9D6 C9

```

```

522 ;
523 ;
524 ; STPDSH
525 ;
526 ; Inputs: BC = current print position
527 ;           ML = display file address
528 ;
529 ; Outputs: none
530 ;
531 ; Globals Read: none.
532 ;
533 ; Globals Written: curpos
534 ;           dfadrs
535 ;
536 ; Procedures Called: none.
537 ;
538 ; Procedures Called By: write_dhar
539 ;           udpsen
540 ;
541 ; Description: this routine saves the inputs in the appropriate variables.
542 ;
543 ;
544 ;
545 ;
546 ;
547 ;
548 ;
549 ;
550 ;
551 ;
552 ;
553 ;
554 ;
555 ;
556 ;
557 ;
558 ;
559 ;
560 ;
561 ;
562 ;
563 ;
564 ;
565 ;
566 ;
567 ;
568 ;
569 ;
570 ;
571 ;
572 ;
573 ;
574 ;
575 ;
576 ;
577 ;
578 ;
579 ;
580 ;
581 ;
582 ;
583 ;
584 ;
585 ;
586 ;
587 ;
588 ;
589 ;
590 ;
591 ;
592 ;
593 ;
594 ;
595 ;
596 ;
597 ;
598 ;
599 ;
600 ;
601 ;
602 ;
603 ;
604 ;
605 ;
606 ;
607 ;
608 ;
609 ;
610 ;
611 ;
612 ;
613 ;
614 ;
615 ;
616 ;
617 ;
618 ;
619 ;
620 ;
621 ;
622 ;
623 ;
624 ;
625 ;
626 ;
627 ;
628 ;
629 ;
630 ;
631 ;
632 ;
633 ;
634 ;
635 ;
636 ;
637 ;
638 ;
639 ;
640 ;
641 ;
642 ;
643 ;
644 ;
645 ;
646 ;
647 ;
648 ;
649 ;
650 ;
651 ;
652 ;
653 ;
654 ;
655 ;
656 ;
657 ;
658 ;
659 ;
660 ;
661 ;
662 ;
663 ;
664 ;
665 ;
666 ;
667 ;
668 ;
669 ;
670 ;
671 ;
672 ;
673 ;
674 ;
675 ;
676 ;
677 ;
678 ;
679 ;
680 ;
681 ;
682 ;
683 ;
684 ;
685 ;
686 ;
687 ;
688 ;
689 ;
690 ;
691 ;
692 ;
693 ;
694 ;
695 ;
696 ;
697 ;
698 ;
699 ;
700 ;
701 ;
702 ;
703 ;
704 ;
705 ;
706 ;
707 ;
708 ;
709 ;
710 ;
711 ;
712 ;
713 ;
714 ;
715 ;
716 ;
717 ;
718 ;
719 ;
720 ;
721 ;
722 ;
723 ;
724 ;
725 ;
726 ;
727 ;
728 ;
729 ;
730 ;
731 ;
732 ;
733 ;
734 ;
735 ;
736 ;
737 ;
738 ;
739 ;
740 ;
741 ;
742 ;
743 ;
744 ;
745 ;
746 ;
747 ;
748 ;
749 ;
750 ;
751 ;
752 ;
753 ;
754 ;
755 ;
756 ;
757 ;
758 ;
759 ;
760 ;
761 ;
762 ;
763 ;
764 ;
765 ;
766 ;
767 ;
768 ;
769 ;
770 ;
771 ;
772 ;
773 ;
774 ;
775 ;
776 ;
777 ;
778 ;
779 ;
780 ;
781 ;
782 ;
783 ;
784 ;
785 ;
786 ;
787 ;
788 ;
789 ;
790 ;
791 ;
792 ;
793 ;
794 ;
795 ;
796 ;
797 ;
798 ;
799 ;
800 ;
801 ;
802 ;
803 ;
804 ;
805 ;
806 ;
807 ;
808 ;
809 ;
810 ;
811 ;
812 ;
813 ;
814 ;
815 ;
816 ;
817 ;
818 ;
819 ;
820 ;
821 ;
822 ;
823 ;
824 ;
825 ;
826 ;
827 ;
828 ;
829 ;
830 ;
831 ;
832 ;
833 ;
834 ;
835 ;
836 ;
837 ;
838 ;
839 ;
840 ;
841 ;
842 ;
843 ;
844 ;
845 ;
846 ;
847 ;
848 ;
849 ;
850 ;
851 ;
852 ;
853 ;
854 ;
855 ;
856 ;
857 ;
858 ;
859 ;
860 ;
861 ;
862 ;
863 ;
864 ;
865 ;
866 ;
867 ;
868 ;
869 ;
870 ;
871 ;
872 ;
873 ;
874 ;
875 ;
876 ;
877 ;
878 ;
879 ;
880 ;
881 ;
882 ;
883 ;
884 ;
885 ;
886 ;
887 ;
888 ;
889 ;
890 ;
891 ;
892 ;
893 ;
894 ;
895 ;
896 ;
897 ;
898 ;
899 ;
900 ;
901 ;
902 ;
903 ;
904 ;
905 ;
906 ;
907 ;
908 ;
909 ;
910 ;
911 ;
912 ;
913 ;
914 ;
915 ;
916 ;
917 ;
918 ;
919 ;
920 ;
921 ;
922 ;
923 ;
924 ;
925 ;
926 ;
927 ;
928 ;
929 ;
930 ;
931 ;
932 ;
933 ;
934 ;
935 ;
936 ;
937 ;
938 ;
939 ;
940 ;
941 ;
942 ;
943 ;
944 ;
945 ;
946 ;
947 ;
948 ;
949 ;
950 ;
951 ;
952 ;
953 ;
954 ;
955 ;
956 ;
957 ;
958 ;
959 ;
960 ;
961 ;
962 ;
963 ;
964 ;
965 ;
966 ;
967 ;
968 ;
969 ;
970 ;
971 ;
972 ;
973 ;
974 ;
975 ;
976 ;
977 ;
978 ;
979 ;
980 ;
981 ;
982 ;
983 ;
984 ;
985 ;
986 ;
987 ;
988 ;
989 ;
990 ;
991 ;
992 ;
993 ;
994 ;
995 ;
996 ;
997 ;
998 ;
999 ;

```

```

616
617
618 |=====
619 |
620 | CALCATT
621 |
622 | Inputs: NL = address of any scan of a character position
623 |
624 | Outputs: NL = address of attribute byte
625 |
626 | Globals Read: none.
627 |
628 | Globals Written: none.
629 |
630 | Procedures Called: none.
631 |
632 | Procedures Called By: usdatt
633 |
634 | Description: this routine calculates the address of the attribute byte
635 | corresponding to the character position identified with
636 | NL.
637 |
638 |=====
639
640
641 calcatt      ld      a, h      | user byte of address
642             rrca
643             rrca
644             rrca
645             and     03h
646             or      80h
647             bit     5, h      | test which of
648             jr      z, calcal
649             or      20h      | d72
650 calcal      ld      h, a      | addr. of attribute in d72
651             ret
652
653 |=====
654
655 |
656 | LDATTR
657 |
658 | Inputs: none.
659 |
660 | Outputs: none.
661 |
662 | Globals Read: ATTR_P
663 |              MASK_P
664 |              P_FLAG
665 |
666 | Globals Written: attrbyt
667 |                 attrmk
668 |                 maskb
669 |
670 | Procedures Called: none.
671 |
672 | Procedures Called By: write_cher
673 |
674 | Description: this routine sets the values of the variables used by this
675 | module for manipulating attributes with values from the
676 | variables used by the Home ROM.
677 |
678 |=====
679
680
681 ldattr      | load internal attribute variables
682             | from system variables
683
684             push   a          | save a
685             ld     a, (ATTR_P)
686             ld     (attrbyt), a
687             ld     a, (MASK_P)
688             ld     (attrmk), a
689             ld     a, (P_FLAG)
690             rrca              | shift odd bits to even
691             ld     (maskb), a
692             pop    a
693             ret
694
695
696 | Standard graphics characters
697
698 graat      defb 00C00000b    | code 80      graphics space
699             defb 00D00000b
700             defb 00E00000b
701             defb 00F00000b
702             defb 00000000b
703             defb 00000000b
704             defb 00000000b
705             defb 00000000b
706
707             defb 00011111b    | code 81      graphics
708             defb 00021111b
709             defb 00031111b
710             defb 00041111b
711             defb 00050000b
712             defb 00060000b
713             defb 00070000b
714             defb 00080000b
715             defb 00090000b
716
717             defb 11110000b    | code 82      graphics
718             defb 11120000b
719             defb 11130000b
720             defb 11140000b
721             defb 00050000b
722             defb 00060000b
723             defb 00070000b
724
725             defb 11111111b    | code 83      graphics
726             defb 11121111b
727             defb 11131111b
728             defb 11141111b

```

E456	00	729	defb 00000000b		
E457	00	730	defb 00000200b		
E458	00	731	defb 00000300b		
E459	00	732	defb 00000000b		
E45A	00	733			
E45B	00	734	defb 00000000b	i code 04	graphics
E45C	00	735	defb 00000000b		
E45D	00	736	defb 00000200b		
E45E	00	737	defb 00000300b		
E45F	0F	738	defb 000C1111b		
E460	0F	739	defb 00001111b		
E461	0F	740	defb 00001111b		
E462	0F	741	defb 00001111b		
E463	0F	742	defb 00001111b	i code 05	graphics
E464	0F	743	defb 000C1111b		
E465	0F	744	defb 00001111b		
E466	0F	745	defb 000C1111b		
E467	0F	746	defb 00001111b		
E468	0F	747	defb 00001111b		
E469	0F	748	defb 000C1111b		
E46A	0F	749	defb 00001111b		
E46B	0F	750	defb 00001111b		
E46C	0F	751			
E46D	0F	752	defb 11110000b	i code 06	graphics
E46E	0F	753	defb 11110000b		
E46F	0F	754	defb 11110000b		
E470	0F	755	defb 11110000b		
E471	0F	756	defb 00001111b		
E472	0F	757	defb 00001111b		
E473	0F	758	defb 00001111b		
E474	0F	759	defb 00001111b		
E475	0F	760	defb 000C1111b		
E476	0F	761	defb 00001111b		
E477	0F	762	defb 00001111b		
E478	0F	763	defb 00001111b		
E479	0F	764	defb 00001111b		
E47A	00	765			
E47B	00	766	defb 00000000b	i code 07	graphics
E47C	00	767	defb 00000000b		
E47D	00	768	defb 00000000b		
E47E	00	769	defb 00000000b		
E47F	00	770	defb 00000000b		
E480	00	771	defb 00000000b		
E481	00	772	defb 00000000b		
E482	0F	773	defb 000C1111b		
E483	0F	774	defb 00001111b	i code 08	graphics
E484	0F	775	defb 00C11111b		
E485	0F	776	defb 00011111b		
E486	0F	777	defb 00011111b		
E487	0F	778	defb 00011111b		
E488	0F	779	defb 00011111b		
E489	0F	780	defb 00011111b		
E48A	0F	781	defb 00011111b		
E48B	0F	782	defb 00011111b		
E48C	0F	783	defb 00011111b		
E48D	0F	784	defb 00011111b		
E48E	0F	785	defb 00011111b		
E48F	0F	786	defb 00011111b		
E490	0F	787			
E491	0F	788	defb 11110000b	i code 0A	graphics
E492	0F	789	defb 11110000b		
E493	0F	790	defb 11110000b		
E494	0F	791	defb 11110000b		
E495	0F	792	defb 11110000b		
E496	0F	793	defb 11110000b		
E497	0F	794	defb 11110000b		
E498	0F	795	defb 11110000b		
E499	0F	796	defb 11110000b		
E49A	00	797	defb 11101111b	i code 0B	graphics
E49B	00	798	defb 11101111b		
E49C	00	799	defb 11101111b		
E49D	00	800	defb 11101111b		
E49E	00	801	defb 11100000b		
E49F	00	802	defb 11100000b		
E4A0	00	803	defb 11100000b		
E4A1	00	804	defb 11100000b		
E4A2	00	805			
E4A3	00	806	defb 00000000b	i code 0C	graphics
E4A4	00	807	defb 00000000b		
E4A5	00	808	defb 00000000b		
E4A6	00	809	defb 00000000b		
E4A7	0F	810	defb 11111111b		
E4A8	0F	811	defb 11111111b		
E4A9	0F	812	defb 11111111b		
E4AA	0F	813	defb 11111111b		
E4AB	0F	814			
E4AC	0F	815	defb 00011111b	i code 0D	graphics
E4AD	0F	816	defb 00011111b		
E4AE	0F	817	defb 00011111b		
E4AF	0F	818	defb 00011111b		
E4B0	0F	819	defb 11111111b		
E4B1	0F	820	defb 11111111b		
E4B2	0F	821	defb 11111111b		
E4B3	0F	822	defb 11111111b		
E4B4	0F	823			
E4B5	0F	824	defb 11110000b	icode 0E	graphics
E4B6	0F	825	defb 11110000b		
E4B7	0F	826	defb 11110000b		
E4B8	0F	827	defb 11110000b		
E4B9	0F	828	defb 11110000b		
E4BA	0F	829	defb 11110000b		
E4BB	0F	830	defb 11110000b		
E4BC	0F	831	defb 11110000b		
E4BD	0F	832			
E4BE	0F	833	defb 11111111b	icode 0F	graphics
E4BF	0F	834	defb 11111111b		
E4C0	0F	835	defb 11111111b		
E4C1	0F	836	defb 11111111b		
E4C2	0F	837	defb 11111111b		
E4C3	0F	838	defb 11111111b		
E4C4	0F	839	defb 11111111b		
E4C5	0F	840	defb 11111111b		
E4C6	0F	841	defb 11111111b		
E4C7	0F	842			
E4C8	0F	843			

end

A	Reserved	ATTBYT	E99A	ATTMSK	E99C	ATTRSP	3C8D	S	Reserved
C	Reserved	CALCAT	E99A	CALCAL	E922	CALCPO	E9AP	CHRSET	3C8D
CHTBL	E999	CDNUPM	E99D	CURPCS	E995	D	Reserved	OPADPS	E997
E	Reserved	ERRRET	E998	GC4642	E99A	GETSCH	E91F	GDORRE	E906
GRPHST	E93A	GRPST	E43A	GRTRC	E492	GTCH1	E92F	GTCH2	E934
GTCH23	E94A	GTCH3	E94B	GTCH31	E94E	GTCH32	E949	GTCH4	E94E
GTCH5	E989	GTCH6	E998	GTINH0	E998	H	Reserved	L	Reserved
LDAT7R	E924	LDPQSN	E9DF	LINLID	E994	LNBUS	E98C	M	Reserved
MASK8	E899	MASK9P	3C8E	PSW	Reserved	PSPLAG	3C91	SCS2	0318
SETSPR	E90E	SP	Reserved	STPDSW	E9D7	UDG	3C78	UPDATY	E9E7
UPDAT1	E404	UPDAT2	E412	UPDPCB	E9AB	VIDMOD	3CC2	WRCHMT	E903
WRCH01	E8A3	WRCH11	E98B	WRCH12	E8C0	WRCH13	E8C4	WRCH14	E8DA
WRCH15	E8D8	WRCH2	E8DB	WRCH3	E8FA	WRCH5	E8EE	WRCH7	E8FA
WRIT8	E890								

No errors detected

APPENDIX D

TS2068 PCB Assembly and Schematic Diagram

The following Appendix contains the PCB Assembly Drawing, the PCB Parts List, and PCB Schematic Diagram (a "fold-out" page located just inside the back cover). The Table below contains some corrections to the Schematic Diagram.

TS2068 PCB Schematic Diagram Corrections

Page 34 of the Technical Manual shows pin 9 of the joystick ports grounded as it should be. The traces were left off the TS2068 PCB.

VR1: U3-33 goes to VR1/Q5

Q4: Connect base to R55/R54

Solder dots on horizontal lines below keyboard:

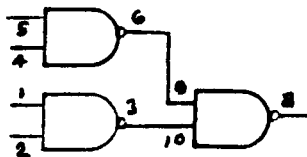
U12-4 to U3-65 (WR)

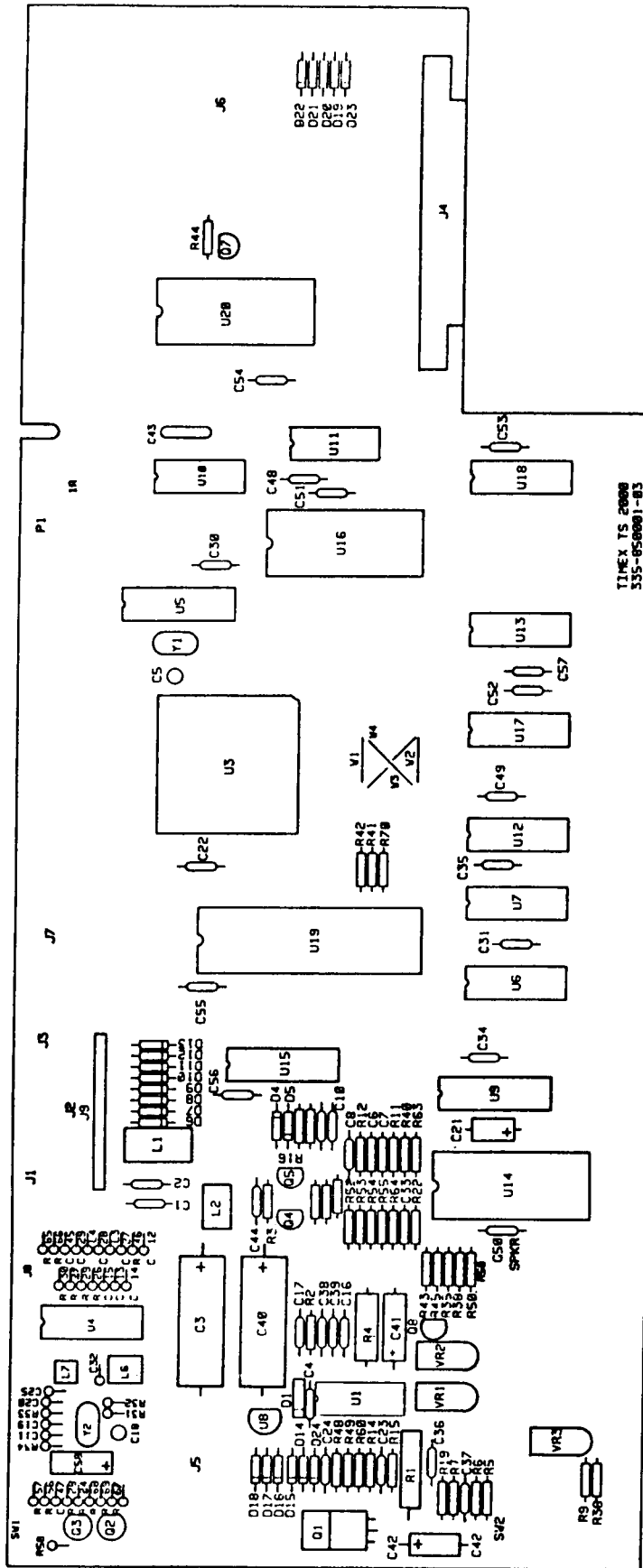
U12-5 to U3-66 (MREQ)

U5: U5-2 to U3-38 (A7R not A7RB)

P1: P1-4B +15V (not -15V)

U21:





TS2068 PC BOARD COMPONENT LAYOUT

APPENDIX D

TS2068 PARTS LIST

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
P.C.B. (Fabrication and Artwork)			REV 3A
CAP. 0.1 uf, Ceramic, Axial TEMP Z5U	C2,7,9,16,24,30 31,34,35,37,39,43 44,48,49,50,51,52 53,54,55,56,57	23	-20 +80% or GMV
CAP. 0.01 uf, Ceramic, Axial	C11,12,14,33,61 62,68,69	8	-20 +80% or GMV TEMP Z5U
CAP. 0.001 uf, Ceramic, Axial	C8,45,46,47	4	-20 +80% or GMV TEMP Z5U
CAP. 0.047 uf, Ceramic, Axial	C10,15,74,75	4	-20 +80% or GMV TEMP Z5U
CAP. 20pf Ceramic Axial	C23	1	-20 +80% or GMV TEMP Z5U
CAP. 39pf Ceramic Axial	C20	1	NPO
CAP. 43pf Ceramic Axial	C19	1	NPO
CAP. 56pf Ceramic Axial	C25	1	NPO
CAP. 75pf Ceramic Axial	C32	1	NPO
CAP.120pf Ceramic Disc	C59,63,64,65,72 73	6	-20 +80% or GMV TEMP Z5U
CAP.470uf, 25V AL Electro- lytic Axial	C3	1	
CAP. 1 uf, 16V MIN AL Electro- lytic Axial	C21	1	
CAP. 47 uf, 16V MIN AL Elec- trolytic Axial or Radial	C41	1	
CAP. 1000 uf, 12V MIN AL Electrolytic Axial	C40	1	LOW ESR
CAP. 1000 pf, 50V MIN FILM MYLAR	C36	1	+/- 20%
CAP. 100 uf, 10V MIN AL Elec- trolytic Axial	C58,67	2	
CAP. 6-50 pf, TRIMMER	C5,18	2	NPO
CAP. 0.47 uf Ceramic Axial	C60	1	-20 +80% or GMV TEMP Z5U
CAP. 33 uf TANTALUM	C71	1	+/- 20%

APPENDIX D
TS2068 PARTS LIST
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
CAP. 68 pf Ceramic Axial	C70	1	-20 +80% or GMV TEMP Z5U
CAP. 24 pf Ceramic Axial	C29,27	2	-20 +80% or GMV TEMP Z5U
CAP. 47 pf Ceramic Axial	C28	1	-20 +80% or GMV TEMP Z5U
RES. 300 OHM, 1/4W, +/-5%, CF	R23	1	
RES. 200 OHM, 1/4W, +/-5%, CF	R19,50,54,55	4	
RES. 100 OHM, 1/4W, +/-5%, CF	R58	1	
RES. 240 OHM, 1/4W, +/-5%, CF	R24,28,56,57	4	
RES. 68 OHM, 1/4W, +/-5%, CF	R2	1	
RES. 680 OHM, 1/4W, +/-5%, CF	R13,68	2	
RES. 390 OHM, 1/4W, +/-5%, CF	R74	1	
RES. 1K OHM, 1/4W, +/-5%, CF	R11,33,34,35,36 38,42,62	8	
RES. 1.5K OHM, 1/4W, +/-5%, CF	R41	1	
RES. 1.8K OHM, 1/4W, +/-5%, CF	R29,30	2	
RES. 620 OHM, 1/4W, +/-5%, CF	R52	1	
RES. 2K OHM, 1/4W, +/-5%, CF	R22	1	
RES. 3K OHM, 1/4W, +/-5%, CF	R32	1	
RES. 2.2K OHM, 1/4W, +/-5%, CF	R61	1	
RES. 110 OHM, 1/4W, +/-5%, CF	R53	1	
RES. 510 OHM, 1/4W, +/-5%, CF	R69	1	
RES. 5.1K OHM, 1/4W, +/-5%, CF	R31	1	
RES. 10K OHM, 1/4W, +/-5%, CF	R16,40,60,70	4	
RES. 13K OHM, 1/4W, +/-5%, CF	R26,27	2	
RES. 20K OHM, 1/4W, +/-5%, CF	R44,45	2	
RES. 62K OHM, 1/4W, +/-5%, CF	R9,73	2	
RES. 100K OHM, 1/4W, +/-5%, CF	R15,49	2	
RES. 220K OHM, 1/4W, +/-5%, CF	R43	1	
RES. 75 OHM, 1/4W, +/-5%, CF	R46,67	2	
RES. 1.10K OHM 1/4W, +/-1%, MF	R6	1	
RES. 3.32K OHM 1/4W, +/-1%, MF	R5	1	
RES. 10K OHM, VARIABLE, LINEAR	VR1,2,3	3	
RES. 330 OHM, 0.5W, +/-5%, CF	R4	1	
RES. 56 OHM, 1/4W, +/-5%, CF	R65,71	2	
RES. 0.110 OHM, 3W, +/-5%, Wire Wound	R1	1	
RES. 20 OHM, 1/4W, +/-5%, CF	R63	1	
RES. 82 OHM, 1/4W, +/-5%, CF	R64	1	
RES. 22 OHM, 1/4W, +/-5%, CF	R66	1	
RES. 680K OHM, 1/4W, +/-5%, CF	R14	1	
RES. 47K OHM, 1/4W, +/-5%, CF	R48	1	
RES. 390K OHM, 1/4W, +/-5%, CF	R72	1	
RES. 6.8K OHM, 1/4W, +/-5%, CF	R12	1	

APPENDIX D
TS2068 PARTS LIST
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
DIODE 1N4148	CR4,5,6,7,8,9,10 11,12,13,14,15,16 17,18,19,20,21,22 23,24,25,26,27,28	25	
DIODE, Schottky 1N5821 or equivalent	CR1	1	
IC, UA 78S40 NPC, Switching Regulator	U1	1	
IC, SCLD	U3	1	
IC, LM1889N, Video Modulator	U4	1	
IC, 74LS244N	U5	1	
IC, TMS4416-15 (150NS) MOS Dynamic RAM	U6,7	2	
IC, UA 78L12 Regulator	U8	1	
IC, 74LS245	U9,15	2	
IC, 74LS157N	U10,11	2	
IC, TMS4416-20 (200NS) MOS Dynamic RAM	U12,13,17,18	4	
IC, AY-3-8912, Sound Gen. and I/O Port	U14	1	
IC, 23128 Mask ROM (16K X 8)	U16	1	
IC, CPU Z80A	U19	1	
IC, 2364 Mask ROM (8K X 8)	U20	1	
IC, 74LS00	U21	1	
TRAN. PNP D43C1	Q1	1	
TRAN. PNP 2N2907	Q3	1	
TRAN. PNP 2N3904	Q7,8	2	
TRAN. PNP 2N2222	Q5,4,2	3	

APPENDIX D

TS2068 PARTS LIST
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
EMI Filter(Bifiler) 2.2mh	L1	1	
Inductor 230 uh	L2	1	
Inductor .33uh Axial	L3,4	2	
Inductor .12uh	L6,7	2	
Crystal Oscillator 14.112 MHz	Y1	1	
Crystal Oscillator 3.579545 MHz	Y2	1	
Switch SPDT, Rocker	SW2	1	
Switch Channel Select, SPDT Slide	SW1	1	
Video Jack Insulation Pad		1	Under J7
Jack, Right Angle RCA Video Jack	J7	1	Monitor
Jack, Mini Phone, EAR & MIC	J2,3	2	Tape
Jack, COAX, DC Power, 2 1/2 MM Pin	J1	1	
Jack, Phono	J8	1	Assembled to Shield, R.F.
Connector, Cartridge 2 X 18 Pin 0.1" Space	J4	1	Key between Contact 4&6
Connector, Flex Cable 14 Pin	J9	1	Keyboard
Connector, Joystick 9-Pin Male (D Type)	J5,6	2	Joysticks
Shield, R.F. Button		1	
Shield, R.F. Top		1	
Heat Sink	HS1	1	
Heat Sink Insulation Pad			

APPENDIX D
 TS2068 PARTS LIST
 (continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
Socket, IC, 28 Pin		2	
Socket, IC, 40 Pin		1	
Speaker, 45 OHM, Mylar Cone		1	
Jumper Wire	W1, 2, 50	3	
Ferrite Bead	L5,8	2	
PC Board Assembly, Daughter		1	

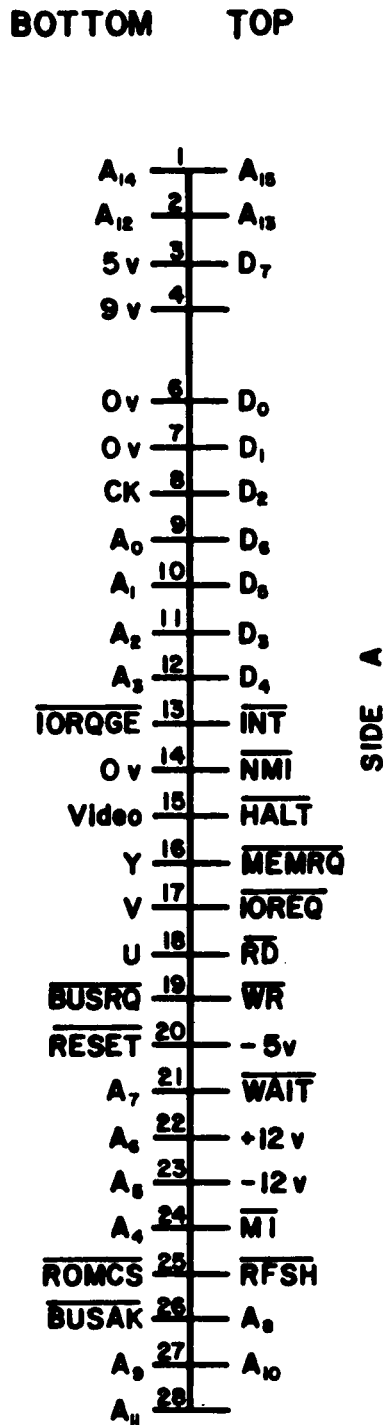
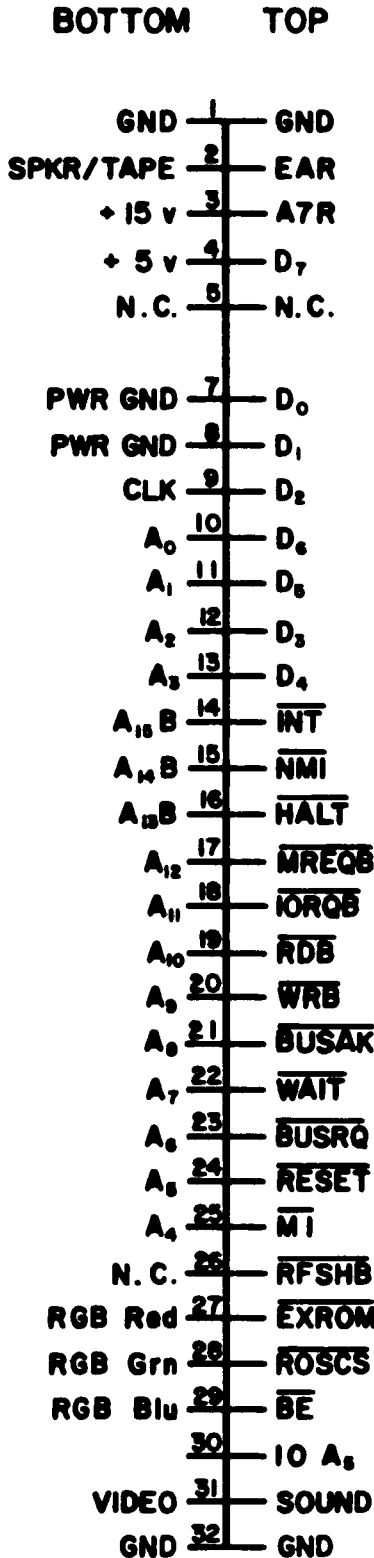
APPENDIX E

Expansion Buss Comparison of
TS2068, Sinclair Spectrum and ZX81

TS 2068

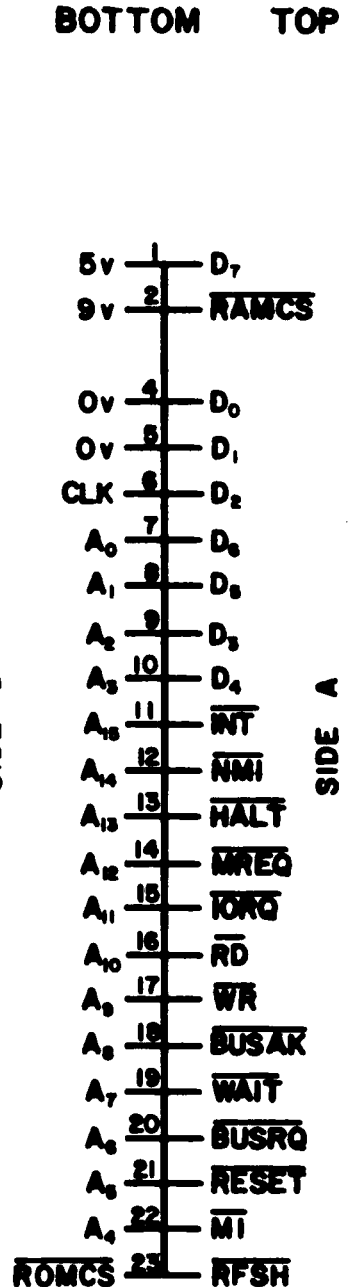
SPECTRUM

ZX-81



SIDE A

SIDE B



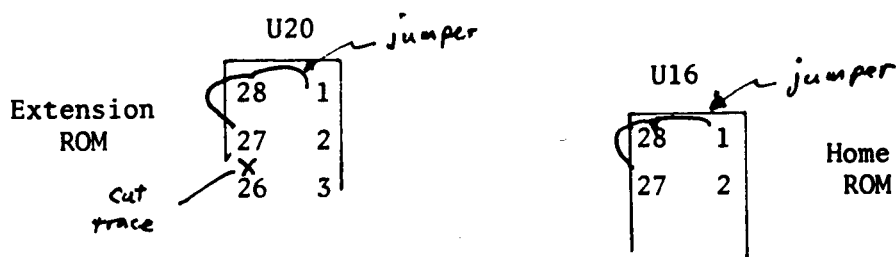
SIDE A

APPENDIX F

August 1985
Bob Orrfelt

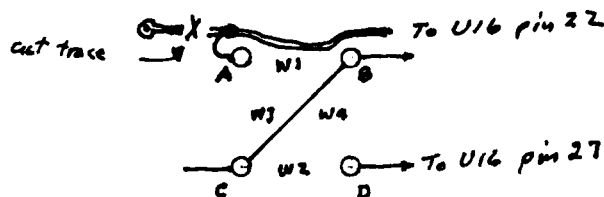
TS2068 MODIFICATIONS FOR EPROMS

There are a number of errors in the TS2068 Home ROM and the Extension ROM. The errors can be corrected by using EPROMs. The following modifications are necessary:



Non-component side of the pcb,

0. Remove ROMs.
1. Cut the trace between U20-26 and U20-27
2. Jumper pins 1 to 28 to 27 on each socket.



Component side of pcb.

3. Remove the two zero ohm resistors W1 and W2.
4. Cut the trace just above and to the left of hole A.
5. Add a jumper from hole A to the trace. This connects \overline{MREQ} to U16 pin 22.
6. Add a jumper from hole C to hole B. This connects \overline{ROMCS} to U16 pin 20.
7. Use a 27128 (16K) EPROM for U16.
8. Use a 2764 (8K) EPROM for U20.

October 1985
Bob Orrfelt

Proposed TS2068 Home ROM Corrections and Improvements

NMI fix.
006D 2801 JR Z,0070H

DELETE delay timing.
0351 010100 LD BC,0001H
0354 0B DEC BC
0355 79 LD A,C
0356 B0 OR B
0357 20FB JR NZ,0354H
0359 F1 POP AF
035A 18D2 JR 032EH

Optional turn on message.
(Last character add 80H)
1118 Property of Bob
1128 Orrfelt.....
1138
1148 ..

INT -65536 etc. errors.
33F1 F5 PUSH AF
33F2 3C INC A
33F3 B3 OR E
33F4 B2 OR D
33F5 C2E435 JP NZ,35E4H
33F8 C3EF35 JP 35EFH

35E2 181A JR 35FEH
35E4 F1 POP AF
35E5 77 LD (HL),A
35E6 23 INC HL
35E7 73 LD (HL),E
35E8 23 INC HL
35E9 72 LD (HL),D
35EA 2B DEC HL
35EB 2B DEC HL
35EC 2B DEC HL
35ED D1 POP DE
35EE C9 RET

35EF F1 POP AF
35F0 2B DEC HL
35F1 3691 LD (HL),91H
35F3 23 INC HL
35F4 3680 LD (HL),80H
35F6 3C INC A
35F7 18ED JR 35E6H

35F9 FFFFFFFF blanks
35FD FF

USR chunk selection.
389F E660 AND 60H
38A1 281B JR Z,38BEH
38A3 D640 SUB A,40H
38A5 FAB738 JP M,38B7H

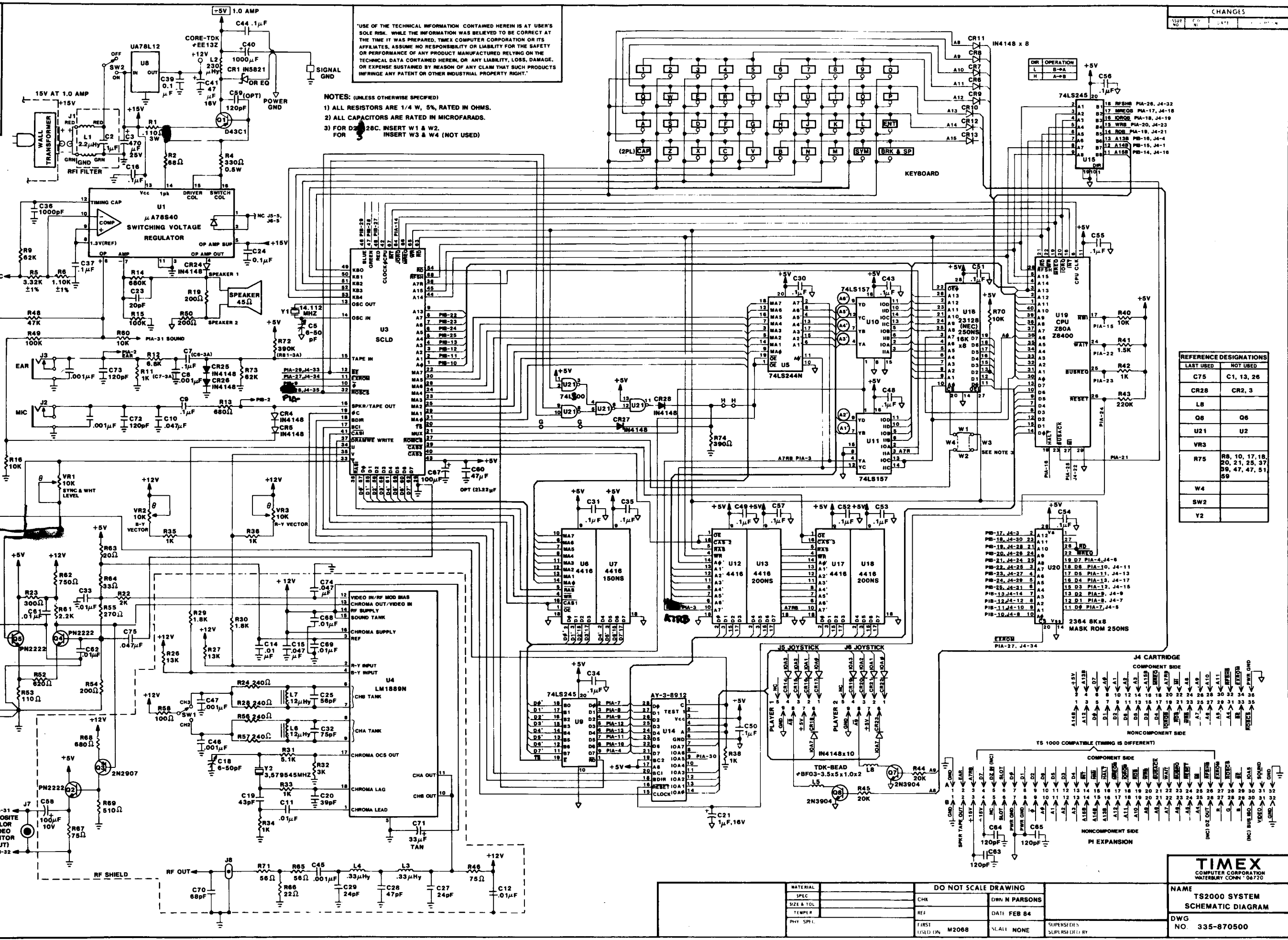
Fix for Oliger EPROM programmer.
(see May 85 Syncware, page 14)
002B 84 DB 84H
002C 87 DB 87H
002D 8B DB 8BH
002E 8D DB 8DH
002F 92 DB 92H

More for EPROM programmer.
37B8 D9 EXX
37B9 212B00 LD HL,002BH
37BC 85 ADD A,L
37BD 6F LD L,A
37BE 6E LD L,(HL)
37BF 2636 LD H,36H

37C1 D9 EXX
37C2 AF XOR A
37C3 C9 RET
37C4 00 NOP

NOTES

RESTRICTED INFORMATION



USE OF THE TECHNICAL INFORMATION CONTAINED HEREIN IS AT USER'S SOLE RISK. WHILE THE INFORMATION WAS BELIEVED TO BE CORRECT AT THE TIME IT WAS PREPARED, TIMEX COMPUTER CORPORATION OR ITS AFFILIATES, ASSUME NO RESPONSIBILITY OR LIABILITY FOR THE SAFETY OR PERFORMANCE OF ANY PRODUCT MANUFACTURED RELYING ON THE TECHNICAL DATA CONTAINED HEREIN, OR ANY LIABILITY, LOSS, DAMAGE, OR EXPENSE SUSTAINED BY REASON OF ANY CLAIM THAT SUCH PRODUCTS INFRINGE ANY PATENT OR OTHER INDUSTRIAL PROPERTY RIGHT.

- NOTES: (UNLESS OTHERWISE SPECIFIED)
- 1) ALL RESISTORS ARE 1/4 W, 5%, RATED IN OHMS.
 - 2) ALL CAPACITORS ARE RATED IN MICROFARADS.
 - 3) FOR D28, INSERT W1 & W2. FOR D29, INSERT W3 & W4 (NOT USED)

CHANGES

NO.	DATE	DESCRIPTION

REFERENCE DESIGNATIONS

LAST USED	NOT USED
C75	C1, 13, 26
CR28	CR2, 3
L8	O6
O8	O6
U21	U2
VR3	
R75	R8, 10, 17, 18, 20, 21, 25, 37, 59, 47, 47, 51, 59
W4	
SW2	
Y2	

TIMEX
COMPUTER CORPORATION
WATERBURY CONN. 06720

NAME: TS2000 SYSTEM SCHEMATIC DIAGRAM
DWG NO: 335-870500

DO NOT SCALE DRAWING

MATERIAL	CHK	DRN	DATE

DATE: FEB 84
SCALE: NONE
SUSPENSES: SUPPLIES BY

BRUNING 44131 22870 3

